

SERIES DL6550 PROGRAMMABLE DIGITAL DELAY LINES

T-47-17

- TTL Compatible • Digitally Programmable in 8 Delay Steps (3 Bit)
- 10 TTL Fan-out Capability • 16 Pins Package

Specifications:

- Logic 1 Input Voltage : 2v Min; 5.5v Max
- Logic 1 Input Current : 100 μ A @2.4v, 2mA @5.5v
- Logic 0 Input Voltage : 0.8v Max
- Logic 0 Input Current : -4mA Max
- Logic 1 Output Voltage : 2.4v Min
- Logic 0 Output Voltage : 0.4v Max
- Inherent Delay : 7NS $\pm$ 1NS
- Operating Temperature : 0°C to 70°C
- Temperature Coefficient : 500PPM/°C



Testing Conditions:

- Input Pulse-Width : $\geq$ 50% of Delay
- Input Pulse Rise Time : 3NS
- Vcc Supply Voltage : 5.0VDC
- Vcc Supply Current : 40mA Typ.

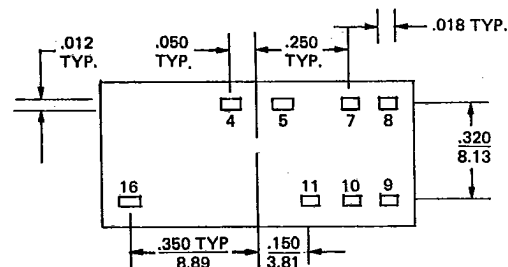
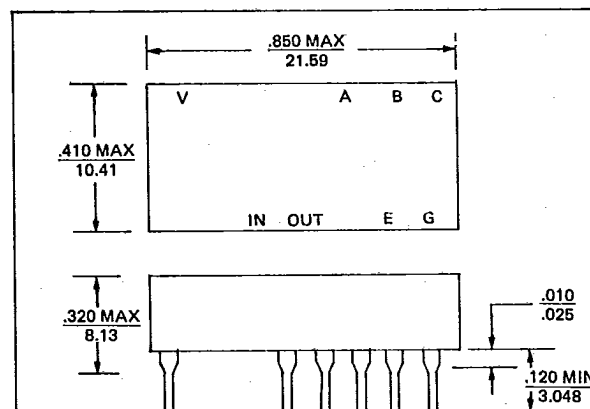
Truth Table

ADDRESS BIT NO. Eo	C	B	A	Total Delay
L	L	L	L	1D
L	L	L	H	1D+T
L	L	H	L	1D+2T
L	L	H	H	1D+3T
L	H	L	L	1D+4T
L	H	L	H	1D+5T
L	H	H	L	1D+6T
L	H	H	H	1D+7T

When Eo is H, no output

1D = Inherent Delay = 7NS $\pm$ 1NST = Multiplier of Incremental Delay
(Delay change per step)

Part Number	Incremental Delay per step (NS)	Total Delay (NS)
DL6551	1 $\pm$ 0.3	14 $\pm$ 1
DL6552	2 $\pm$ 0.4	21 $\pm$ 1
DL6553	3 $\pm$ 0.5	28 $\pm$ 1
DL6554	5 $\pm$ 0.6	42 $\pm$ 1
DL6555	10 $\pm$ 1.0	77 $\pm$ 2
DL6556	15 $\pm$ 1.3	112 $\pm$ 4
DL6557	20 $\pm$ 1.5	140 $\pm$ 5
DL6578	40 $\pm$ 2.0	280 $\pm$ 6
DL6579	50 $\pm$ 2.5	350 $\pm$ 7



INCHES .XXX $\pm$.010
MILLIMETERS .XX $\pm$.25

