

## DM100416 256 × 4-Bit ECL PROM

### General Description

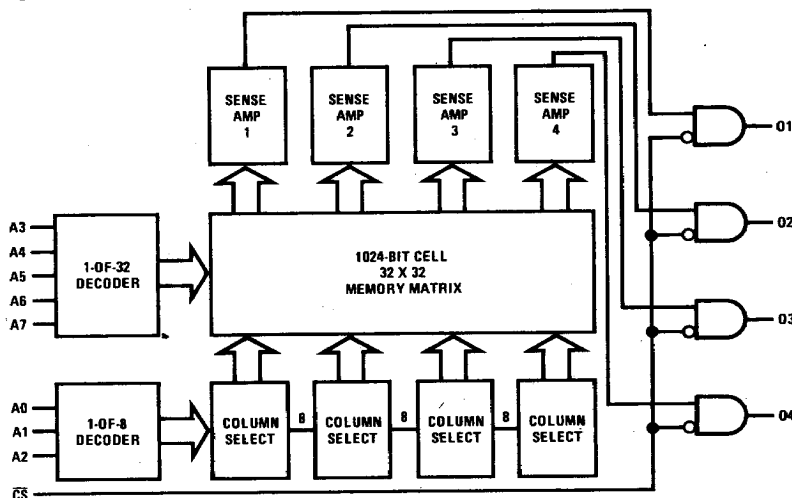
The DM100416 is a fully decoded high speed 1024-bit field Programmable Read Only Memory, organized 256 words by 4 bits. The DM100416 is voltage and temperature compensated and compatible with the 100k family. The device is enabled when  $\overline{CS}$  is LOW. Programmed bits will furnish HIGH levels at corresponding outputs. When the device is disabled ( $\overline{CS}$  is HIGH) all outputs are LOW.

See the last page of this section for detailed programming information.

### Features

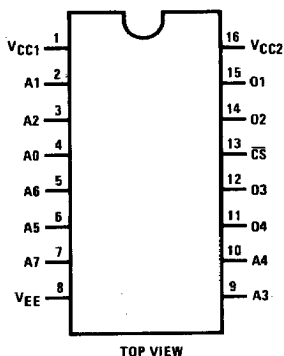
- Fast cycle time—12 ns typ
- Organization—256 words × 4 bits
- 100k logic
- Chip select input provides easy memory expansion
- Open emitter outputs for memory expansion
- Standard 16-pin dual-in-line package
- Full address decoding on chip

### Block Diagram



### Connection Diagram

#### Dual-In-Line Package



**Pin Names**  
 $\overline{CS}$  Chip Select Input  
 A0 to A7 Address Inputs  
 01 to 04 Data Outputs

**Note:**  
 $V_{CC1}$  (Pin 1) is connected to the Programmer (+6.0V) during programming only.

$V_{CC1} = \text{GND (Read only)} = \text{Pin 1}$   
 $V_{CC2} = \text{GND} = \text{Pin 16}$   
 $V_{EE} = \text{Pin 8}$

### Logic Symbol

