

## 10415 1024 x 1-Bit Static Random Access Memory

### General Description

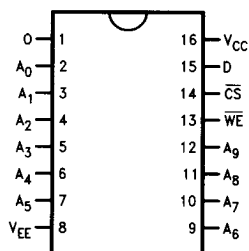
The 10415 is a 1024-bit read/write Random Access Memory (RAM), organized as 1024 words by one bit per word and designed for high-speed scratchpad, control and buffer storage applications. The device includes full on-chip address decoding, separate Data input and non-inverting Data output lines, as well as an active-LOW Chip Select line.

### Features

- Address access time—10 ns max
- Chip select access time—5 ns max
- Open-emitter output for easy memory expansion
- Power dissipation—0.92 mW/Bit Typ
- Power dissipation decreases with increasing temperature
- Polyimide die coat for alpha immunity

### Connection Diagrams

16-Pin Ceramic Dual-In-Line Package



TL/D/9641-2

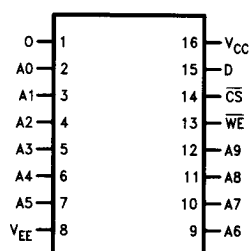
Top View

Order Number 10415DC10  
See NS Package Number J16A\*

\*For most current package information, contact product marketing.

Optional Processing QR = Burn-In

16-Pin Ceramic Flatpack



TL/D/9641-11

Top View

Order Number 10415FC10  
See NS Package Number W16A\*

\*For most current package information, contact product marketing.

Optional Processing QR = Burn-In