

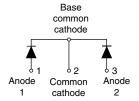
Vishay High Power Products

COMPLIANT

Schottky Rectifier New Generation 3 D-61 Package, 2 x 40 A

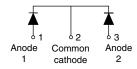
VS-85CNQ015APbF





VS-85CNQ015ASMPbF



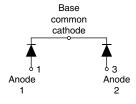


D-61-8-SM

VS-85CNQ015ASLPbF







 PRODUCT SUMMARY

 I_{F(AV)}
 2 x 40 A

 V_R
 15 V

 I_{RM}
 1000 mA at 100 °C

FEATURES

- 125 °C T_J operation (V_R < 5 V)
- Center tap module
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- New fully transfer-mold low profile, small footprint, high current package
- Through-hole versions are currently available for use in lead (Pb)-free applications ("PbF" suffix)
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	80	A		
V _{RRM}		15	V		
I _{FSM}	$t_p = 5 \mu s sine$	5200	A		
V _F	40 Apk, T _J = 75 °C (per leg)	0.32	V		
T _J	Range	- 55 to 125	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-85CNQ015APbF	UNITS	
Maximum DC reverse voltage	V_{R}	15	V	
Maximum working peak reverse voltage	V_{RWM}	25	V	

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^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

VS-85CNQ015A PbF Series

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	ARAMETER SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current See fig. 5		50 % duty cycle at T _C = 78 °C, rectangular waveform		80	
Maximum peak one cycle non-repetitive surge current per leg	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	5200	Α
See fig. 7		10 ms sine or 6 ms rect. pulse		850	
Non-repetitive avalanche energy per leg	E _{AS}	$T_{J} = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 4.5 \text{mH}$		9	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 3 x V _R typical		2	Α

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg		40 A	T _{.1} = 25 °C	0.36	
	V _{FM} ⁽¹⁾	80 A	- 1J=25 0	0.45	
See fig. 1	VFM (1)	40 A	T _{.1} = 75 °C	0.32	V
	8	80 A	- IJ=75 C	0.42	
Maximum reverse leakage current per leg		$I_{RM}^{(1)}$ $T_{J} = 100 ^{\circ}C$ $V_{R} = 12 V$ $V_{R} = 5 V$	V _R = 12 V	890	- mA
	I (1)		V _R = 5 V	540	
See fig. 2	'RM \"	T _J = 25 °C	V _B = Rated V _B	20	
	-	T _J = 100 °C	V _R = nateu V _R	1000	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		3600	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		5.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 125	°C
Maximum thermal resistance,	per leg	D	DC operation (see fig. 4)	0.85	
junction to case	per package	R _{thJC}	DC operation	0.42	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased Device flatness < 5 mils	0.30	
A managina ata maialat				7.8	g
Approximate weight				0.28	oz.
Mounting torque	minimum			40 (35)	kgf · cm
Mounting torque	maximum			58 (50)	(lbf \cdot in)
			Case style D-61	85CNC	Q015A
Marking device			Case style D-61-8-SM	85CNQ015ASM	
			Case style D-61-8-SL	85CNQ0	15ASL





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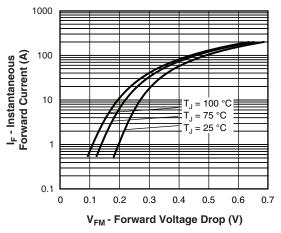


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

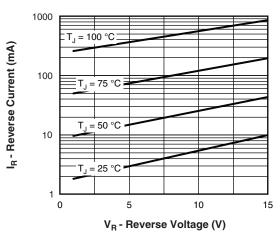


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

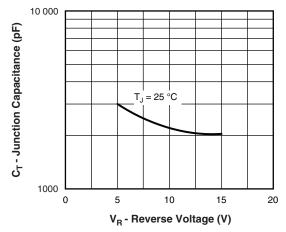


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

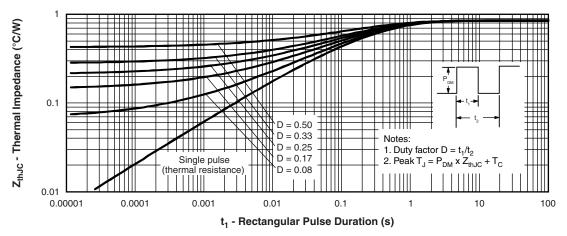


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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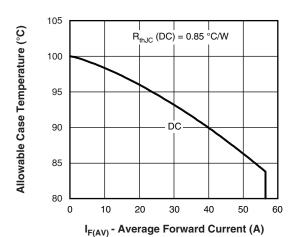


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

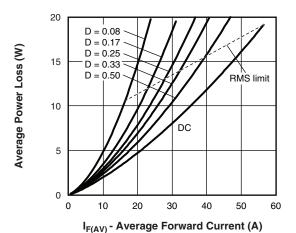


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

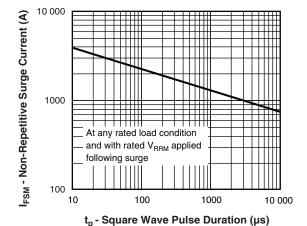


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

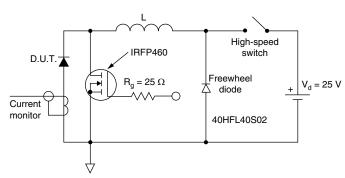


Fig. 8 - Unclamped Inductive Test Circuit

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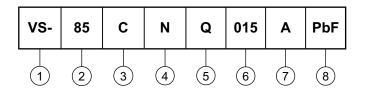


VS-85CNQ015A PbF Series

Schottky Rectifier Vishay High Power Products New Generation 3 D-61 Package, 2 x 40 A

ORDERING INFORMATION TABLE

Device code



1 - HPP product suffix

2 - Current rating (80 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

N = D-61

5 - Schottky "Q" series

6 - Voltage ratings (015 = 15 V)

7 - Package style:

• A = D-61-8

• ASM = D-61-8-SM

• ASL = D-61-8-SL

8 - • None = Standard production

• PbF = Lead (Pb)-free

Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95354			
Part marking information	www.vishay.com/doc?95356			

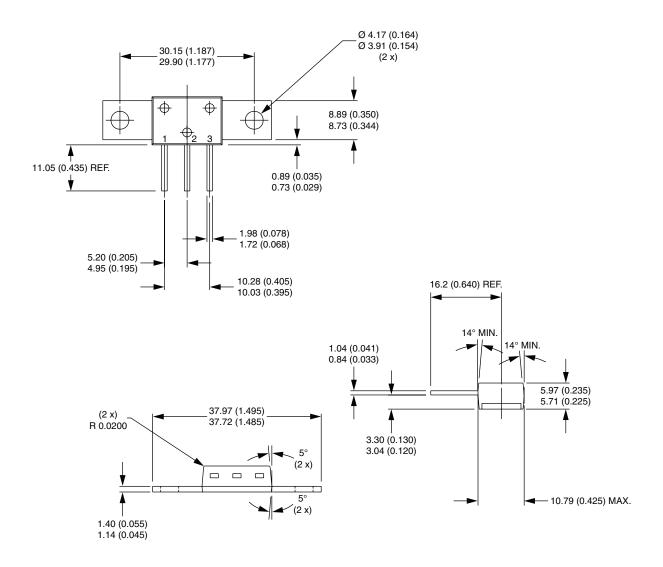
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D-61-8, D-61-8-SM, D-61-8-SL

DIMENSIONS FOR D-61-8 in millimeters (inches)



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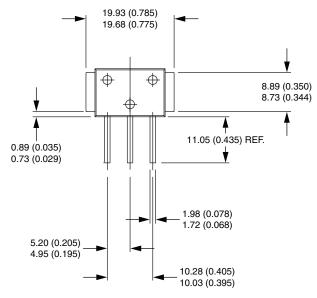
D-61-8, D-61-8-SM, D-61-8-SL

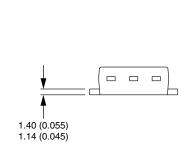


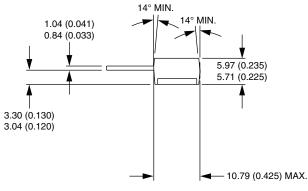
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DIMENSIONS FOR D-61-8-SM in millimeters (inches)



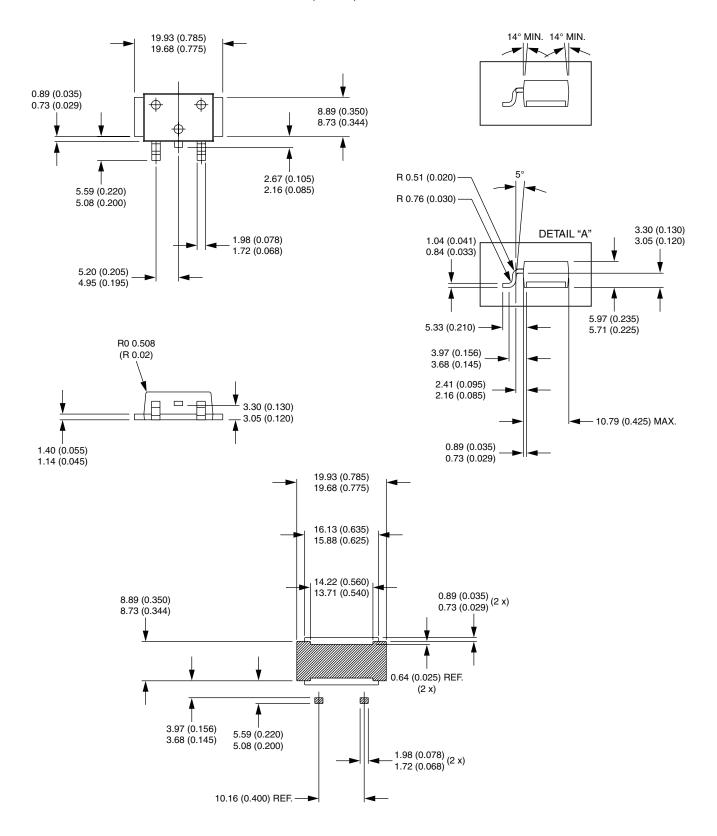






D-61-8, D-61-8-SM, D-61-8-SL Vishay High Power Products

DIMENSIONS FOR D 61-8-SL in millimeters (inches)







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