

DM9003C Triple 3-Input NAND Gates

General Description

The DM9003C device is designed to be used in existing systems as replacements for Fairchild 9000-type circuits. The DM9003C circuit offers several significant advantages over 9003 type circuits, some of which are:

- Input clamp diodes
- Output short-circuit current specified to guarantee the high-level impedance.
- Power-dissipation of DM9003C circuits is in most cases lower than that for the equivalent 9003 type.

The DM9003C circuit is characterized for operation over the industrial temperature range of 0°C to 75°C.

For the new designs, the 54/74 families of TTL circuits offer the industry's broadest choice of high-performance digital circuits. Series 54/74 pin-for-pin equivalent is available for the following SSI type:

DM9000C Series
DM9003C

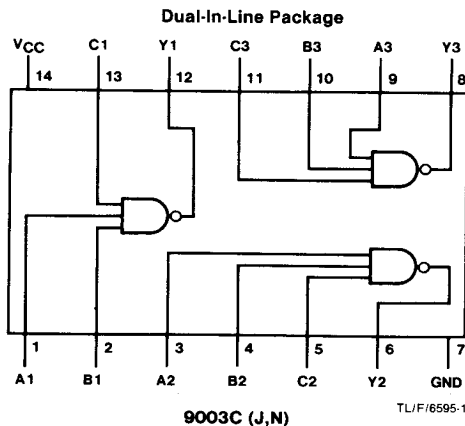
Equivalent Series 74
DM7410

Absolute Maximum Ratings (Note 1)

Supply Voltage	7V
Input Voltage	5.5V
Storage Temperature Range	~ 65°C to 150°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device can not be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Connection Diagrams



Recommended Operating Conditions

Symbol	Parameter		DM9003C			Units
			Min	Nom	Max	
V _{CC}	Supply Voltage		4.75	5	5.25	V
V _{IH}	High Level Input Voltage	0°C	1.9			V
		25°C	1.8			
		75°C	1.6			
V _{IL}	Low Level Input Voltage				0.85	V
I _{OH}	High Level Output Current				- 1.2	mA
I _{OL}	Low Level Output Current				50	mA
T _A	Free Air Operating Temperature		0		75	°C

Electrical Characteristics over recommended operating free air temperature (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 1)	Max	Units
V _I	Input Clamp Voltage	V _{CC} = Min, I _I = - 12 mA			- 1.5	V
V _{OH}	High Level Output Voltage	V _{CC} = Min, I _{OH} = Max V _{IL} = Max	2.4			V
V _{OL}	Low Level Output Voltage	V _{CC} = Max, I _{OL} = 16 mA V _{IH} = Min			0.45	V
		I _{OL} = 14.1 mA V _{CC} = Min			0.45	
I _{IH}	High Level Input Current	V _{CC} = Max, V _I = 4.5V Other Input at GND			60	μA
I _{IL}	Low Level Input Current	V _I = 4.5V Other Inputs at 5.25V	V _{CC} = Max		- 1.6	mA
			V _{CC} = Min		- 1.41	
I _{OS}	Short Circuit Output Current	V _{CC} = Max (Note 2)	- 18		- 55	mA
I _{COH}	Supply Current With Outputs High	V _{CC} = 5V			1.7	mA
I _{CCL}	Supply Current With Outputs Low	V _{CC} = 5V			6.1	mA

Switching Characteristics at V_{CC} = 5V and T_A = 25°C (See Section 1 for Test Waveforms and Output Load)

Parameter	Conditions	C _L = 15 pF R _L = 400Ω			Units
		Min	Typ	Max	
t _{PLH} Propagation Delay Time Low to High Level Output		3		13	ns
t _{PHL} Propagation Delay Time High to Low Level Output		3		15	ns

Note 1: All typicals are at V_{CC} = 5V, T_A = 25°C.

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.