LT1076-5 5V Step-Down Switching Regulator

## feATURES

- Fixed 5V Output
- 2A On-Board Switch
- 100kHz Switching Frequency
- $2 \%$ Output Voltage Tolerance Over Temperature
- Greatly Improved Dynamic Behavior
- Available in Low Cost 5- and 7-Lead Packages
- Only 9.5mA Quiescent Current
- Operates Up to 60V Input


## APPLICATIONS

- 5V Output Buck Converter
- Tapped Inductor Buck Converter with 4A Output at 5 V
- Positive-to-Negative Converter


## DESCRIPTIOn

The LT1076-5 is a 2 A fixed 5 V output monolithic bipolar switching regulator which requires only a few external parts for normal operation. The power switch, all oscillator and control circuitry, all current limit components, and an output monitor are included on the chip. The topology is a classic positive "buck" configuration but several design innovations allow this device to be used as a positive-to-negative converter, a negative boost converter, and as a flyback converter. The switch output is specified to swing 40V below ground, allowing the LT1076-5 to drive a tapped inductor in the buck mode with output currents up to 4A.
The LT1076-5 uses a true analog multiplier in the feedback loop. This makes the device respond nearly instantaneously to input voltage fluctuations and makes loop gain independent of input voltage. As a result, dynamic behavior of the regulator is significantly improved over previous designs.
On-chip pulse by pulse current limiting makes the LT1076-5 nearly bust-proof for output overloads or shorts. The input voltage range as a buck converter is 8 V to 60 V , but a selfboot feature allows input voltages as low as 5 V in the inverting and boost configurations.
The LT1076-5 is available in alow cost5-and 7-lead T0-220 packages with frequency pre-set at 100 kHz and current limit at 2.6A. See Application Note 44 for design details.
$\mathbf{\boxed { \top }}$, LTC and LT are registered trademarks of Linear Technology Corporation.

TYPICAL APPLICATION
Basic Positive Buck Converter


## absolute maximum ratings

(Note 1)
Input Voltage
LT1076-5 45 V
LT1076HV-5 ...................................................... 64V
Switch Voltage with Respect to Input Voltage LT1076-5. 64V
LT1076HV-5 ..... 75 V
Switch Voltage with Respect to Ground Pin
( $V_{\text {SW }}$ Negative)
LT1076-5 (Note 6) ..... 35 V
LT1076HV-5 (Note 6) ..... 45 V
Sense Pin Voltage

$\qquad$
$-2 \mathrm{~V}, 10 \mathrm{~V}$

Maximum Operating Ambient Temperature Range LT1076C-5, LT1076HVC-5 $\qquad$ $0^{\circ} \mathrm{C}$ to $70^{\circ} \mathrm{C}$ LT1076I-5, LT1076HVI-5 $\qquad$ $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ Maximum Operating Junction Temperature Range LT1076C-5, LT1076HVC-5 $\qquad$ $0^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ LT1076I-5, LT1076HVI-5 ................ $-40^{\circ} \mathrm{C}$ to $125^{\circ} \mathrm{C}$ Maximum Storage Temperature ........... $-65^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$ Lead Temperature (Soldering, 10 sec ) $\qquad$ $300^{\circ} \mathrm{C}$

PACKAGE/ORDER INFORMATION


Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS The • denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C} . \mathrm{V}_{\text {IN }}=25 \mathrm{~V}$, unless otherwise noted.

| PARAMETER | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Switch "On" Voltage (Note 2) | $\begin{aligned} & I_{S W}=0.5 \mathrm{~A} \\ & I_{S W}=2 \mathrm{~A} \end{aligned}$ | $\bullet$ |  |  | $\begin{aligned} & 1.2 \\ & 1.7 \end{aligned}$ | V |
| Switch "Off" Leakage | $\begin{aligned} & \hline V_{\text {IN }}=25 \mathrm{~V}, V_{S W}=0 \\ & V_{\text {IN }}=V_{\text {MAX }}, V_{\text {SW }}=0 \text { (Note 7) } \end{aligned}$ |  |  |  | $\begin{aligned} & 150 \\ & 250 \end{aligned}$ | $\mu \mathrm{A}$ $\mu \mathrm{A}$ |
| Supply Current (Note 3) | $\begin{aligned} & \mathrm{V}_{\text {OUT }}=5.5 \mathrm{~V}, \mathrm{~V}_{\text {IN }} \leq 40 \mathrm{~V} \\ & 40 \mathrm{~V}<\mathrm{V}_{\text {IN }}<60 \mathrm{~V} \\ & \mathrm{~V}_{\text {SHDN }}=0.1 \mathrm{~V} \text { (Device Shutdown) (Note } 9 \text { ) } \end{aligned}$ | $\bullet$ |  | $\begin{aligned} & 8.5 \\ & 9.0 \\ & 140 \end{aligned}$ | $\begin{aligned} & 11 \\ & 12 \\ & 300 \end{aligned}$ | mA mA $\mu \mathrm{A}$ |
| Minimum Supply Voltage | Normal Mode Start-Up Mode (Note 4) | $\bullet$ |  | $\begin{aligned} & 7.3 \\ & 3.5 \end{aligned}$ | $\begin{aligned} & 8.0 \\ & 4.8 \end{aligned}$ | V |
| Switch Current Limit (Note 5) | $\begin{aligned} & \hline \text { LIIM }=\text { Open } \\ & \text { RLIM }=10 \mathrm{k}(\text { Note } 10) \\ & \text { RLIM }^{\text {L }} 7 \mathrm{k} \text { (Note 10) } \end{aligned}$ | $\bullet$ | 2 | $\begin{aligned} & 2.6 \\ & 1.8 \\ & 1.2 \end{aligned}$ | 3.2- | A A A |
| Maximum Duty Cycle |  | $\bullet$ | 85 | 90 |  | \% |
| Switching Frequency | $\begin{aligned} & \mathrm{T}_{\mathrm{J}} \leq 125^{\circ} \mathrm{C} \\ & \mathrm{~V}_{\text {OUT }}=\mathrm{V}_{\text {SENSE }}=0 \mathrm{~V} \text { (Note 5) } \end{aligned}$ | $\bullet$ | $\begin{aligned} & 90 \\ & 85 \end{aligned}$ | $\begin{aligned} & 100 \\ & 20 \end{aligned}$ | $\begin{aligned} & \hline 110 \\ & 120 \end{aligned}$ | kHz kHz kHz |
| Switching Frequency Line Regulation | $8 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq \mathrm{V}_{\text {MAX }}$ (Note 8) | $\bullet$ |  | 0.03 | 0.1 | \%/V |
| Error Amplifier Voltage Gain (Note 8) | $1 \mathrm{~V} \leq \mathrm{V}_{\mathrm{C}} \leq 4 \mathrm{~V}$ |  |  | 2000 |  | V/V |
| Error Amplifier Transconductance (Note 8) |  |  | 3700 | 5000 | 8000 | $\mu \mathrm{mho}$ |

ELECTRICAL CHARACTERISTICS The • denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C} . \mathrm{V}_{\mathbb{I N}}=25 \mathrm{~V}$, unless otherwise noted.

| PARAMETER | CONDITIONS |  | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Error Amplifier Source and Sink Current | $\begin{aligned} & \text { Source }\left(V_{\text {SENSE }}=4.5 \mathrm{~V}\right) \\ & \text { Sink }\left(V_{\text {SENSE }}=5.5 \mathrm{~V}\right) \end{aligned}$ |  | $\begin{aligned} & 100 \\ & 0.7 \end{aligned}$ | $\begin{aligned} & 140 \\ & 1.0 \end{aligned}$ | $\begin{array}{r} 225 \\ 1.6 \end{array}$ | $\mu_{\mathrm{mA}}^{\mu \mathrm{A}}$ |
| Sense Pin Divider Resistance |  |  | 3 | 5 | 8 | $\mathrm{k} \Omega$ |
| Sense Voltage | $\mathrm{V}_{\mathrm{C}}=2 \mathrm{~V}$ | $\bullet$ | 4.85 | 5 | 5.15 | V |
| Output Voltage Tolerance | $\mathrm{V}_{\text {out }}(\text { Nominal })=5 \mathrm{~V}$ <br> All Conditions of Input Voltage, Output Voltage, Temperature and Load Current | $\bullet$ |  | $\begin{aligned} & \pm 0.5 \\ & \pm 1.0 \end{aligned}$ | $\begin{aligned} & \pm 2 \\ & \pm 3 \end{aligned}$ | \% |
| Output Voltage Line Regulation | $8 \mathrm{~V} \leq \mathrm{V}_{\text {IN }} \leq \mathrm{V}_{\text {MAX }}$ (Note 7) | $\bullet$ |  | 0.005 | 0.02 | \%/V |
| V ${ }_{\text {C }}$ Voltage at 0\% Duty Cycle | Over Temperature | $\bullet$ |  | $\begin{array}{r} 1.5 \\ -4.0 \end{array}$ |  | $\begin{array}{r} \mathrm{V} \\ \mathrm{mV} /{ }^{\circ} \mathrm{C} \end{array}$ |
| Multiplier Reference Voltage |  |  |  | 24 |  | V |
| Shutdown Pin Current | $\begin{aligned} & \mathrm{V}_{\text {SHDN }}=5 \mathrm{~V} \\ & \mathrm{~V}_{\text {SHDN }} \leq \mathrm{V}_{\text {THRESHOLD }}(\cong 2.5 \mathrm{~V}) \end{aligned}$ |  | 5 | 10 | $\begin{aligned} & 20 \\ & 50 \end{aligned}$ | $\mu \mathrm{A}$ $\mu \mathrm{A}$ |
| Shutdown Thresholds | $\text { Switch Duty Cycle }=0$ <br> Fully Shut Down |  | $\begin{aligned} & 2.2 \\ & 0.1 \end{aligned}$ | $\begin{aligned} & 2.45 \\ & 0.30 \end{aligned}$ | $\begin{aligned} & 2.7 \\ & 0.5 \end{aligned}$ | V |
| Thermal Resistance Junction to Case |  |  |  |  | 4 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device ay be impaired.
Note 2: To calculate maximum switch "on" voltage at currents between low and high conditions, a linear interpolation may be used.
Note 3: A sense pin voltage ( $V_{\text {SENSE }}$ ) of 5.5 V forces the $\mathrm{V}_{\mathrm{C}}$ pin to its low clamp level and the switch duty cycle to zero. This approximates the zero load condition where duty cycle approaches zero.
Note 4: Total voltage from $V_{\text {IN }}$ pin to ground pin must be $\geq 8 \mathrm{~V}$ after startup for proper regulation. For $\mathrm{T}_{\mathrm{A}}<25^{\circ} \mathrm{C}$, limit $=5 \mathrm{~V}$.
Note 5: Switch frequency is internally scaled down when the sense pin voltage is less than 2.6 V to avoid extremely short switch on times. During
current limit testing, $\mathrm{V}_{\text {SENSE }}$ is adjusted to give a minimum switch on time of $1 \mu \mathrm{~s}$.
Note 6: Switch to input voltage limitation must also be observed.
Note 7: $\mathrm{V}_{\mathrm{MAX}}=40 \mathrm{~V}$ for the LT1076-5 and 60V for the LT1076HV-5.
Note 8: Error amplifier voltage gain and transconductance are specified relative to the internal feedback node. To calculate gain and transconductance from the Sense pin (Output) to the $\mathrm{V}_{\mathrm{C}}$ pin, multiply by 0.44 .

Note 9: Does not include switch leakage.
Note 10: ILIM $\approx \frac{R_{\text {LIM }}-1 k}{5 k}$

## PACKAGE DESCRIPTION

R Package<br>7-Lead Plastic DD Pak<br>(Reference LTC DWG \# 05-08-1462)



Information furnished by Linear Technology Corporation is believed to be accurate and reliable. However, no responsibility is assumed for its use. Linear Technology Corporation makes no representation that the interconnection of its circuits as described herein will not infringe on existing patent rights.

## PACKAGE DESCRIPTION

## T Package

5-Lead Plastic TO-220 (Standard)
(Reference LTC DWG \# 05-08-1421)


T7 Package
7-Lead Plastic TO-220 (Standard)
(Reference LTC DWG \# 05-08-1422)


## RELATGD PARTS

| PART NUMBER | DESCRIPTION | COMMENTS |
| :---: | :---: | :---: |
| LT1074/HV | 4.4A (Iout), 100kHz High Efficiency Step-Down DC/DC Converter | $\mathrm{V}_{\text {In: }}: 7.3 \mathrm{~V}$ to $45 \mathrm{~V} / 64 \mathrm{~V}, \mathrm{~V}_{\text {OUT(MII) }}: 2.21 \mathrm{~V}, \mathrm{I}_{\mathrm{Q}}: 8.5 \mathrm{~mA}$, ISHDN: $10 \mu \mathrm{~A}, \mathrm{DD} 5 / 7$, T0-2205/7 |
| LT3430 | 60V, 2.75A (IOUT), 200kHz High Efficiency Step-Down DC/DC Converter | $\mathrm{V}_{\text {IN }}: 5.5 \mathrm{~V}$ to $60 \mathrm{~V}, \mathrm{~V}_{\text {OUT(MIN) }}: 1.20 \mathrm{~V}, \mathrm{I}_{\mathrm{Q}}: 2.5 \mathrm{~mA}, \mathrm{I}_{\text {SHDN }}: 25 \mu \mathrm{~A}, \mathrm{TSSOP} 16 \mathrm{E}$ |
| LT1956 | 60V, 1.2A (Iout), 500kHz High Efficiency Step-Down DC/DC Converter | $\mathrm{V}_{\text {IN }}: 5.5 \mathrm{~V}$ to $60 \mathrm{~V}, \mathrm{~V}_{\text {OUT(MIN) }}: 1.20 \mathrm{~V}, \mathrm{I}_{\mathrm{Q}}: 2.5 \mathrm{~mA}, \mathrm{I}_{\text {SHDN }}: 25 \mu \mathrm{~A}, \mathrm{TSSOP} 16 \mathrm{E}$ |

