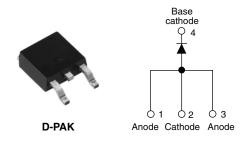




Vishay High Power Products

Surface Mountable Input Rectifier Diode, 8 A



PRODUCT SUMMARY			
V _F at 10 A 1 V			
I _{FSM}	200 A		
V _{RRM} 1600 V			

DESCRIPTION/FEATURES

The 8EWS16SPbF rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.

The **high reverse voltage** range available allows design of input stage primary rectification with **outstanding voltage surge** capability.

Typical applications are in input rectification and these products are designed to be used with Vishay HPP switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level.

Compliant to RoHS directive 2002/95/EC.

OUTPUT CURRENT IN TYPICAL APPLICATIONS						
APPLICATIONS SINGLE-PHASE BRIDGE THREE-PHASE BRIDGE UNITS						
NEMA FR-4 or G10 glass fabric-based epoxy with 4 oz. (140 μm) copper	1.2	1.6				
Aluminum IMS, R _{thCA} = 15 °C/W	2.5	2.8	А			
Aluminum IMS with heatsink, R _{thCA} = 5 °C/W	5.5	6.5				

Note

• $T_A = 55$ °C, $T_J = 125$ °C, footprint 300 mm²

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL CHARACTERISTICS VALUES UNITS							
I _{F(AV)}	Sinusoidal waveform	8	A				
V _{RRM}		1600	V				
I _{FSM}		200	A				
V _F	8 A, T _J = 25 °C	1.10	V				
T _J		- 40 to 150	°C				

VOLTAGE RATINGS							
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA				
8EWS16SPbF	1600	1700	0.5				

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	$T_C = 105$ °C, 180° conduction half sine wave	8		
Maximum peak one cycle		10 ms sine pulse, rated V _{RRM} applied	170	Α	
non-repetitive surge current	I _{FSM}	10 ms sine pulse, no voltage reapplied	200		
Maximum I ² t for fusing	l ² t	10 ms sine pulse, rated V _{RRM} applied	130	A ² s	
waxiinum i-t ioi iusing	1-1	10 ms sine pulse, no voltage reapplied	145	A-5	
Maximum I ² √t for fusing	I²√t	t = 0.1 ms to 10 ms, no voltage reapplied	1450	A²√s	

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS			UNITS	
Maximum forward voltage drop	V_{FM}	8 A, T _J = 25 °C		1.1	V	
Forward slope resistance	r _t	r _t T _J = 150 °C		20	mΩ	
Threshold voltage	V _{F(TO)}	1J=150 C		0.82	V	
Maximum rayaraa laakaga gurrant		T _J = 25 °C	V - Poted V	0.05	mΛ	
Maximum reverse leakage current	IRM	T _J = 150 °C	V _R = Rated V _{RRM}	0.50	mA	

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 40 to 150	°C	
Soldering temperature	T _S		240		
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	2.5	°C/W	
Typical thermal resistance, junction to ambient (PCB mount) ⁽¹⁾	R _{thJA}		62	*C/W	
Approximate weight			1	g	
Approximate weight			0.03	oz.	
Marking device		Case style TO-252AA (D-PAK)	8EW	S16S	

 $^{^{(1)}}$ When mounted on 1" square (650 mm²) PCB of FR-4 or G-10 material 4 oz. (140 μm) copper 40 °C/W For recommended footprint and soldering techniques refer to application note #AN-994



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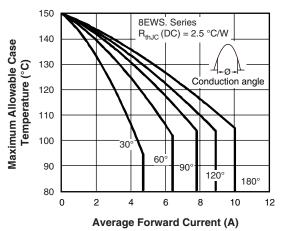


Fig. 1 - Current Rating Characteristics

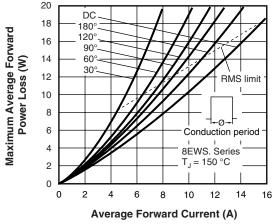


Fig. 4 - Forward Power Loss Characteristics

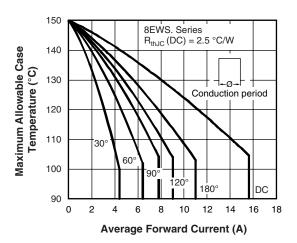


Fig. 2 - Current Rating Characteristics

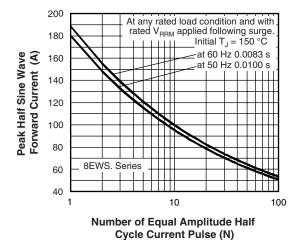


Fig. 5 - Maximum Non-Repetitive Surge Current

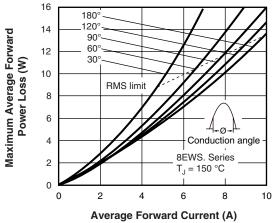


Fig. 3 - Forward Power Loss Characteristics

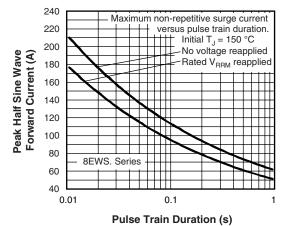


Fig. 6 - Maximum Non-Repetitive Surge Current

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Surface Mountable Input Rectifier Diode, 8 A



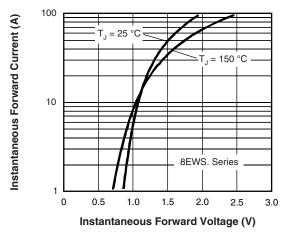


Fig. 7 - Forward Voltage Drop Characteristics

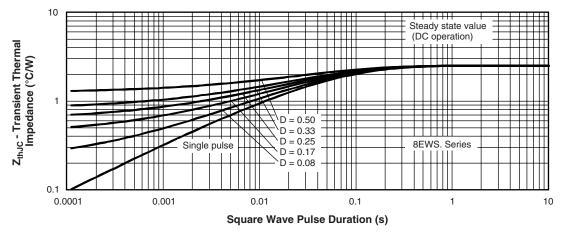


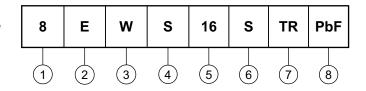
Fig. 8 - Thermal Impedance Z_{thJC} Characteristics



Surface Mountable Vishay High Power Products Input Rectifier Diode, 8 A

ORDERING INFORMATION TABLE

Device code



- 1 Current rating (8 = 8 A)
- 2 Circuit configuration:

E = Single diode

Package:

W = D-PAK

4 - Type of silicon:

S = Standard recovery rectifier

- 5 Voltage rating (16 = 1600 V)
- 6 S = Surface mountable
- 7 • TR = Tape and reel
 - TRR = Tape and reel (right oriented)
 - TRL = Tape and reel (left oriented)
- PbF = Lead (Pb)-free

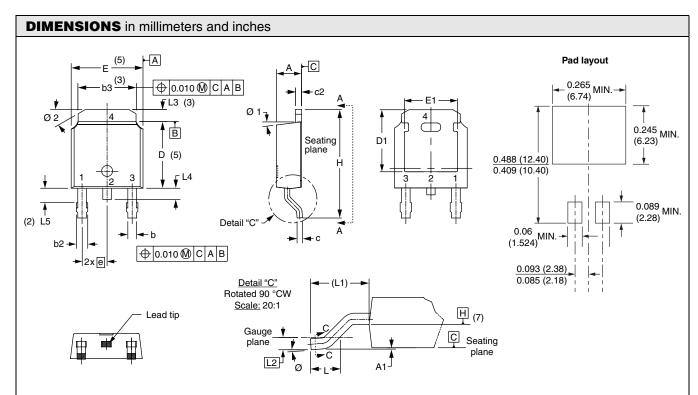
LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95016				
Part marking information	www.vishay.com/doc?95059			
Packaging information	www.vishay.com/doc?95033			

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D-PAK (TO-252AA)



SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	1	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	2.29 BSC		BSC	
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74	2.74 BSC		0.108 REF.	
L2	0.51 BSC		0.020 BSC		
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0°	10°	0°	10°	
Ø1	0°	15°	0°	15°	
Ø2	25°	35°	25°	35°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- $^{(7)}$ Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC outline TO-252AA





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