

MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, ADVANCED SCHOTTKY TTL, NAND GATES, MONOLITHIC SILICON

This specification is approved for use by all Departments and Agencies of the Department of Defense.

1. SCOPE

1.1 Scope. This specification covers the detail requirements for monolithic silicon, Advanced Schottky TTL, positive NAND logic gate microcircuits. Two product assurance classes and a choice of case outlines and lead finishes are provided for each type and are reflected in the complete part number.

1.2 Part number. The part number shall be in accordance with MIL-M-38510, and as specified herein.

1.2.1 Device type. The device type shall be as follows:

Device type	Circuit
01	Quadruple, 2-input positive NAND gate
02	Hex, 1-input inverter gate
03	Triple, 3-input positive NAND gate
04	Dual, 4-input positive NAND gate

1.2.2 Device class. The device class shall be the product assurance level as defined in MIL-M-38510.

1.2.3 Case outline. The case outline shall be designated as follows:

Letter	Case outline (see MIL-M-38510, appendix C)
A	F-1 (14-lead, 1/4" x 1/4"), flat package
B	F-3 (14-lead, 3/16" x 1/4"), flat package
C	D-1 (14-lead, 1/4" x 3/4"), dual-in-line package
D	F-2 (14-lead, 1/4" x 3/8"), flat package
X	C-2 (20-terminal, .350" x .350"), square chip carrier package, option A
Y	C-2 (20-terminal, .350" x .350"), square chip carrier package, option B

1.3 Absolute maximum ratings.

Supply voltage range - - - - -	-0.5 V to +7.0 V
Input voltage range - - - - -	-1.2 V at -18 mA to +7.0 V
Storage temperature range - - - - -	-65°C to +150°C
Maximum power dissipation per device (P _D) ^{1/} :	
Device type 01 - - - - -	56 mW
Device type 02 - - - - -	84 mW
Device type 03 - - - - -	42 mW
Device type 04 - - - - -	28 mW
Lead temperature (soldering, 10 seconds) - - -	300°C
Thermal resistance, junction to case (θ _{JC}):	
Cases A, B, and D - - - - -	70°C/W
Case C - - - - -	50°C/W
Cases X and Y - - - - -	60°C/W
Junction temperature (T _J) - - - - -	175°C

^{1/} Must withstand the added P_D due to short-circuit test (e.g., I_{OS}).

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Rome Air Development Center (RBE-2), Griffiss AFB, NY 13441, by using the self-addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

1.4 Recommended operating conditions.

Supply voltage- - - - -	4.5 V minimum to 5.5 V maximum
Minimum high level input voltage (V_{IH}) - - -	2.0 V
Maximum low level input voltage (V_{IL}) - - -	0.8 V
Normalized fanout (each output) ^{2/} :	
Low logic level - - - - -	33 maximum
High logic level - - - - -	50 maximum
Case operating temperature range (T_C) - - -	-55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specifications and standards. Unless otherwise specified, the following specifications and standards, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this specification to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of specifications, standards, handbooks, drawings, and publications required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting officer.)

2.2 Order of precedence. In the event of a conflict between the text of this specification and the references cited herein, the text of this specification shall take precedence.

3. REQUIREMENTS

3.1 Detail specification. The individual item requirements shall be in accordance with MIL-M-38510, and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections and logic diagrams. The terminal connections and logic diagrams shall be as specified on figure 1.

3.2.2 Truth tables and logic equations. The truth tables and logic equations shall be as specified on figure 2.

3.2.3 Schematic circuits. The schematic circuits shall be submitted to the preparing activity prior to inclusion of a manufacturer's device in this specification and shall be submitted to the qualifying activity as a prerequisite for qualification. All manufacturers' schematics shall be maintained and available upon request.

3.2.4 Case outlines. The case outlines shall be as specified in 1.2.3.

3.3 Lead material and finish. The lead material and finish shall be in accordance with MIL-M-38510 (see 6.5).

3.4 Electrical performance characteristics. The electrical performance characteristics are specified in table 1, and apply over the full recommended case operating temperature range, unless otherwise specified.

^{2/} The device shall fanout in both high and low levels to the specified number of data inputs for the same device type as that being tested.

TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-5^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Device type	Limits		Unit
				Min	Max	
High-level output voltage	V_{OH}	$V_{CC} = 4.5\text{ V}$, $I_{OH} = -1.0\text{ mA}$, $V_{IL} = 0.8\text{ V}$	A11	2.5		V
Low-level output voltage	V_{OL}	$V_{CC} = 4.5\text{ V}$, $I_{OL} = 20\text{ mA}$, $V_{IH} = 2.0\text{ V}$	A11		0.5	V
Input clamp voltage	V_{IC}	$V_{CC} = 4.5\text{ V}$, $I_{IN} = -18\text{ mA}$, $T_C = 25^{\circ}\text{C}$	A11		-1.2	V
High-level input current	I_{IH1}	$V_{CC} = 5.5\text{ V}$, $V_{IN} = 2.7\text{ V}$	A11		20	μA
	I_{IH2}	$V_{CC} = 5.5\text{ V}$, $V_{IN} = 7.0\text{ V}$	A11		100	μA
Low-level input current	I_{IL}	$V_{CC} = 5.5\text{ V}$, $V_{IN} = 0.5\text{ V}$	A11	-0.03	-0.60	mA
Short-circuit output current <u>1/</u>	I_{OS}	$V_{CC} = 5.5\text{ V}$	A11	-60	-150	mA
Output drive	I_{OD}	$V_{CC} = 4.5\text{ V}$, $V_{IN} = 5.5\text{ V}$, $V_{OUT} = 2.5\text{ V}$	A11	60		mA
High-level supply current	I_{CCH}	$V_{CC} = 5.5\text{ V}$	01		2.8	mA
			02		4.2	mA
			03		2.1	mA
			04		1.4	mA
Low-level supply current	I_{CCL}	$V_{CC} = 5.5\text{ V}$	01		10.2	mA
			02		15.3	mA
			03		7.7	mA
			04		5.1	mA
Propagation delay time, high-to-low level	t_{PHL}	$V_{CC} = 5.0\text{ V}$, $C_L = 50\text{ pF} \pm 10\%$, $R_L = 500\Omega \pm 5\%$	A11	1.5	6.5	ns
Propagation delay time, low-to-high level	t_{PLH}	$V_{CC} = 5.0\text{ V}$, $C_L = 50\text{ pF} \pm 10\%$, $R_L = 500\Omega \pm 5\%$	A11	2.0	7.0	ns

1/ Not more than one output should be shorted at a time.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (see table III)	
	Class S devices	Class B devices
Interim electrical parameters (pre burn-in) (method 5004)	1	1
Final electrical test parameters (method 5004)	1*,2,3,9, 10,11	1*,2,3,9
Group A test requirements (method 5005)	1,2,3, 9,10,11	1,2,3,9
Group B test requirements (method 5005), subgroup 5	1,2,3, 9,10,11	N/A
Group C end-point electrical parameters (method 5005)	N/A	1,2,3
Additional electrical subgroups for group C periodic inspections	N/A	10,11
Group D end-point electrical parameters (method 5005)	1,2,3	1,2,3

*Indicates PDA applies to subgroup 1 (see 4.2c).

3.5 Electrical test requirements. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.

3.6 Marking. Marking shall be in accordance with MIL-M-38510. At the option of the manufacturer, marking of the country of origin may be omitted from the body of the microcircuit, but shall be retained on the initial container.

3.7 Microcircuit group assignment. The devices covered by this specification shall be in microcircuit group number 8 (see MIL-M-38510, appendix E).

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-M-38510 and methods 5005 and 5007, as applicable, of MIL-STD-883, except as modified herein.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to qualification and quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in test (method 1015 of MIL-STD-883).
 - (1) Test condition D or E, using the circuit shown on figure 3, or equivalent.
 - (2) $T_A = +125^\circ\text{C}$ minimum.
- b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.

- c. The percent defective allowable (PDA) for class S devices shall be as specified in MIL-M-38510. The PDA for class B devices shall be 10 percent based on failures from group A, subgroup 1 test after cooldown as the final electrical test in accordance with method 5004 of MIL-STD-883, and with no intervening electrical measurements. If interim electrical parameter tests are performed prior to burn-in, failures resulting from pre burn-in screening may be excluded from the PDA. If interim electrical parameter tests prior to burn-in are omitted, then all screening failures shall be included in the PDA. The verified failures of group A, subgroup 1, after burn-in divided by the total number of devices submitted for burn-in in that lot shall be used to determine the percent defective for that lot, and the lot shall be accepted or rejected based on the PDA for the applicable device class.

4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-M-38510. Inspections to be performed shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).

4.4 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-M-38510. Inspections to be performed shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4).

4.4.1 Group A inspection. Group A inspection shall be in accordance with table I of method 5005 of MIL-STD-883 and as follows:

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, 6, 7, and 8 shall be omitted.

4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of method 5005 of MIL-STD-883. Electrical parameters shall be as specified in table II herein.

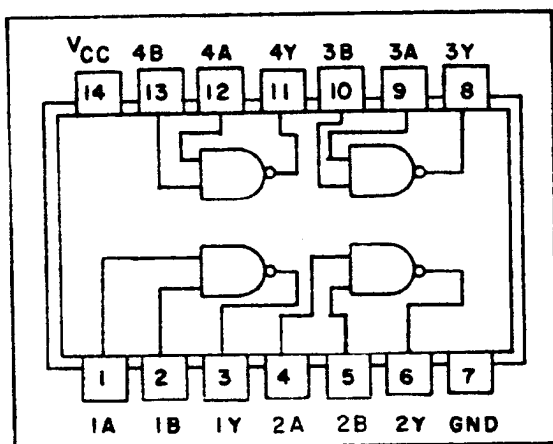
4.4.3 Group C inspection. Group C inspection shall be in accordance with table III of method 5005 of MIL-STD-883 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Subgroups 3 and 4 shall be added to the group C inspection requirements for class B devices and shall consist of the tests, conditions, and limits specified for subgroups 10 and 11 of group A inspection.
- c. Steady state life test (method 1005 of MIL-STD-883) conditions, or equivalent.
 - (1) Test condition D or E, using the circuit shown on figure 3, or equivalent.
 - (2) $T_A = +125^{\circ}\text{C}$ minimum.
 - (3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510.

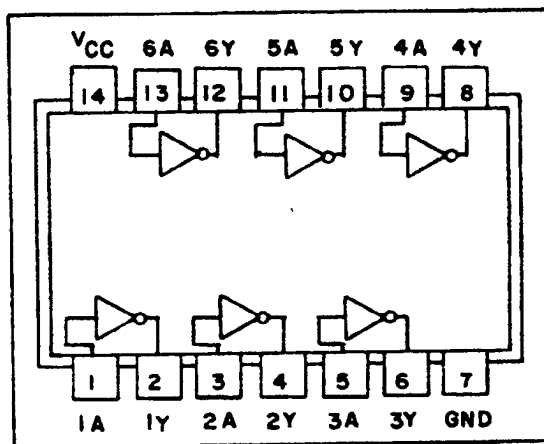
4.4.4 Group D inspection. Group D inspection shall be in accordance with table IV of method 5005 of MIL-STD-883. End-point electrical parameters shall be as specified in table II herein.

4.5 Methods of inspection. Methods of inspection shall be as follows:

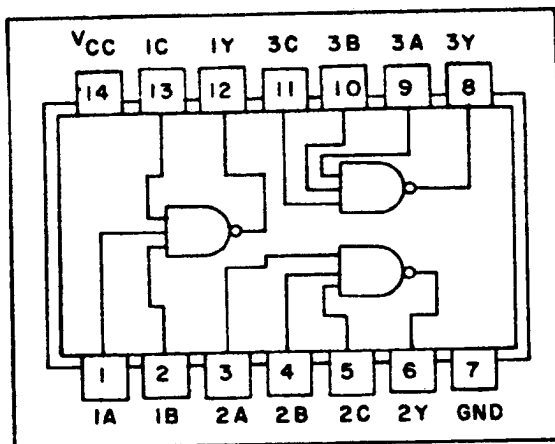
Device type 01
Cases A, B, C, and D



Device type 02
Cases A, B, C, and D



Device type 03
Cases A, B, C, and D



Device type 04
Cases A, B, C, and D

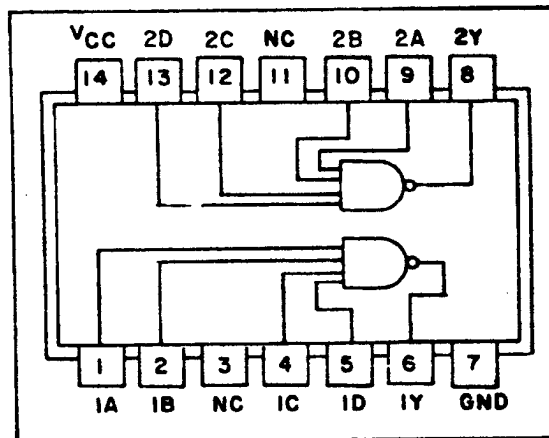


FIGURE 1. Terminal connections (top view).

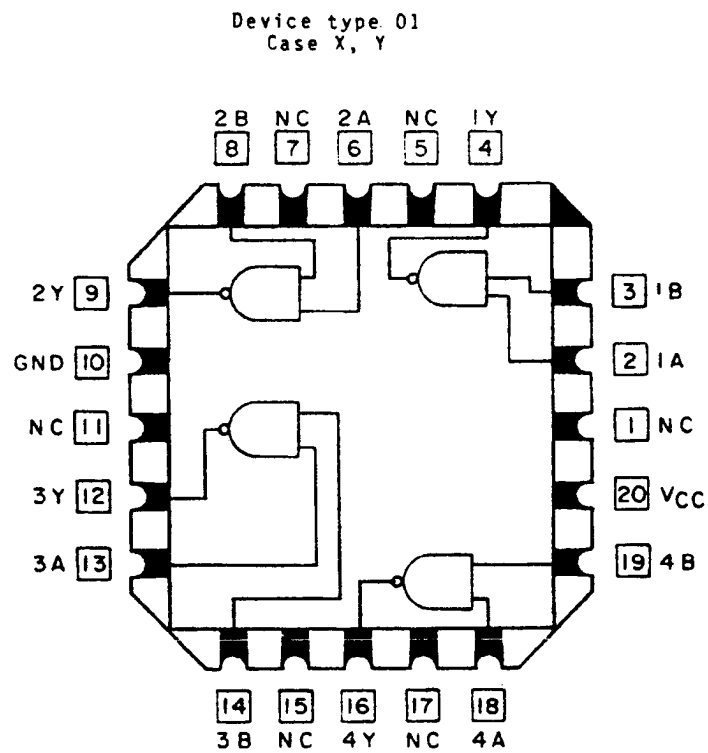


FIGURE 1. Terminal connections (top view) - Continued.

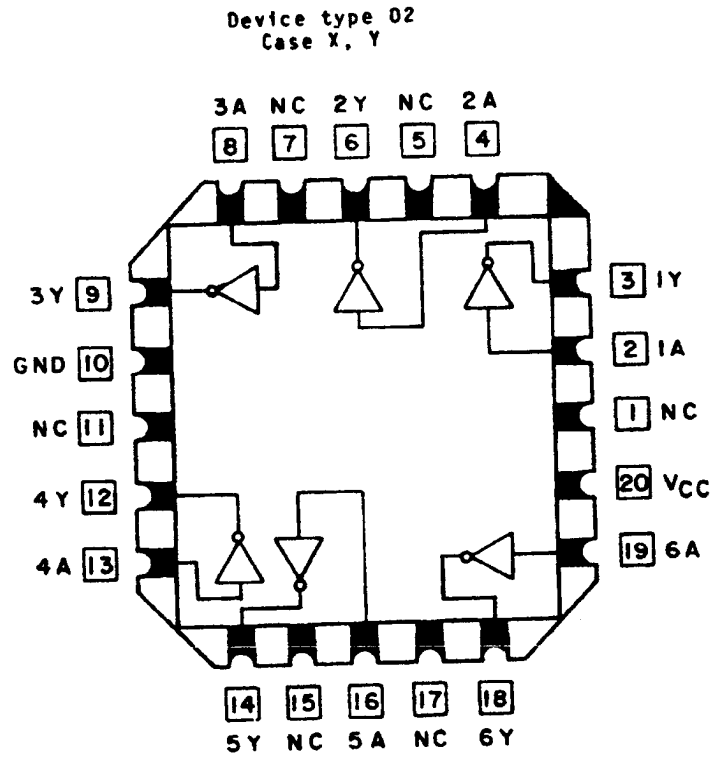


FIGURE 1. Terminal connections (top view) - Continued.

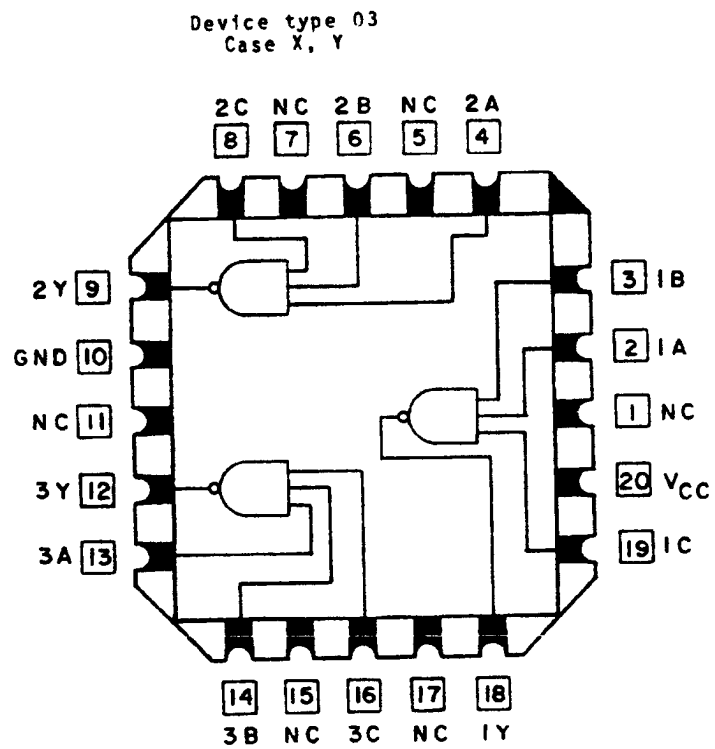


FIGURE 1. Terminal connections (top view) - Continued.

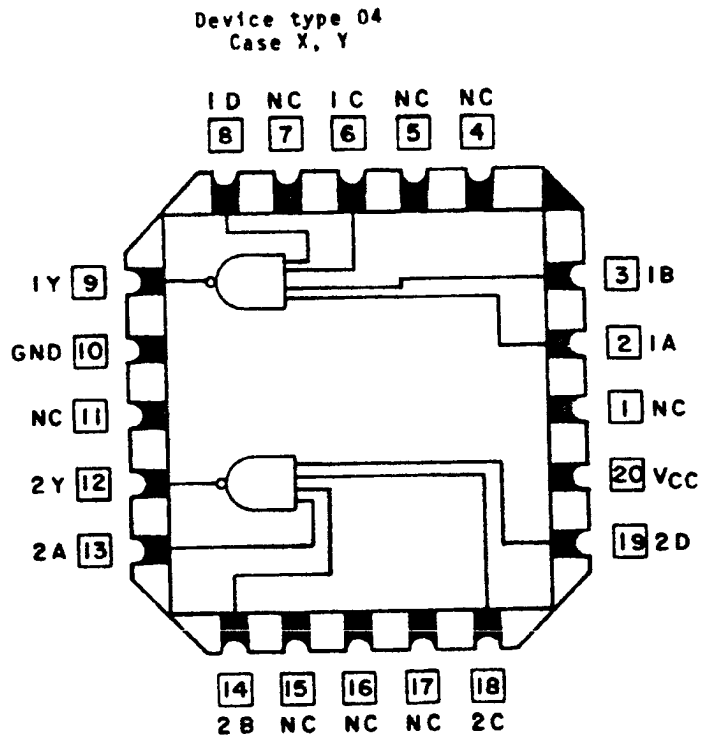


FIGURE 1. Terminal connections (top view) - Continued.

Device type 01

Truth table each gate		
Input		Output
A	B	Y
L	L	H
H	L	H
L	H	H
H	H	L

Positive logic $Y = \overline{AB}$

Device type 02

Truth table each gate	
Input	Output
A	Y
L	H
H	L

Positive logic $Y = \overline{A}$

Device type 03

Truth table each gate			
Input			Output
A	B	C	Y
L	L	L	H
H	L	L	H
L	H	L	H
H	H	L	H
L	L	H	H
H	L	H	H
L	H	H	H
H	H	H	L

Positive logic $Y = \overline{ABC}$

Device type 04

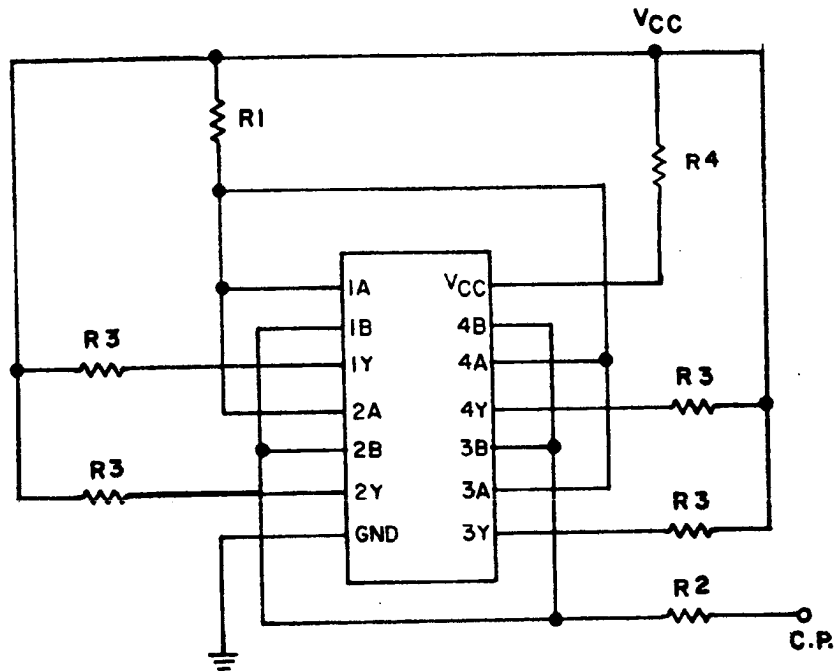
Truth table each gate				
Input				Output
A	B	C	D	Y
L	L	L	L	H
H	L	L	L	H
L	H	L	L	H
H	H	L	L	H
L	L	H	L	H
H	L	H	L	H
L	H	H	L	H
H	H	H	L	H
L	L	L	H	H
H	L	L	H	H
L	H	L	H	H
H	H	L	H	H
L	L	H	H	H
H	L	H	H	H
L	H	H	H	H
H	H	H	H	L

Positive logic $Y = \overline{ABCD}$

FIGURE 2. Truth tables and logic equations.

Device type 01

Cases A, B, C, D, X, and Y
 (Pins not shown for cases X and Y are N/C)

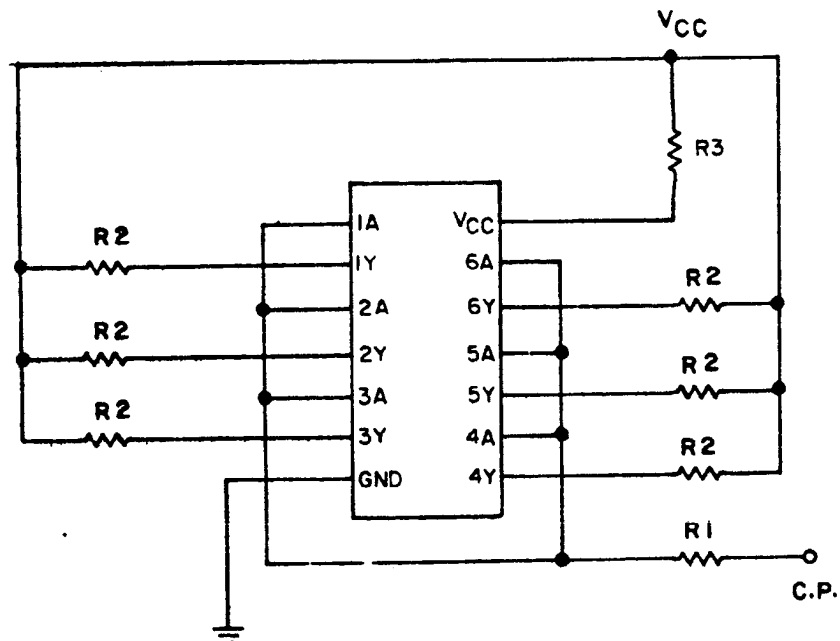


1. $R1 = 1\text{ k}\Omega \pm 5\%$.
 $R2 = 160\Omega \pm 5\%$.
 $R3 = 270\Omega \pm 5\%$.
2. V_{CC} and $R4$ shall be chosen to insure a 5.5 V minimum is present at device V_{CC} terminal.
3. C.P. = 100 kHz $\pm 50\%$ square wave; duty cycle = 50 $\pm 15\%$; $V_{IL} = -0.5\text{ V min to } 0.8\text{ V max}$; $V_{IH} = 2.0\text{ V min to } 5.5\text{ V max}$.

FIGURE 3. Burn-in and life test circuit.

Device type 02

Cases A, B, C, D, X, and Y
 (Pins not shown for cases X and Y are N/C)



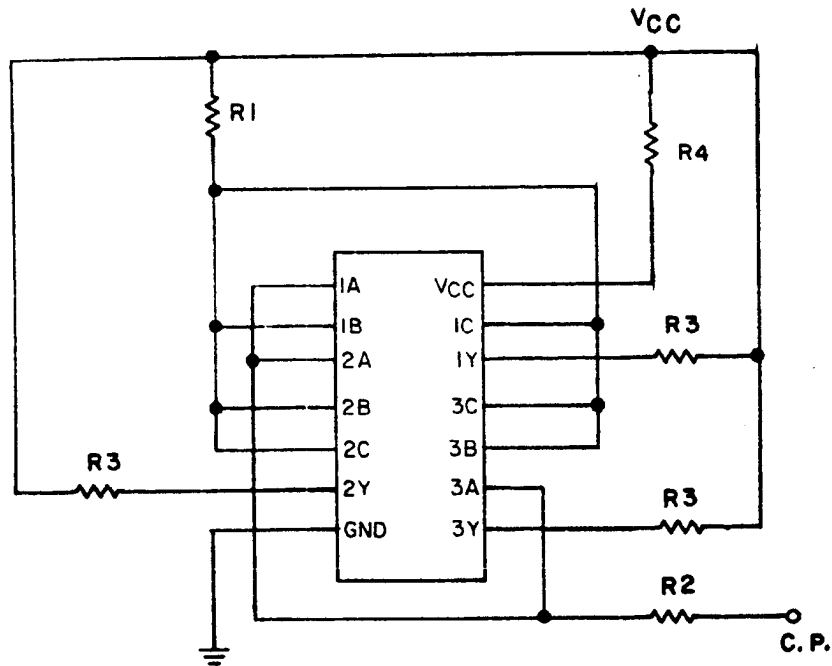
NOTES:

1. $R1 \leq 110\Omega$.
 $R2 = 270\Omega \pm 5\%$.
2. V_{CC} and $R3$ shall be chosen to insure a 5.5 V minimum is present at device V_{CC} terminal.
3. C.P. = 100 kHz square wave $\pm 50\%$; duty cycle = $50 \pm 15\%$; $V_{IL} = -0.5$ V min to 0.8 V max; $V_{IH} = 2.0$ V min to 5.5 V max.

FIGURE 3. Burn-in and life test circuit - Continued.

Device type 03

Cases A, B, C, D, X, and Y
 (Pins not shown for cases X and Y are N/C)



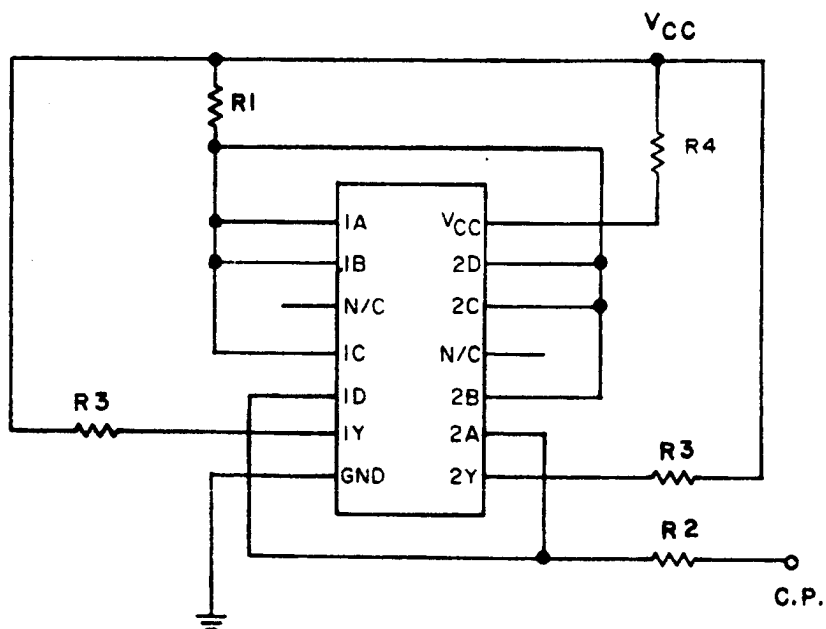
NOTES:

1. $R1 = 1\text{ k}\Omega \pm 5\%$.
 $R2 = 220\Omega \pm 5\%$.
 $R3 = 270\Omega \pm 5\%$.
2. V_{CC} and $R4$ shall be chosen to insure a 5.5 V minimum is present at device V_{CC} terminal.
3. C.P. = 100 kHz $\pm 50\%$ square wave; duty cycle = 50 $\pm 15\%$; $V_{IL} = -0.5\text{ V min to } 0.8\text{ V max}$; $V_{IH} = 2.0\text{ V min to } 5.5\text{ V max}$.

FIGURE 3. Burn-in and life test circuit - Continued.

Device type 04

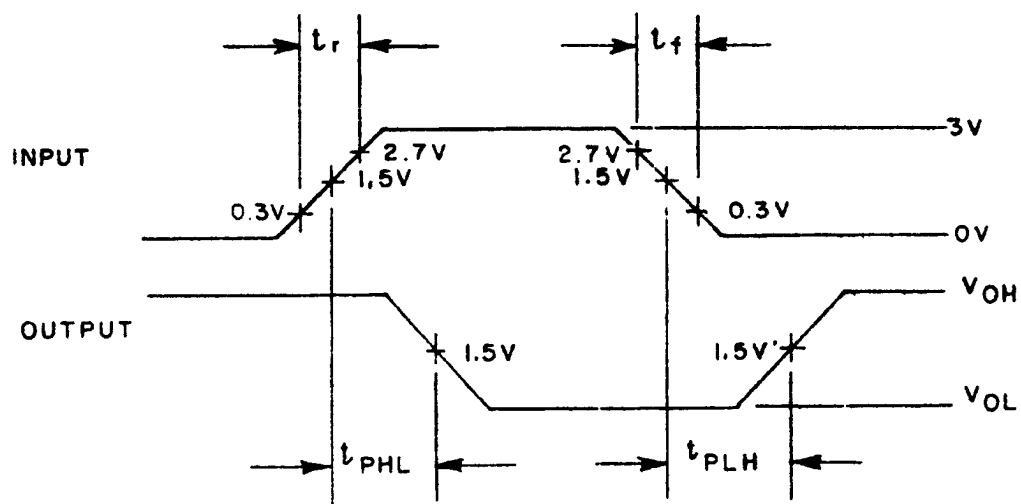
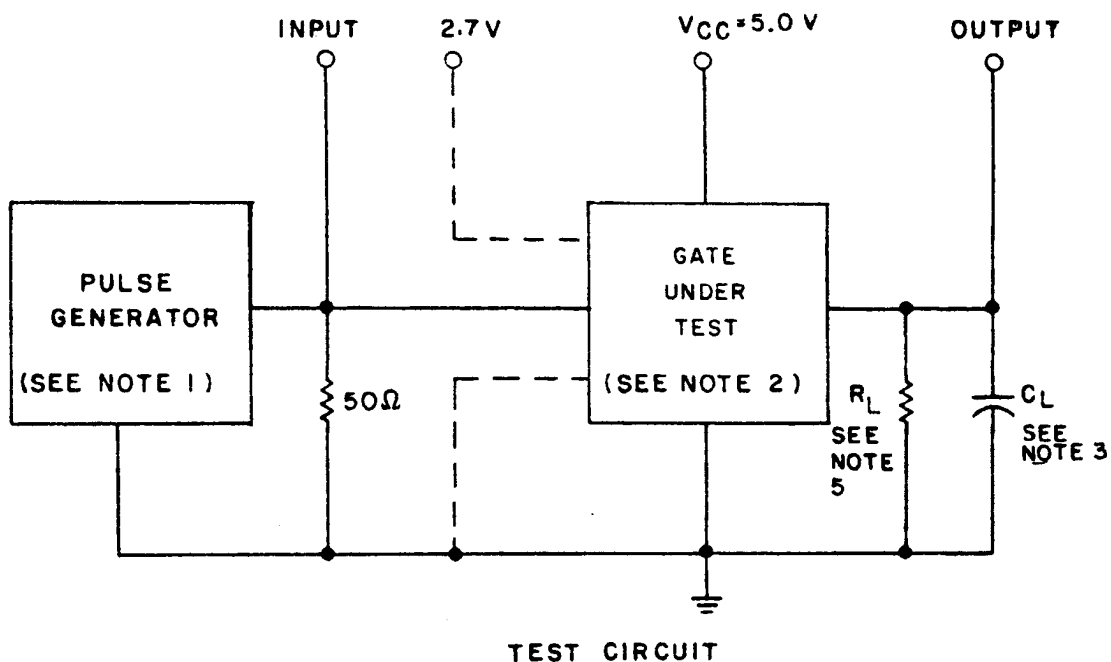
Cases A, B, C, D, X, and Y
 (Pins not shown for cases X and Y are N/C)



NOTES:

1. $R1 = 1\text{ k}\Omega \pm 5\%$.
 $R2 = 330\Omega \pm 5\%$.
 $R3 = 270\Omega \pm 5\%$.
2. V_{CC} and $R4$ shall be chosen to insure a 5.5 V minimum is present at device V_{CC} terminal.
3. C.P. = 100 kHz $\pm 50\%$ square wave; duty cycle = 50 $\pm 15\%$; $V_{IL} = -0.5\text{ V min to } 0.8\text{ V max}$; $V_{IH} = 2.0\text{ V min to } 5.5\text{ V max}$.

FIGURE 3. Burn-in and life test circuit - Continued.



NOTES:

1. Pulse generator has following characteristics:
 $t_r = t_f < 2.5 \text{ ns}$, $\text{PRR} = 1 \text{ MHz}$ and $Z_{\text{OUT}} = 50\Omega$.
2. Inputs not under test are at 2.7 V or GND as specified in table III.
3. $C_L = 50 \text{ pF} \pm 10\%$ including scope, probe, wiring, and stray capacitance, without package in test fixture.
4. Voltage measurements are to be made with respect to network ground terminal.
5. $R_L = 500\Omega \pm 5\%$.

FIGURE 4. Switching time test circuit (all device types).

TABLE III. Group A inspection for device type 01.
 Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.8 V, or open)

Subgroup	Symbol	MIL-STD-883 method	Cases A, B, C, D	Terminals														Limits		Measured terminal	Unit
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	Min	Max		
1 $T_C = 25^\circ\text{C}$	V_{OH}	3006	Test no.	1A	1B	1Y	2A	2B	2Y	GND	3Y	3A	3B	4Y	4A	4B	V _{CC}				
			1	5.5 V	0.8 V	-1 mA	5.5 V	0.8 V	-1 mA	GND							4.5 V	2.5		1Y	V
			2	0.8 V	5.5 V	-1 mA	0.8 V	5.5 V	-1 mA											1Y	V
			3				0.8 V	5.5 V	-1 mA											2Y	V
			4				5.5 V	0.8 V	-1 mA											2Y	V
			5				0.8 V	5.5 V	-1 mA											3Y	V
			6																	3Y	V
			7																	4Y	V
	V_{OL}	3007	9	2.0 V	2.0 V	20 mA	2.0 V	2.0 V	20 mA											1Y	V
			10																	2Y	V
			11																	3Y	V
			12																	4Y	V
	V_{TC}		13	-18 mA	-18 mA		-18 mA	-18 mA												1A	-1.2
			14																	1B	
			15																	2A	
			16																	2B	
			17																	3A	
			18																	3B	
			19																	4A	
			20																	4B	
	t_{TH1}	3010	21	2.7 V	GND		2.7 V	GND									5.5 V			1A	20
			22	GND	2.7 V															1B	
			23																	2A	
			24																	2B	
			25																	3A	
			26																	3B	
			27																	4A	
			28																	4B	
	t_{TH2}		29	7.0 V	GND		7.0 V	GND												1A	100
			30	GND	7.0 V															1B	
			31																	2A	
			32																	2B	
			33																	3A	
			34																	3B	
			35																	4A	
			36																	4B	
	t_{TL}	3009	37	0.5 V	5.5 V		0.5 V	5.5 V												1A	2/
			38	5.5 V	0.5 V															1B	2/
			39																	2A	
			40																	2B	
			41																	3A	
			42																	3B	
			43																	4A	
			44																	4B	
	t_{OS}	3011	45	GND	GND	0.0 V	GND	GND	0.0 V											1Y	-150
			46																	2Y	
			47																	3Y	
			48																	4Y	

See footnotes at end of table.

TABLE III. Group A inspection for device type 01 - Continued.
Terminal conditions (pins not designated may be high ≥ 0.0 V, low ≤ 0.8 V, or open)

Terminal conditions (pins not designated may be high 2.0 V, low 20.8 V, or open)																				
Subgroup	Symbol	MIL-STD-883C method	Cases A, B, C, D														Measured terminal	Limits		
			1	2	3	4	5	6	7	8	9	10	11	12	13	14		Min	Max	Unit
1 $T_C = 25^\circ\text{C}$	I_{DD}	method 100	Test no.	1A	1B	1Y	2A	2B	2Y	3A	3B	3Y	4A	4B	4Y	5A	5B	5Y	6A	
			49	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			50	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			51	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			52	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
2	T_{ICH}	3005	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	10.2	
	T_{OCL}	3005	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	10.2	
Same tests, terminal conditions, and limits as for subgroup 1, except $T_C = 125^\circ\text{C}$ and V _{IC} tests are omitted.																				
3 $T_C = 25^\circ\text{C}$	I_{DPL}	method 3003	Test no.	1A	1B	1Y	2A	2B	2Y	3A	3B	3Y	4A	4B	4Y	5A	5B	5Y	6A	
			53	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			54	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			55	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			56	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			57	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			58	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			59	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			60	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			61	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
10	I_{DPL}	method 3003	Test no.	1A	1B	1Y	2A	2B	2Y	3A	3B	3Y	4A	4B	4Y	5A	5B	5Y	6A	
			62	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			63	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			64	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			65	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			66	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			67	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			68	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			69	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
			70	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
11	I_{DPL}	method 3003	Test no.	1A	1B	1Y	2A	2B	2Y	3A	3B	3Y	4A	4B	4Y	5A	5B	5Y	6A	
			71	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	5.5 V	2.5 V	5.5 V	60
Same tests, terminal conditions, and limits as for subgroup 10, except $T_C = -55^\circ\text{C}$.																				

1/ Pins not referenced (i.e., 1, 5, 7, 11, 15, and 17) are M/C.

2/ I_{IL} limits shall be as follows:

Test	MIN/MAX LIMITS IN mA
I_{IL}	-0.03/-0.80

TABLE III. Group A inspection for device type 02.
Terminal conditions (pins not designated may be high 2.0 V, low 0.8 V, or open)

Subgroup	Symbol	MIL-STD-883 method	Cases A, B, C, D	Cases A, B, C, D														Measured terminal	Limits		Unit
				1	2	3	4	5	6	7	8	9	10	11	12	13	14		Min	Max	
				Test no.	1A	1V	2A	2V	3A	3V	GND	4V	4A	5V	5A	6V	6A				
1 T _C = 25°C	V _{OH}	3006	1	0.8 V	-1 mA	0.8 V	-1 mA	0.8 V	-1 mA	0.8 V	-1 mA	0.8 V	-1 mA	0.8 V	-1 mA	0.8 V	4.5 V	1V	2.5	V	
			2															2V			
			3															3V			
			4															4V			
			5															5V			
			6															6V			
	V _{OL}	3007	7	2.0 V	20 mA	2.0 V	20 mA	2.0 V	20 mA	2.0 V	20 mA	2.0 V	20 mA	2.0 V	20 mA	2.0 V		1V	0.5	V	
			8															2V			
			9															3V			
			10															4V			
			11															5V			
			12															6V			
	V _{IC}		13	-18 mA		-18 mA		-18 mA		-18 mA		-18 mA		-18 mA		-18 mA		1A	-1.2	V	
			14															2A			
			15															3A			
			16															4A			
			17															5A			
			18															6A			
	V _{IH1}	3010	19	2.7 V		2.7 V		2.7 V		2.7 V		2.7 V		2.7 V		2.7 V	5.5 V	1A	20	μA	
			20															2A			
			21															3A			
			22															4A			
			23															5A			
			24															6A			
	V _{IH2}		25	7.0 V		7.0 V		7.0 V		7.0 V		7.0 V		7.0 V		7.0 V		1A	100	V	
			26															2A			
			27															3A			
			28															4A			
			29															5A			
			30															6A			
	V _{IL}	3009	31	0.5 V		0.5 V		0.5 V		0.5 V		0.5 V		0.5 V		0.5 V		1A	2	mA	
			32															2A			
			33															3A			
			34															4A			
			35															5A			
			36															6A			
	V _{GS}	3011	37	GND	0.0 V	GND	0.0 V	GND	0.0 V	GND	0.0 V	GND	0.0 V	GND	0.0 V	GND		1V	-80	-150	V
			38															2V			
			39															3V			
			40															4V			
			41															5V			
			42															6V			

/ See footnotes at end of table.

TABLE III. Group A Inspection for device type 02 - Continued.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.8 V, or open)

terminal conditions (limits not designated) at -55°C																						
Subgroup	Symbol	MIL-STD-883C Method	Cases A, B, C, D	Test no.														Measured terminal	Limits			
				1	2	3	4	5	6	7	8	9	10	11	12	13	14		Min	Max	Unit	
1	t_{f00}		1/	1A	1V	2A	2V	3A	3V	GND	4V	4A	5V	5A	6V	6A	V _{CC}					
				43	5.5 V	2.5 V				GND								4.5 V	1V	60	mA	
				44			5.5 V	2.5 V												2V		
				45					2.5 V											3V		
				46							2.5 V									4V		
				47									5.5 V	2.5 V	5.5 V					5V		
				48														6V				
	t_{fCH}	3005		49	GND		GND					GND		GND		GND	5.5 V	V _{CC}	4.2	mA		
				50	5.5 V		5.5 V					5.5 V		5.5 V		5.5 V	5.5 V	V _{CC}	15.3	mA		
2	Same tests, terminal conditions, and limits as for subgroup 1, except $T_C = 125^\circ\text{C}$ and VIC tests are omitted.																					
3	Same tests, terminal conditions, and limits as for subgroup 1, except $T_C = -55^\circ\text{C}$ and VIC tests are omitted.																					
9	t_{fHL}	3003	Fig. 4	51	IN	OUT				GND							5.0 V	1A → 1V	2.0	ns		
				52			IN	OUT											2A → 2V			
				53					IN	OUT										3A → 3V		
				54							OUT									4A → 4V		
				55								IN								5A → 5V		
				56										IN	OUT					6A → 6V		
				57	IN	OUT												1A → 1V	2.4	ns		
				58			IN	OUT											2A → 2V			
				59					IN	OUT									3A → 3V			
				60							OUT									4A → 4V		
				61								IN								5A → 5V		
				62										IN	OUT					6A → 6V		
10	t_{fHL}			63 - 68	Same tests and terminal conditions as for subgroup 9, except $T_C = 125^\circ\text{C}$														1.5	6.5		
				69 - 74															2.0	7.0		
11	Same tests, terminal conditions, and limits as for subgroup 10, except $T_C = -55^\circ\text{C}$.																					

1/ Pins not referenced (i.e., 1, 5, 7, 11, 15, and 17) are W/C.

2/ t_{fL} limits shall be as follows:

Test	MIN/MAX LIMITS IN mA	
	A	B
t_{fL}	-0.25/-0.00	-0.03/-0.00

TABLE III. Group A inspection for device type 03.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low ≤ 0.8 V, or open)

Subgroup	Symbol	MIL-STD-883C Method	Cases A, B, C, D Test no.	Terminal conditions															Measured terminal	Limits		Unit																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																											
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	15		16	17		18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	101	102	103	104	105	106	107	108	109	110	111	112	113	114	115	116	117	118	119	120	121	122	123	124	125	126	127	128	129	130	131	132	133	134	135	136	137	138	139	140	141	142	143	144	145	146	147	148	149	150	151	152	153	154	155	156	157	158	159	160	161	162	163	164	165	166	167	168	169	170	171	172	173	174	175	176	177	178	179	180	181	182	183	184	185	186	187	188	189	190	191	192	193	194	195	196	197	198	199	200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216	217	218	219	220	221	222	223	224	225	226	227	228	229	230	231	232	233	234	235	236	237	238	239	240	241	242	243	244	245	246	247	248	249	250	251	252	253	254	255	256	257	258	259	260	261	262	263	264	265	266	267	268	269	270	271	272	273	274	275	276	277	278	279	280	281	282	283	284	285	286	287	288	289	290	291	292	293	294	295	296	297	298	299	300	301	302	303	304	305	306	307	308	309	310	311	312	313	314	315	316	317	318	319	320	321	322	323	324	325	326	327	328	329	330	331	332	333	334	335	336	337	338	339	340	341	342	343	344	345	346	347	348	349	350	351	352	353	354	355	356	357	358	359	360	361	362	363	364	365	366	367	368	369	370	371	372	373	374	375	376	377	378	379	380	381	382	383	384	385	386	387	388	389	390	391	392	393	394	395	396	397	398	399	400	401	402	403	404	405	406	407	408	409	410	411	412	413	414	415	416	417	418	419	420	421	422	423	424	425	426	427	428	429	430	431	432	433	434	435	436	437	438	439	440	441	442	443	444	445	446	447	448	449	450	451	452	453	454	455	456	457	458	459	460	461	462	463	464	465	466	467	468	469	470	471	472	473	474	475	476	477	478	479	480	481	482	483	484	485	486	487	488	489	490	491	492	493	494	495	496	497	498	499	500	501	502	503	504	505	506	507	508	509	510	511	512	513	514	515	516	517	518	519	520	521	522	523	524	525	526	527	528	529	530	531	532	533	534	535	536	537	538	539	540	541	542	543	544	545	546	547	548	549	550	551	552	553	554	555	556	557	558	559	560	561	562	563	564	565	566	567	568	569	570	571	572	573	574	575	576	577	578	579	580	581	582	583	584	585	586	587	588	589	590	591	592	593	594	595	596	597	598	599	600	601	602	603	604	605	606	607	608	609	610	611	612	613	614	615	616	617	618	619	620	621	622	623	624	625	626	627	628	629	630	631	632	633	634	635	636	637	638	639	640	641	642	643	644	645	646	647	648	649	650	651	652	653	654	655	656	657	658	659	660	661	662	663	664	665	666	667	668	669	670	671	672	673	674	675	676	677	678	679	680	681	682	683	684	685	686	687	688	689	690	691	692	693	694	695	696	697	698	699	700	701	702	703	704	705	706	707	708	709	710	711	712	713	714	715	716	717	718	719	720	721	722	723	724	725	726	727	728	729	730	731	732	733	734	735	736	737	738	739	740	741	742	743	744	745	746	747	748	749	750	751	752	753	754	755	756	757	758	759	760	761	762	763	764	765	766	767	768	769	770	771	772	773	774	775	776	777	778	779	780	781	782	783	784	785	786	787	788	789	790	791	792	793	794	795	796	797	798	799	800	801	802	803	804	805	806	807	808	809	810	811	812	813	814	815	816	817	818	819	820	821	822	823	824	825	826	827	828	829	830	831	832	833	834	835	836	837	838	839	840	841	842	843	844	845	846	847	848	849	850	851	852	853	854	855	856	857	858	859	860	861	862	863	864	865	866	867	868	869	870	871	872	873	874	875	876	877	878	879	880	881	882	883	884	885	886	887	888	889	890	891	892	893	894	895	896	897	898	899	900	901	902	903	904	905	906	907	908	909	910	911	912	913	914	915	916	917	918	919	920	921	922	923	924	925	926	927	928	929	930	931	932	933	934	935	936	937	938	939	940	941	942	943	944	945	946	947	948	949	950	951	952	953	954	955	956	957	958	959	960	961	962	963	964	965	966	967	968	969	970	971	972	973	974	975	976	977	978	979	980	981	982	983	984	985	986	987	988	989	990	991	992	993	994	995	996	997	998	999	1000	1001	1002	1003	1004	1005	1006	1007	1008	1009	1010	1011	1012	1013	1014	1015	1016	1017	1018	1019	1020	1021	1022	1023	1024	1025	1026	1027	1028	1029	1030	1031	1032	1033	1034	1035	1036	1037	1038	1039	1040	1041	1042	1043	1044	1045	1046	1047	1048	1049	1050	1051	1052	1053	1054	1055	1056	1057	1058	1059	1060	1061	1062	1063	1064	1065	1066	1067	1068	1069	1070	1071	1072	1073	1074	1075	1076	1077	1078	1079	1080	1081	1082	1083	1084	1085	1086	1087	1088	1089	1090	1091	1092	1093	1094	1095	1096	1097	1098	1099	1100	1101	1102	1103	1104	1105	1106	1107	1108	1109	1110	1111	1112	1113	1114	1115	11

See footnotes at end of table.

TABLE III. Group A inspection for device type D3 - Continued.
Terminal conditions (pins not designated may be high ≥ 7.0 V, low ≤ 0.8 V, or open)

Subgroup	Symbol	MIL-STD-883 method	Cases A, B, C, D Test no.	Cases A, B, C, D														Measured terminal		Limits																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	Min	Max	Unit																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
1 $T_C = 25^\circ\text{C}$	I_{DD}		52	5.5 V	5.5 V																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																												
				53	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5

1/ Pins not referenced (i.e., 1, 5, 7, 11, 15, and 17) are N/C.

2/ I_{IL} limits shall be as follows:

Test	Min/Max Limits in mA	
	A	B
I_{IL}	-0.25/-0.60	-0.03/-0.60

TABLE III. Group A Inspection for device type DA.
Terminal conditions (pins not designated may be High ≥ 2.0 V, low ≤ 0.8 V, or open)

Subgroup	Symbol	MIL-STD-883 method	Cases A, B, C, D		Terminal conditions (pins not designated may be High ≥ 2.0 V, low ≤ 0.8 V, or open)														Measured terminal	Limits		Unit	
			Test no.	Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14		Min	Max		
$T_C = 25^\circ\text{C}$	V_{OH}	3006	1	0.8 V	5.5 V	5.5 V	5.5 V	5.5 V	-1 mA	GND								4.5 V	1V	2.5	V		
			2	5.5 V	0.8 V		5.5 V												1V			V	
			3	5.5 V	5.5 V		5.5 V												1V			V	
			4		5.5 V		5.5 V	0.8 V												2V			V
			5																	2V			V
			6																	2V			V
			7																	2V			V
			8																	2V			V
V_{OL}	3007	9	2.0 V	2.0 V		2.0 V	2.0 V	20 mA										1V	0.5	V			
		10																2V	0.5	V			
V_{IC}			11	-18 mA				-18 mA											1A	-1.2	A		
			12																1A		A		
			13																1C		A		
			14																1D		A		
			15																2A		A		
			16																2B		A		
			17																2C		A		
			18																2D		A		
I_{IH1}	3010	19	2.7 V	GND		GND	GND											5.5 V	1A	70	μA		
		20																	1A		μA		
		21																	1C		μA		
		22																	1D		μA		
		23																	2A		μA		
		24																	2B		μA		
		25																	2C		μA		
		26																	2D		μA		
I_{IH2}		27	7.0 V	GND		GND	GND												1A	100	μA		
		28																	1A		μA		
		29																	1C		μA		
		30																	1D		μA		
		31																	2A		μA		
		32																	2B		μA		
		33																	2C		μA		
		34																	2D		μA		
I_{IHL}	3009	35	0.5 V	5.5 V		5.5 V	5.5 V												1A	2/	mA		
		36	5.5 V	0.5 V		5.5 V													1A		mA		
		37																	1C		mA		
		38																	1D		mA		
		39																	2A		mA		
		40																	2B		mA		
		41																	2C		mA		
		42																	2D		mA		
I_{IOS}	3011	43	GND	GND		GND	GND	0.0 V											1V	-80	-150	mA	
		44																	2V	-80	-150	mA	

See footnotes at end of table.

TABLE III. Group A inspection for device type 04 - Continued.
Terminal conditions (pins not designated may be high ≥ 2.0 V, low < 0.8 V, or open)

Subgroup	Symbol	MIL-STD-883 method	Test no.	Cases A, B, C, D																				Measured terminal	Limits		Unit		
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20		Min	Max			
				1A	1B	1C	1D	1E	1F	1G	1H	1I	1J	1K	1L	1M	1N	1O	1P	1Q	1R	1S	1T		1U	1V		1W	1X
1 $T_C = 25^\circ\text{C}$	I_{DD}	3005	45	5.5 V	5.5 V		5.5 V	5.5 V	2.5 V	GND																			
			46																										
			47	GND	GND		GND	GND																					
			48	5.5 V	5.5 V		5.5 V	5.5 V																					
2																													
3																													
9 $T_C = 25^\circ\text{C}$	t_{PHL}	3003 Fig. 4	49	IN	2.7 V		2.7 V	2.7 V	OUT	GND																			
			50	2.7 V	IN		2.7 V																						
			51		2.7 V		IN																						
			52				2.7 V	IN																					
			53																										
			54																										
			55																										
			56																										
			57	IN	2.7 V		2.7 V	2.7 V	OUT																				
			58	2.7 V	IN		2.7 V																						
			59		2.7 V		IN																						
			60				2.7 V	IN																					
10	t_{PHL}		65 - 72																										
			73 - 80																										
11																													

1/ Pins not referenced (i.e., 1, 5, 7, 11, 15, and 17) are M/C.

2/ I_{IL} limits shall be as follows:

Test	Min/Max limits in mA
	A
	B
	C
I_{IL}	-25/-60
	-03/-60

4.5.1 Voltage and current. All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Notes. The notes specified in MIL-M-38510 are applicable to this specification.

6.2 Intended use. Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment.

6.3 Ordering data. The contract or purchase order should specify the following:

- a. Complete part number (see 1.2).
- b. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.
- c. Requirements for certificate of compliance, if applicable.
- d. Requirements for notification of change of product or process to the contracting activity in addition to notification to the qualifying activity, if applicable.
- e. Requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action, and reporting of results, if applicable.
- f. Requirements for product assurance options.
- g. Requirements for special carriers, lead lengths, or lead forming, if applicable. Unless otherwise specified, these requirements will not apply to direct purchase by or direct shipment to the Government.
- h. Requirements for "JAN" marking.

6.4 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-M-38510, MIL-STD-1331, and as follows:

GND	- - - - -	Electrical ground (common terminal)
V _{IN}	- - - - -	Voltage level at an input terminal
I _{IN}	- - - - -	Current flowing into an input terminal

6.5 Logistic support. Lead materials and finishes (see 3.3) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2) and lead finish C (see 3.3). Longer lengths and lead forming shall not affect the part number.

6.6 Substitutability. The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-35810 device types and may have slight physical variations in relation to case size. The presence of this information shall not be deemed as permitting substitution of generic-industry types for MIL-M-35810 types or as a waiver of any of the provisions of MIL-M-38510.

Military device
type

01
02
03
04

Generic-industry
type

54F00
54F04
54F10
54F20

6.7 Manufacturers' designation. Manufacturers' circuits which form a part of this specification are designated with an 'X' as shown in table IV herein.

TABLE IV. Manufacturers' designations.

Device type	Manufacturer		
	Circuit A	Circuit B	Circuit C
	Fairchild Semiconductor	Motorola Inc.	Signetics Corp.
01	X	X	
02	X	X	
03	X	X	
04	X	X	

6.8 Changes from previous issue. Asterisks are not used in this revision to identify changes with respect to the previous issue, due to the extensiveness of the changes.

Custodians:

Army - ER
Navy - EC
Air Force - 17

Preparing activity:
Air Force - 17

(Project 5962-0565)

Review activities:

Army - AR, MI
Navy - OS, SH
Air Force - 11, 19, 85, 99
DLA - ES

User activities:

Army - SM
Navy - AS, CG, MC

Agent:

DLA - ES