

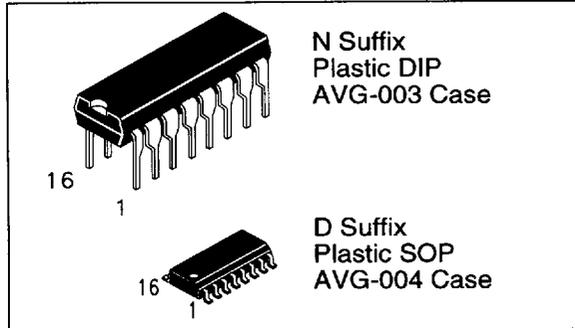
Available Q2, 1995

### DV74AC157, DV74ACT157 DV74AC158, DV74ACT158

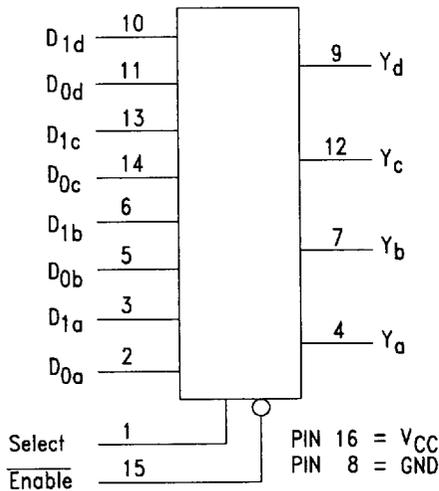
## Quad 2-Input Multiplexer

These devices are high speed, quad 2-input multiplexers with common select inputs and enable inputs for each section. It can select 4 bits of data from two sources. In addition to multiplexer operation, it can act as a function generator. The '157 has non-inverted outputs, the '158 has inverted outputs.

- Advanced very high speed CMOS
- Outputs source/sink 24 mA
- Transmission line driving 50 ohms
- ACT has TTL compatible inputs
- Operation from 2 to 6 volts guaranteed
- DC & AC Parameters guaranteed over -40 to +85°C



### LOGIC DIAGRAM



### PIN ASSIGNMENT

Select	1	16	V <sub>CC</sub>
D <sub>0a</sub>	2	15	Enable
D <sub>1a</sub>	3	14	D <sub>0c</sub>
Y <sub>a</sub>	4	13	D <sub>1c</sub>
D <sub>0b</sub>	5	12	Y <sub>c</sub>
D <sub>1b</sub>	6	11	D <sub>0d</sub>
Y <sub>b</sub>	7	10	D <sub>1d</sub>
GND	8	9	Y <sub>d</sub>

### TRUTH TABLE

Inputs				'157 Output	'158 Output
$\bar{E}$	S	D <sub>0</sub>	D <sub>1</sub>	Y	Y
H	X	X	X	L	H
L	H	X	L	L	H
L	H	X	H	H	L
L	L	L	X	L	H
L	L	H	X	H	L

H=HIGH Voltage Level  
L=LOW Voltage Level  
X=Don't Care

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### ABSOLUTE MAXIMUM RATINGS

Maximum ratings are those values beyond which damage to the device may occur.

Symbol	Parameter	AC157-158, ACT157-158	Unit
V <sub>CC</sub>	DC Supply Voltage (Referenced to GND)	- 0.5 to +7.0	V
V <sub>IN</sub>	DC Input Voltage (Referenced to GND)	- 0.5 to V <sub>CC</sub> +0.5	V
V <sub>OUT</sub>	DC Output Voltage (Referenced to GND)	- 0.5 to V <sub>CC</sub> +0.5	V
I <sub>IN</sub>	DC Input Current, per Pin	± 20	mA
I <sub>OUT</sub>	DC Output Sink/Source Current, per Pin	± 50	mA
I <sub>CC</sub>	DC V <sub>CC</sub> or GND Current per Output Pin	± 50	mA
T <sub>STG</sub>	Storage Temperature	- 65 to +150	°C

## GUARANTEED OPERATING CONDITIONS

Symbol	Parameter	Min	Typ	Max	Unit	
V <sub>CC</sub>	Supply Voltage	'AC	2.0	5.0	6.0	V
		'ACT	4.5	5.0	5.5	
V <sub>IN</sub> , V <sub>OUT</sub>	DC Input Voltage, Output Voltage, (Ref. to GND)	0		V <sub>CC</sub>	V	
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Time (Note 1) 'AC Devices	V <sub>CC</sub> @ 3.0 V			150	ns/V
		V <sub>CC</sub> @ 4.5 V			40	ns/V
		V <sub>CC</sub> @ 5.5 V			25	ns/V
t <sub>r</sub> , t <sub>f</sub>	Input Rise and Fall Time (Note 2) 'ACT Devices	V <sub>CC</sub> @ 4.5 V			10	ns/V
		V <sub>CC</sub> @ 5.5 V			8.0	ns/V
T <sub>A</sub>	Operating Ambient Temperature Range	-40		85	°C	
CPD	Power Dissipation Capacitance	V <sub>CC</sub> = 5.0 V		50	pF	
C <sub>IN</sub>	Input Capacitance V <sub>CC</sub> = 5.0 V	V <sub>CC</sub> = 5.0 V		4.5	pF	

1. V<sub>IN</sub> from 30% to 70% V<sub>CC</sub>

2. V<sub>IN</sub> from 0.8 to 2.0 V

## AC — 157,158

### DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	AC157,158			Unit	
				T <sub>A</sub> = +25°C		T <sub>A</sub> = -40 to +85°C		
				Typ	Guaranteed Limits			
V <sub>IH</sub>	Minimum High Level Input Voltage	V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> - 0.1 V	3.0	1.5	2.1	2.1	V	
			4.5	2.25	3.15	3.15		
			5.5	2.75	3.85	3.85		
V <sub>IL</sub>	Maximum Low Level Input Voltage	V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> - 0.1 V	3.0	1.5	0.9	0.9	V	
			4.5	2.25	1.35	1.35		
			5.5	2.75	1.65	1.65		
V <sub>OH</sub>	Minimum High Level Output Voltage	I <sub>OUT</sub> = -50 μA	3.0	2.99	2.9	2.9	V	
			4.5	4.49	4.4	4.4		
			5.5	5.49	5.4	5.4		
		V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>	-12mA	3.0		2.56	2.46	V
			I <sub>OH</sub> -24mA	4.5		3.86	3.76	
	-24 mA	5.5		4.86	4.76			
V <sub>OL</sub>	MaximumLow Level Output Voltage	I <sub>OUT</sub> = 50 μA	3.0	0.002	0.1	0.1	V	
			4.5	0.001	0.1	0.1		
			5.5	0.001	0.1	0.1		
		V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>	12mA	3.0		0.36	0.44	V
			I <sub>OH</sub> 24mA	4.5		0.36	0.44	
	24 mA	5.5		0.36	0.44			
I <sub>IN</sub>	Maximum Input Leakage Current	V <sub>I</sub> =V <sub>CC</sub> , GND	5.5		±0.1	±1.0	μA	
I <sub>CC</sub>	Maximum Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	5.5		8.0	80	μA	

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**AC CHARACTERISTICS** (\*Voltage Range 3.3 V is 3.3 V ± 0.3 V; Voltage Range 5.0 V is 5.0 V ± 0.5 V)

Symbol	Parameter ( $C_L = 50$ pF)	V <sub>CC</sub> (V)	AC157				Unit
			T <sub>A</sub> = +25°C		T <sub>A</sub> = -40°C to +85°C		
			Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay S to Y <sub>n</sub>	3.3	1.5	11.5	1.5	13.0	ns
t <sub>PHL</sub>		5.0	1.5	9.0	1.5	10.0	
t <sub>PLH</sub>	Propagation Delay E to Y <sub>n</sub>	3.3	1.5	11.0	1.5	12	ns
t <sub>PHL</sub>		5.0	1.5	8.5	1.0	9.5	
t <sub>PLH</sub>	Propagation Delay D <sub>n</sub> to Y <sub>n</sub>	3.3	1.5	8.5	1.0	9.0	ns
t <sub>PHL</sub>		5.0	1.5	6.5	1.0	7.0	

Symbol	Parameter ( $C_L = 50$ pF)	V <sub>CC</sub> ±10% (V)	AC158				Unit
			T <sub>A</sub> = +25°C		T <sub>A</sub> = -40°C to +85°C		
			Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay S to Y <sub>n</sub>	3.3	1.5	11.5	1.5	12.5	ns
t <sub>PHL</sub>		5.0	1.5	9.0	1.0	9.5	
t <sub>PLH</sub>	Propagation Delay E to Y <sub>n</sub>	3.3	1.5	11.5	1.5	12.5	ns
t <sub>PHL</sub>		5.0	1.5	9.0	1.0	10.0	
t <sub>PLH</sub>	Propagation Delay D <sub>n</sub> to Y <sub>n</sub>	3.3	1.5	12.0	1.5	13.0	ns
t <sub>PHL</sub>		5.0	1.5	9.5	1.5	10.5	
t <sub>PLH</sub>	Propagation Delay D <sub>n</sub> to Y <sub>n</sub>	3.3	1.5	11.0	1.5	12.0	ns
t <sub>PHL</sub>		5.0	1.5	8.5	1.0	9.5	
t <sub>PLH</sub>	Propagation Delay D <sub>n</sub> to Y <sub>n</sub>	3.3	1.5	9.0	1.5	10.0	ns
t <sub>PHL</sub>		5.0	1.5	7.0	1.5	7.5	
t <sub>PLH</sub>	Propagation Delay D <sub>n</sub> to Y <sub>n</sub>	3.3	1.5	8.0	1.0	8.5	ns
t <sub>PHL</sub>		5.0	1.5	6.5	1.0	6.5	

**ACT — 157, 158**

**DC ELECTRICAL CHARACTERISTICS**

Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	ACT157, ACT158			Unit
				T <sub>A</sub> = +25°C		T <sub>A</sub> = -40 to +85°C	
				Typ	Guaranteed Limits		
V <sub>IH</sub>	Minimum High Level Input Voltage	V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> - 0.1 V	4.5	1.5	2.0	2.0	V
			5.5	1.5	2.0	2.0	
V <sub>IL</sub>	Maximum Low Level Input Voltage	V <sub>OUT</sub> = 0.1V or V <sub>CC</sub> - 0.1 V	4.5	1.5	0.8	0.8	V
			5.5	1.5	0.8	0.8	
V <sub>OH</sub>	Minimum High Level Output Voltage	I <sub>OUT</sub> = -50 μA	4.5	4.49	4.4	4.4	V
			5.5	5.49	5.4	5.4	
		V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> I <sub>OH</sub> = -24mA -24 mA	4.5		3.86	3.76	V
			5.5		4.86	4.76	
V <sub>OL</sub>	Maximum Low Level Output Voltage	I <sub>OUT</sub> = 50 μA	4.5	0.001	0.1	0.1	V
			5.5	0.001	0.1	0.1	
		V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> I <sub>OL</sub> = 24mA 24 mA	4.5		0.36	0.44	V
			5.5		0.36	0.44	
I <sub>IN</sub>	Maximum Input Leakage Current	V <sub>I</sub> = V <sub>CC</sub> , GND	5.5		±0.1	±1.0	μA
ΔI <sub>CC</sub> T	Additional Max I <sub>CC</sub> /Input	V <sub>I</sub> = V <sub>CC</sub> - 2.1 V	5.5	0.6		1.5	mA

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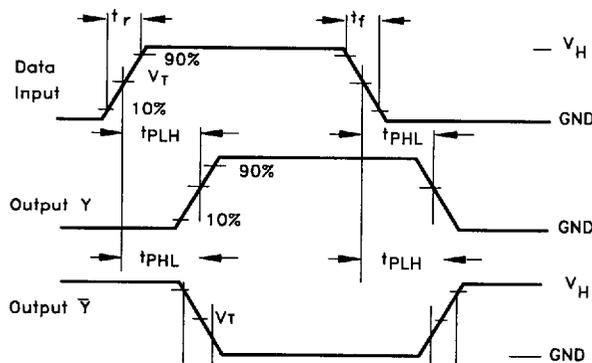
Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	ACT157, ACT158		Unit	
				T <sub>A</sub> = +25°C			T <sub>A</sub> = -40 to +85°C
				Typ	Guaranteed Limits		
I <sub>CC</sub>	Maximum Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	5.5		8.0    80	μA	

### AC CHARACTERISTICS

Symbol	Parameter (C <sub>L</sub> = 50 pF)	V <sub>CC</sub> ±10% (V)	ACT157				Unit
			T <sub>A</sub> = +25°C		T <sub>A</sub> = -40°C to +85°C		
			Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay, S <sub>n</sub> to Y <sub>n</sub>	5.0	2.0	9.0	1.5	10	ns
t <sub>PHL</sub>	Propagation Delay, S <sub>n</sub> to Y <sub>n</sub>	5.0	2.0	9.5	2.0	10.5	ns
t <sub>PLH</sub>	Propagation Delay, $\bar{E}_n$ to Y <sub>n</sub>	5.0	1.5	10	1.5	11.5	ns
t <sub>PHL</sub>	Propagation Delay, $\bar{E}_n$ to Y <sub>n</sub>	5.0	1.5	8.5	1.0	9.0	ns
t <sub>PLH</sub>	Propagation Delay, D <sub>n</sub> to Y <sub>n</sub>	5.0	1.5	7.0	1.0	8.5	ns
t <sub>PHL</sub>	Propagation Delay, D <sub>n</sub> to Y <sub>n</sub>	5.0	1.5	7.5	1.0	8.5	ns

Symbol	Parameter (C <sub>L</sub> = 50 pF)	V <sub>CC</sub> (V)	ACT158				Unit
			T <sub>A</sub> = +25°C		T <sub>A</sub> = -40°C to +85°C		
			Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay, Select to Y <sub>n</sub>	5.0	2.5	9.5	2.0	11.0	ns
t <sub>PHL</sub>	Propagation Delay, Select to Y <sub>n</sub>	5.0	1.5	9.0	1.5	10.0	ns
t <sub>PLH</sub>	Propagation Delay, $\bar{E}_n$ to Y <sub>n</sub>	5.0	1.5	9.5	1.5	10.5	ns
t <sub>PHL</sub>	Propagation Delay, $\bar{E}_n$ to Y <sub>n</sub>	5.0	1.5	5.5	1.5	10.5	ns
t <sub>PLH</sub>	Propagation Delay, D <sub>n</sub> to Y <sub>n</sub>	5.0	1.5	4.5	1.0	8.5	ns
t <sub>PHL</sub>	Propagation Delay, D <sub>n</sub> to Y <sub>n</sub>	5.0	1.5	6.5	1.0	7.5	ns

### SWITCHING WAVEFORMS



Input and output threshold voltage:  
 $V_T = 50\% V_{CC}$  for AC; 1.5V for ACT  
 $V_H = V_{CC}$  for AC, 3V for ACT

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