

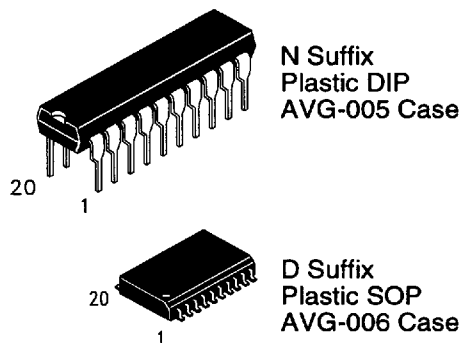
Octal 3-State Noninverting D-Type Flip-Flop

Pinouts for the 'HC574A and 'HCT574A are identical to the LS574. The 'HC 574A Inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs. The 'HCT574A is particularly suited as a level convertor interfacing TTL or NMOS outputs to High-Speed CMOS inputs. Both devices are similar in function to the 'HC374/'HCT374 but have the flip-flop inputs on the opposite side of the package from the outputs to facilitate PC board layout.

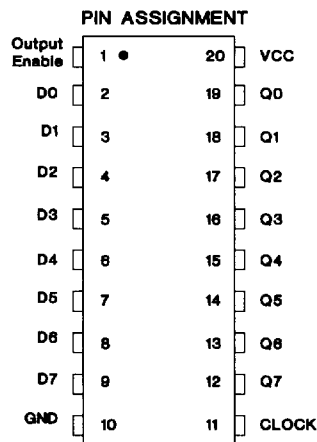
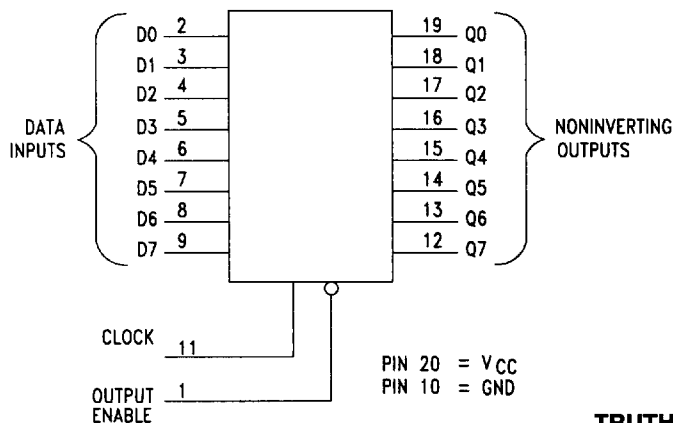
Data meeting the setup time is clocked to the outputs with the rising edge of the Clock. The Output Enable input does not affect the states of the flip-flops, but when the Output Enable is high, the outputs are forced to the high impedance state, thus, data may be stored even when the outputs are not enabled.

- Output Drive Capability: 15 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 2 to 6 V
- Low Input Current: 1 μ A
- DC, AC parameters guaranteed from -55°C to 125°C

DV74HC574A DV74HCT574A



574A



TRUTH TABLE
(Each Flip-Flop)

Inputs			Output
Output Enable	Clock	D	Q
L	$\uparrow$	H	H
L	$\uparrow$	L	L
L	L,H, $\downarrow$	X	no change
H	X	X	Z

$\uparrow$ = Low to High Edge Transition

$\downarrow$ = High to Low Edge Transition

Z = High Impedence

X = Don't Care

ABSOLUTE MAXIMUM RATINGS

Maximum ratings are those values beyond which damage to the device may occur.

Symbol	Parameter	Value	Unit
V _{CC}	DC Supply Voltage (Referenced to GND)	− 0.5 to +7.0	V
V _{IN}	DC Input Voltage (Referenced to GND)	− 1.5 to V _{CC} +1.5	V
V _{OUT}	DC Output Voltage (Referenced to GND)	− 0.5 to V _{CC} +0.5	V
I _{IN}	DC Input Current, per Pin	± 20	mA
I _{OUT}	DC Output Sink/Source Current, per Pin	± 35	mA
I _{CC}	DC V _{CC} or GND Current Pin	± 75	mA
P _D	Power Dissipation in Still Air Plastic DIP SOP Package	750 500	mW
T _{stg}	Storage Temperature	− 65 to +150	°C
T _L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C

GUARANTEED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V _{CC}	DC Supply Voltage (Referenced to GND)	2.0	6.0	V
V _{IN} , V _{OUT}	DC Input Voltage, Output Voltage (Referenced to GND)	0	V _{CC}	V
T _A	Ambient Temperature	−55	+125	°C
t _r , t _f	Input Rise and Fall Time: HC: V _{CC} =2.0V HCT: V _{CC} =5.5V / HC: V _{CC} =4.5V HC: V _{CC} =6.0V	0 0 0	1000 500 400	ns

HC — 574A

ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V _{CC} (V)	Guaranteed Limits			Unit
				25°C to −55°C	≤ 85°C	≤ 125°C	
V _{IH}	Minimum High Level Input Voltage	V _{OUT} = V _{CC} − 0.1 V I _{OUT} ≤ 20 μA	2.0	1.50	1.50	1.50	V
			4.5	3.15	3.15	3.15	
			6.0	4.20	4.20	4.20	
V _{IL}	Maximum Low Level Input Voltage	V _{OUT} = 0.1V I _{OUT} ≤ 20 μA	2.0	0.50	0.50	0.50	V
			4.5	1.35	1.35	1.35	
			6.0	1.80	1.80	1.80	
V _{OH}	Minimum High Level Output Voltage	V _{IN} = V _{IH} I _{OUT} ≤ 20 μA	2.0	1.90	1.90	1.90	V
			4.5	4.40	4.40	4.40	
			6.0	5.90	5.90	5.90	
V _{OL}	Maximum Low Level Output Voltage	V _{IN} = V _{IH} I _{OUT} ≤ 6.0 mA I _{OUT} ≤ 7.8 mA	4.5	3.98	3.84	3.70	V
			6.0	5.48	5.34	5.20	
V _{OL}	Maximum Low Level Output Voltage	V _{IN} = V _{IL} I _{OUT} ≤ 20 μA	2.0	0.10	0.10	0.10	V
			4.5	0.10	0.10	0.10	
			6.0	0.10	0.10	0.10	
V _{OL}	Maximum Low Level Output Voltage	V _{IN} = V _{IL} I _{OUT} ≤ 6.0 mA I _{OUT} ≤ 7.8 mA	4.5	0.26	0.33	0.40	V
			6.0	0.26	0.33	0.40	
I _{IN}	Maximum Input Leakage Current	V _{IN} = V _{CC} or GND	6.0	±0.1	±1.00	±1.00	μA
I _{OZ}	Maximum 3-State Current (Output in High Impedance State)	V _{IN} = V _{IL} or V _{IH} V _{OUT} = V _{CC} or GND	6.0	±0.5	±5.0	±10.0	mA
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{CC} or GND I _{OUT} = 0 μA	6.0	4	40	160	μA

AC CHARACTERISTICS over full operating conditions ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

Symbol	Parameter	V _{CC} (V)	Guaranteed Limits			Unit
			+25°C to -55°C	≤ 85°C	≤ 125°C	
f _{MAX}	Maximum Clock Frequency (50% Duty Cycle)	2.0 4.5 6.0	6.0 30 35	4.8 24 28	4.0 20 24	MHz
t _{PLH} , t _{PHL}	Maximum Propagation Delay Clock to Q	2.0 4.5 6.0	160 32 27	200 40 34	240 48 41	ns
t _{PLZ} , t _{PHZ}	Maximum Propagation Delay Time, Output Disable to Q	2.0 4.5 6.0	150 30 26	190 38 33	225 45 38	ns
t _{PZL} , t _{PZH}	Maximum Propagation Delay Time, Output Enable to Q	2.0 4.5 6.0	140 28 24	175 35 30	210 42 36	ns
t _{TLH} , t _{THL}	Maximum Output Transition Time, any Output	2.0 4.5 6.0	60 12 10	75 15 13	90 18 15	ns
C _{IN}	Maximum Input Capacitance	—	10	10	10	pF
C _{OUT}	Maximum Three-State Output Capacitance (Output in High-Impedance State)	—	15	15	15	pF

C _{PD}	Power Dissipation Capacitance (Per Buffer) Used to determine the no-load dynamic power consumption: $P_D = C_{PD} V_{CC}^2 f + I_{CC} V_{CC}$	Typical @ 25°C, V _{CC} = 5.0 V	pF
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TIMING REQUIREMENTS ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6.0 \text{ ns}$)

Symbol	Parameter	Vcc (V)	Guaranteed Limits						Unit
			25°C to -55°C		≤ 85°C		≤ 125°C		
			Min	Max	Min	Max	Min	Max	
t _{su}	Minimum Setup Time, Data to Clock	2.0	50		65		75		ns
		4.5	10		13		15		
		6.0	9		11		13		
t _h	Minimum Hold Time, Clock to Data	2.0	5		5		5		ns
		4.5	5		5		5		
		6.0	5		5		5		
t _w	Minimum Pulse Width, Clock	2.0	75		95		110		ns
		4.5	15		19		22		
		6.0	13		16		19		

HCT — 574 A**DC ELECTRICAL CHARACTERISTICS**

Symbol	Parameter	Conditions	V _{CC} (V)	Guaranteed Limits			Unit
				+25°C to -55°C	≤ 85°C	≤ 125°C	
V _{IH}	Minimum High Level Input Voltage	V _{OUT} = 0.1V or V _{CC} - 0.1 I _{OUT} = ≤20 μA	4.5 5.5	2.0 2.0	2.0 2.0	2.0 2.0	V
V _{IL}	Maximum Low Level Input Voltage	V _{OUT} = 0.1V or V _{CC} - 0.1 V I _{OUT} = ≤20 μA	4.5 5.5	0.8 0.8	0.8 0.8	0.8 0.8	V
V _{OH}	Minimum High Level Output Voltage	V _{IN} = V _{IL} or V _{IH} I _{OUT} ≤ 20 μA	4.5 5.5	4.4 5.4	4.4 5.4	4.4 5.4	V
		V _{IN} = V _{IL} or V _{IH} I _{OUT} ≤ 6 mA	4.5	3.98	3.84	3.7	V

Symbol	Parameter	Conditions	V _{CC} (V)	Guaranteed Limits			Unit
				+25°C to -55°C	< 85°C	≤ 125°C	
V _{OL}	Maximum Low Level Output Voltage	V _{IN} = V _{IL} or V _{IH} I _{OUT} ≤ 20 μA	4.5 5.5	0.1 0.1	0.1 0.1	0.1 0.1	V
		V _{IN} = V _{IL} or V _{IH} I _{OUT} ≤ 6 mA	4.5	0.26	0.33	0.4	V
I _{IN}	Maximum Input Leakage Current	V _{IN} =V _{CC} or GND	5.5	±0.1	±1.0	±1.0	μA
I _{OZ}	Maximum 3-State Current (Output in High Impedance State)	V _{IN} =V _{IL} or V _{IH} V _{OUT} =V _{CC} or GND	5.5	-0.5	-5.0	-10.0	μA
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{CC} or GND I _{OUT} = 0 μA	5.5	4.0	40.0	160	μA

ΔI _{CC}	Additional Quiescent Supply Current (per Package) V _{IN} =2.4 V, Any One Input; V _{IN} = V _{CC} or GND, Other Inputs; I _{OUT} = 0 μA	5.5	> - 55°C	25°C to 125°C	mA
			2.9	2.4	

AC CHARACTERISTICS over full operating conditions (V_{CC} = 5.0 V ±10%, Input t_r = t_f = 6.0 ns

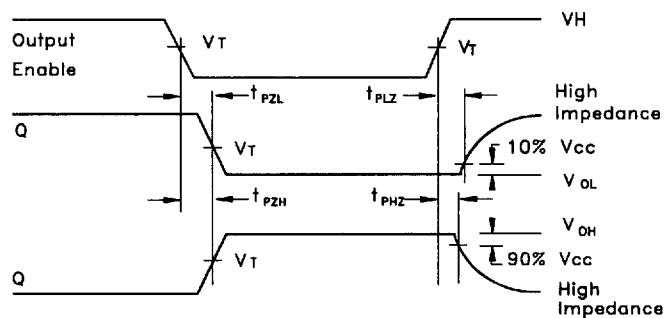
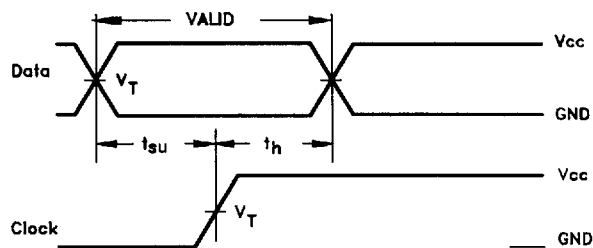
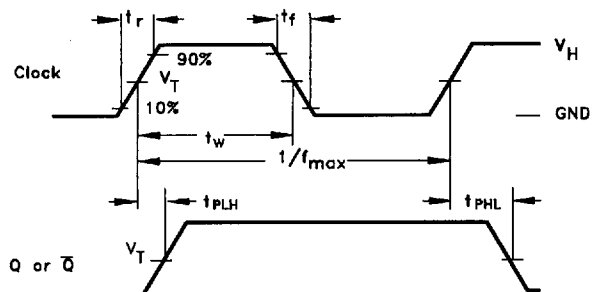
Symbol	Parameter	Guaranteed Limits			Unit
		25°C to -55°C	≤ 85°C	≤ 125°C	
f _{MAX}	Maximum Clock Frequency (50% Duty Cycle)	30	24	20	MHz
t _{PLH} , t _{PHL}	Maximum Propagation Delay Clock to Q	30	38	45	ns
t _{PLZ} , t _{PHZ}	Maximum Propagation Delay Time, Output Enable to Q	28	35	42	ns
t _{PZH} , t _{PZL}	Maximum Propagation Delay Time, Output Enable to Q	28	35	42	ns
t _{TLH} , t _{THL}	Maximum Output Transition Time, any Output	12	15	18	ns
C _{IN}	Maximum Input Capacitance	10	10	10	pF

C _{PD}	Power Dissipation Capacitance (Per Buffer) Used to determine the no-load dynamic power consumption: P _D = C _{PD} V _{CC} ^{2f} + I _{CC} V _{CC}	Typical @ 25°C, V _{CC} = 5.0 V	pF
		58	

TIMING REQUIREMENTS (V_{CC} = 5.0 V ±10%, Input t_r = t_f = 6.0 ns.

Symbol	Parameter	Guaranteed Limits						Unit
		25°C to -55°C		≤ 85°C		≤ 125°C		
		Min	Max	Min	Max	Min	Max	
t _{su}	Minimum Setup Time, Data to Clock	10.0		13.0		15.0		ns
t _h	Minimum Hold Time, Clock to Data	5.0		5.0		5.0		ns
t _w	Minimum Pulse Width, Clock	15.0		19.0		22.0		ns

SWITCHING WAVE FORMS



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