

## HIGH RELIABILITY HYBRID DC-DC CONVERTERS

#### **DESCRIPTION**

The DVFL series of high reliability DC-DC converters is operable over the full military (-55 °C to +125 °C) temperature range with no power derating. Unique to the DVFL series is a magnetic feedback circuit that is radiation immune. Operating at a nominal fixed frequency of 500 kHz, these regulated, isolated units utilize well-controlled undervoltage lockout circuitry to eliminate slow start-up problems. The current sharing function allows a maximum of five units to be connected in parallel to boost the total output power to 5 times. The output voltage is trimmable up to +10% or down –20%.

These converters are designed and manufactured in a facility qualified to ISO9001, compliant to AS9000, and certified to MIL-PRF-38534 and MIL-STD-883.

#### **FEATURES**

- High Reliability
- Parallel Up to 5 Units With Current Sharing
- Output Voltage Trim Up +10% or Down -20%
- Wide Input Voltage Range: 16 to 40 Volts per MIL-STD-704
- Up to 120 Watts Output Power
- Radiation Immune Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Industry Standard Pinout
- Input Transient Voltage: 50 Volts for 1 second
- Radiation Hardened Version Available
- Precision Seam Welded Hermetic Package
- High Power Density: > 80 W/in<sup>3</sup>
- Custom Versions Available
- Additional Environmental Screening Available
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements When Used With a DVME28 EMI Filter
- MIL-PRF-38534 Element Evaluated Components

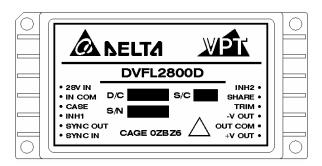


Figure 1 – DVFL2800D DC-DC Converter (Not To Scale)





**SPECIFICATIONS** ( $T_{CASE} = -55^{\circ}C$  to  $+125^{\circ}C$ ,  $V_{IN} = +28V \pm 5\%$ , Full Load<sup>5</sup>, Unless Otherwise Specified)

#### **ABSOLUTE MAXIMUM RATINGS**

Junction Temperature Rise to Case +15°C
Storage Temperature -65°C to +150°C

Lead Solder Temperature (10 seconds) Weight

270°C 100 grams

Darameter		Conditions	ı	DVFL2805[	)	ı	DVFL2812E	)	Unito
Parameter		Conditions	Min	Тур	Max	Min	Тур	Max	Units
STATIC									
INPUT		Continuous	16	28	40	16	28	40	V
Voltage⁴	Voltage <sup>4</sup>		-	-	50	-	-	50	V
		Inhibited 1	-	-	3	-	-	3	mA
Current		Inhibited 2	-	-	70	-	-	70	mA
		No Load	-	-	140	-	-	140	mA
Ripple Current		Full Load <sup>5</sup> , 20Hz to 10MHz	-	-	80	-	-	80	mA <sub>p-p</sub>
INH1 Pin Input <sup>4</sup>			0	-	1.5	0	-	1.5	V
INH2 Pin Input <sup>4</sup>			0	-	1.0	0	-	1.0	V
INH1 Pin Open Circuit V	oltage <sup>4</sup>		10.5	-	13.5	10.5	-	13.5	V
INH2 Pin Open Circuit V	oltage <sup>4</sup>		5.0	-	8.0	5.0	-	8.0	V
UVLO Turn On			14.0	-	16.0	14.0	-	16.0	V
UVLO Turn Off <sup>4</sup>			11.0	-	14.5	11.0	-	14.5	V
	+V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	4.95	5.00	5.05	11.88	12.00	12.12	V
OUTPUT	$+V_{OUT}$	T <sub>CASE</sub> = -55°C to +125°C	4.925	5.00	5.075	11.82	12.00	12.18	V
Voltage <sup>5</sup>	$-V_{OUT}$	T <sub>CASE</sub> = 25°C	4.80	5.00	5.20	11.80	12.00	12.20	V
-V	-V <sub>OUT</sub>	T <sub>CASE</sub> = -55°C to +125°C	4.75	5.00	5.25	11.52	12.00	12.48	V
Power <sup>3,6</sup>	Total		-	-	100	-	-	110	W
±V <sub>C</sub>		Either Output	-	-	70	-	-	77	W
Current <sup>3,6</sup>	±V <sub>OUT</sub>	Either Output	-	-	14	-	-	6.4	Α
Ripple Voltage	$\pm V_{\text{OUT}}$	Full Load <sup>5</sup> , 20Hz to 10MHz	-	-	80	-	-	80	$mV_{p-p}$
Line Degulation	+V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	-	20	-	-	20	mV
Line Regulation	$-V_{OUT}$	V <sub>IN</sub> = 16V to 40V	-	-	200	-	-	200	mV
Load Regulation +V <sub>OUT</sub>		No Load to Full Load⁵	-	-	100	-	-	120	mV
		No Load to Full Load⁵	-	-	200	-	-	200	mV
Cross Regulation	-V <sub>OUT</sub>	V1+ Load 30% - Load 70% V2+ Load 70% - Load 30%	-	-	450	-	-	450	mV
Voltage Trim		Full Load	-20	-	10	-20	-	10	%
Share Pin Voltage <sup>4</sup>			2.0	-	3.0	2.0	-	3.0	V
EFFICIENCY		Full Load⁵	73	-	-	80	-	-	%
LOAD FALL T DOWED DICC	NDATION	Overload <sup>4</sup>	-	-	80	-	-	80	W
LOAD FAULT POWER DISS	SIPATION	Short Circuit	-	-	80	-	-	80	W
CAPACITIVE LOAD4	CAPACITIVE LOAD⁴		-	-	500	-	-	500	μF
SWITCHING FREQUENCY			450	500	600	450	500	600	kHz
SYNC FREQUENCY RANG	E	V <sub>H</sub> – V <sub>L</sub> = 5V Duty Cycle = 20% - 80%	500	-	600	500	-	600	kHz
ISOLATION		500 V <sub>DC</sub> , T <sub>CASE</sub> = 25°C	100	-	-	100	-	-	ΜΩ
THERMAL RESISTANCE		Case to Ambient (θCA)	-	12	-	-	12	-	°C/W
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	400	-	-	400	1	kHrs



**SPECIFICATIONS** (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +28V ± 5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

### **ABSOLUTE MAXIMUM RATINGS**

Input Voltage (Continuous)  $40 V_{DC}$ Junction Temperature Rise to Case +15°C -65°C to +150°C Input Voltage (Transient, 1 second) 50 Volts Storage Temperature Output Power<sup>1</sup> 120 Watts Lead Solder Temperature (10 seconds) 270°C Power Dissipation (Full Load,  $T_{CASE} = +125^{\circ}C$ ) 40 Watts Weight 100 grams

Parameter		Conditions	DVFL2805D		)	ı	Units		
		Conditions	Min	Min Typ Max		Min	Тур	Max	Offics
DYNAMIC									
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	400	-	-	500	$mV_{PK}$
Load Step Recovery <sup>2</sup>		Tiali Load to Full Load	-	-	500	-	-	500	μSec
Line Step Output Transient4	±V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	300	600	-	600	1200	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>	_	V <sub>IN</sub> = 16V to 40V	-	300	500	-	300	500	μSec
Turn On Delay	±V <sub>OUT</sub>	V <sub>IN</sub> = 0V to 28V	-	-	20	-	-	20	mSec
Turn On Overshoot <sup>2</sup>		V <sub>IN</sub> - UV (U 20V	-	-	25	-	-	25	$mV_{PK}$

Notes: 1. Dependant on output voltage.

2. Time for output voltage to settle within 1% of its nominal value.

3. Derate linearly to 0 at 135°C.

4. Verified by qualification testing.

5. Half load at  $+V_{OUT}$  and half load at  $-V_{OUT}$ .

6. Up to 70% of the total power or current can be drawn from any one of the two outputs.



**SPECIFICATIONS** ( $T_{CASE} = -55^{\circ}C$  to  $+125^{\circ}C$ ,  $V_{IN} = +28V \pm 5\%$ , Full Load<sup>5</sup>, Unless Otherwise Specified)

#### **ABSOLUTE MAXIMUM RATINGS**

 $\begin{array}{ll} \mbox{Input Voltage (Continuous)} & 40 \ \mbox{V}_{DC} \\ \mbox{Input Voltage (Transient, 1 second)} & 50 \ \mbox{Volts} \\ \mbox{Output Power}^1 & 120 \ \mbox{Watts} \\ \mbox{Power Dissipation (Full Load, $T_{CASE} = +125^{\circ}C)} & 40 \ \mbox{Watts} \\ \end{array}$ 

Junction Temperature Rise to Case Storage Temperature

Lead Solder Temperature (10 seconds) Weight +15°C -65°C to +150°C

270°C 100 grams

Danamatan		Conditions		DVFL2815[	)	Units	
Parameter	i didiliotoi		Min	Тур	Max	Units	
STATIC							
INPUT		Continuous	16	28	40	V	
Voltage <sup>4</sup>		Transient, 1 sec	-	-	50	V	
		Inhibited 1	-	-	3	mA	
Current		Inhibited 2	-	-	70	mA	
		No Load	-	-	140	mA	
Ripple Current		Full Load <sup>5</sup> , 20Hz to 10MHz	-	-	120	$mA_{p-p}$	
INH1 Pin Input⁴			0	-	1.5	V	
INH2 Pin Input⁴			0	-	1.0	V	
INH1 Pin Open Circuit V	oltage <sup>4</sup>		10.5	-	13.5	٧	
INH2 Pin Open Circuit V	oltage <sup>4</sup>		5.0	1	8.0	<b>V</b>	
UVLO Turn On			14.5	-	16.0	V	
UVLO Turn Off <sup>4</sup>			11.0	-	14.5	V	
	+V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	14.85	15.00	15.15	V	
OUTPUT	$+V_{OUT}$	T <sub>CASE</sub> = -55°C to +125°C	14.775	15.00	15.225	V	
Voltage⁵	$-V_{OUT}$	T <sub>CASE</sub> = 25°C	14.80	15.00	15.20	V	
	$-V_{OUT}$	T <sub>CASE</sub> = -55°C to +125°C	14.40	15.00	15.60	V	
Power <sup>3,6</sup>	Total		-	-	120	W	
	$\pm V_{\text{OUT}}$	Either Output	-	ı	84	W	
Current <sup>3,6</sup>	±V <sub>OUT</sub>	Either Output	-		5.6	Α	
Ripple Voltage	$\pm V_{OUT}$	Full Load <sup>5</sup> , 20Hz to 10MHz	-	ı	80	$mV_{p-p}$	
Line Demulation	+V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-		20	mV	
Line Regulation	$-V_{OUT}$	V <sub>IN</sub> = 16V to 40V	-	ı	200	mV	
+\		No Load to Full Load⁵	-	-	120	mV	
Load Regulation	-V <sub>OUT</sub>	No Load to Full Load⁵	-	-	200	mV	
Cross Regulation	-V <sub>OUT</sub>	V1+ Load 30% - Load 70% V2+ Load 70% - Load 30%	-	-	450	mV	
Voltage Trim		Full Load	-20	1	10	%	
Share Pin Voltage <sup>4</sup>			2.0	-	3.0	٧	
EFFICIENCY		Full Load⁵	81	-	-	%	
LOAD FALL T DOLLED DIO	UDATION	Overload <sup>4</sup>	-	-	80	W	
LOAD FAULT POWER DISS	IPATION	Short Circuit	-	-	80	W	
CAPACITIVE LOAD⁴			-	-	500	μF	
SWITCHING FREQUENCY			450	500	600	kHz	
SYNC FREQUENCY RANGE	E	$V_H - V_L = 5V$ Duty Cycle = 20% - 80%	500	-	600	kHz	
ISOLATION		500 V <sub>DC</sub> , T <sub>CASE</sub> = 25°C	100	-	-	ΜΩ	
THERMAL RESISTANCE		Case to Ambient (θCA)	-	12	-	°C/W	
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	400	-	kHrs	



**SPECIFICATIONS** ( $T_{CASE}$  = -55°C to +125°C,  $V_{IN}$  = +28V ± 5%, Full Load<sup>5</sup>, Unless Otherwise Specified)

ABSOLUTE MAXIMUM RATINGS						
Input Voltage (Continuous)	40 V <sub>DC</sub>	Junction Temperature Rise to Case	+15°C			
Input Voltage (Transient, 1 second)	50 Volts	Storage Temperature	-65°C to +150°C			
Output Power <sup>1</sup>	120 Watts	Lead Solder Temperature (10 seconds)	270°C			
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	40 Watts	Weight	100 grams			

Parameter		Conditions		DVFL2815[	Units	
		Conditions	Min	Тур	Max	Offics
DYNAMIC						
Load Step Output Transient	$\pm V_{\text{OUT}}$	Half Load to Full Load	-	-	500	$mV_{PK}$
Load Step Recovery <sup>2</sup>		Tiali Load to Full Load	-	-	500	μSec
Line Step Output Transient <sup>4</sup>	±V <sub>OUT</sub>	V <sub>IN</sub> = 16V to 40V	-	600	1200	$mV_{PK}$
Line Step Recovery <sup>2, 4</sup>		V <sub>IN</sub> - 10V to 40V	-	300	500	μSec
Turn On Delay	±V <sub>OUT</sub>	\/ = 0\/ to 29\/	-	-	20	mSec
Turn On Overshoot <sup>2</sup>		$V_{IN} = 0V \text{ to } 28V$	-	-	50	$mV_{PK}$

Notes: 1. Dependant on output voltage.

2. Time for output voltage to settle within 1% of its nominal value.

3. Derate linearly to 0 at 135°C.

4. Verified by qualification testing.

5. Half load at +V<sub>OUT</sub> and half load at -V<sub>OUT</sub>.
6. Up to 70% of the total power or current can be drawn from any one of the two outputs.



#### **BLOCK DIAGRAM**

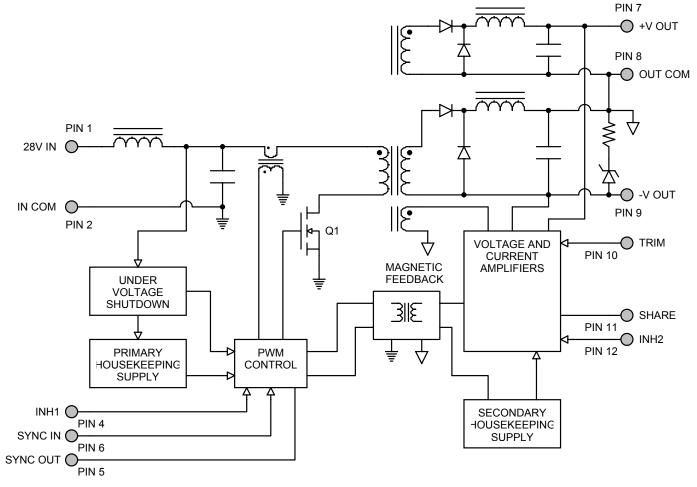


Figure 2

#### **CONNECTION DIAGRAM**

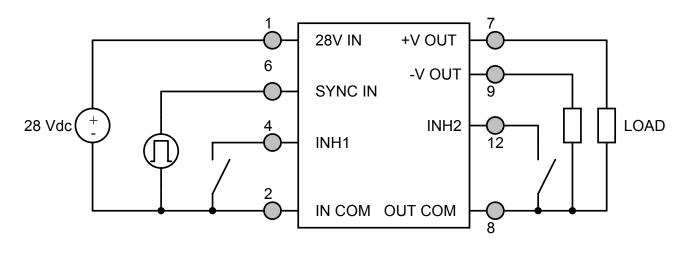
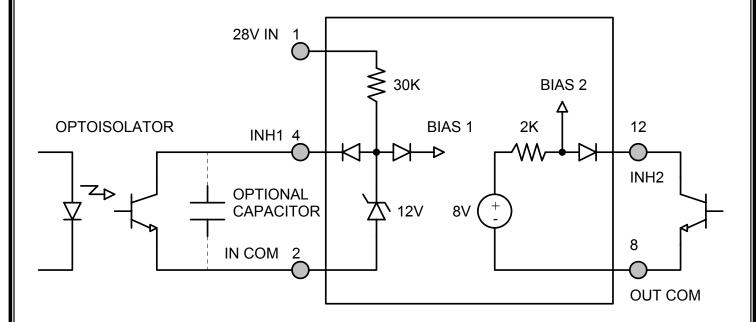


Figure 3

7



#### **INHIBIT DRIVE CONNECTION DIAGRAM**



**Figure 4** – Isolated Inhibit Drive and Internal Equivalent Circuit (Shown with optional capacitor for turn-on delay)

#### **EMI FILTER HOOKUP DIAGRAM**

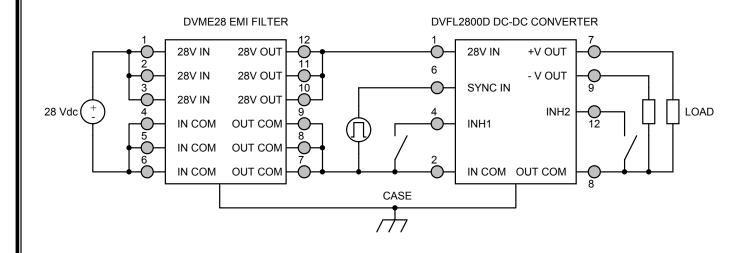


Figure 5 – Converter with EMI Filter



#### +28 VOLT OUTPUT CONNECTION DIAGRAM

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Figure 6: +28 Volt Output Converter Using DVFL2815D Converter



#### **PARALLEL CONNECTION DIAGRAM**

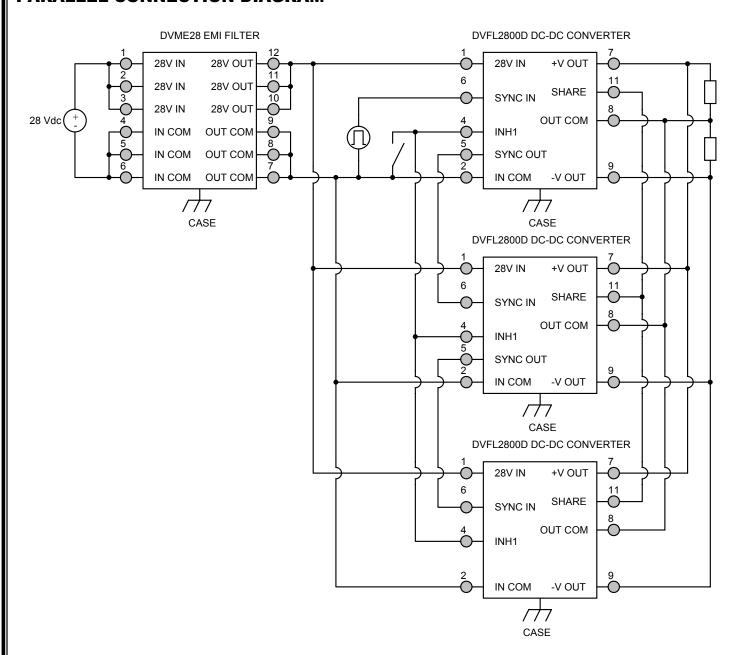
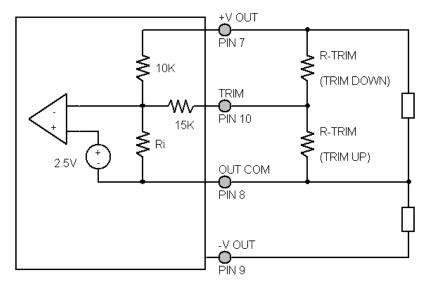


Figure 7 – Current Sharing Parallel Connection for Multiple Converters



#### **OUTPUT VOLTAGE TRIM**



The output voltage can be trimmed down by connecting a resistor between the TRIM pin (PIN 10) and the +V OUT pin (PIN 7), or can be trimmed up by connecting a resistor between the TRIM pin (PIN 10) and the OUT COM pin (PIN 8). The maximum trim range is +10% up and -20% down. The appropriate resistor values versus the output voltage are given in the trim table below.

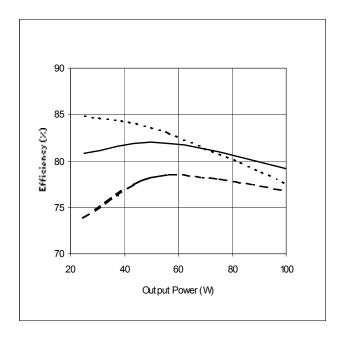
Figure 8 - Output Voltage Trim

DVFL2805D		DVFL	2812D	DVFL2	2815D
±V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	±V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)	±V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)
5.5	35k	13.2	5.8k	16.50	1.7k
5.4	47.5k	13.0	10k	16.25	5k
5.3	68.3k	12.8	16.2k	16.00	10k
5.2	110k	12.6	26.6k	15.75	18.3k
5.1	235k	12.4	47.3k	15.50	35k
5.0	-	12.2	109k	15.25	85k
4.9	225k	12.0	-	15.00	-
4.8	100k	11.8	454k	14.75	475k
4.7	58.3k	11.6	213k	14.50	225k
4.6	37.5k	11.4	134k	14.25	142k
4.5	25k	11.2	94k	14.00	100k
4.4	16.7k	11.0	70.1k	13.75	75k
4.3	10.7k	10.8	54.3k	13.50	58.3k
4.2	6.3k	10.6	42.9k	13.25	46.4k
4.1	2.8k	10.4	34.4k	13.00	37.5k
4.0	0	10.2	27.8k	12.75	30.6k
		10.0	22.5k	12.50	25k
		9.8	18.2k	12.25	20.5k
		9.6	14.6k	12.00	16.7k



**EFFICIENCY PERFORMANCE CURVES** (T<sub>CASE</sub> = 25°C, Full Load, Unless Otherwise Specified)





90 85 80 75 30 50 70 90 110 Output Power (W)

Figure 9 – DVFL2805D Efficiency (%) vs. Output Power (W)

Figure 10 – DVFL2812D Efficiency (%) vs. Output Power (W)

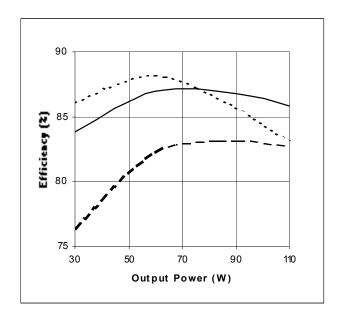


Figure 11 – DVFL2815D Efficiency (%) vs. Output Power (W)



#### **EMI PERFORMANCE CURVES**

 $(T_{CASE} = 25^{\circ}C, V_{IN} = +28V \pm 5\%, Full Load, Unless Otherwise Specified)$ 

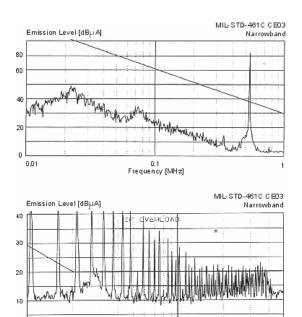


Figure 12 - DVFL2800D without EMI Filter

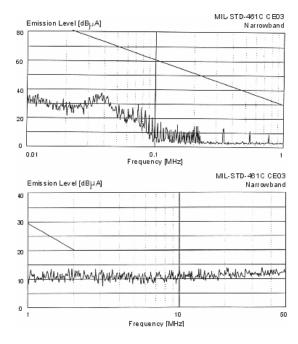
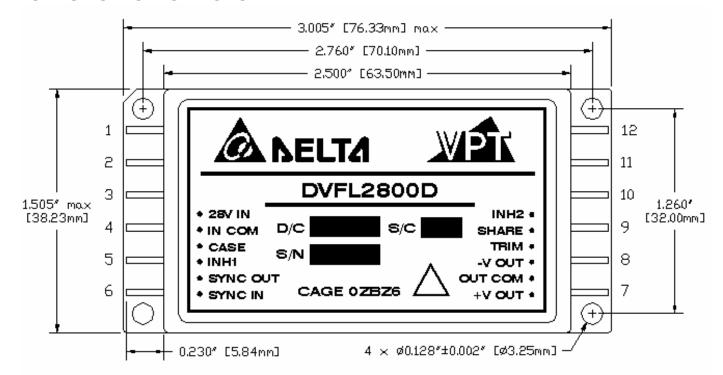


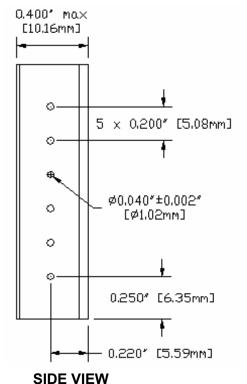
Figure 13 – DVFL2800D with EMI Filter



#### **PACKAGE SPECIFICATIONS**



#### **TOP VIEW**

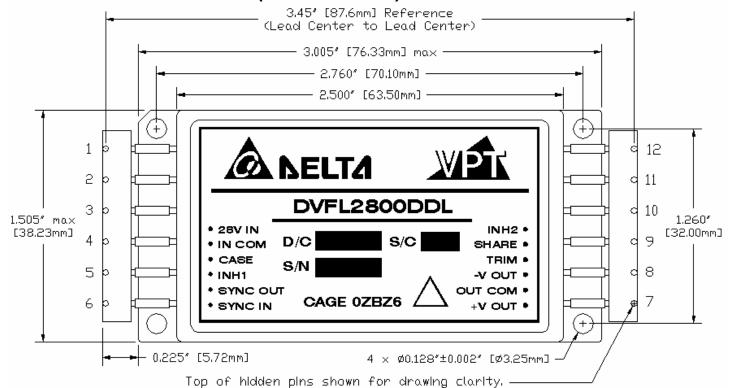


PIN	FUNCTION
1	28V IN
2	IN COM
3	N/C
4	INH1
5	SYNC OUT
6	SYNC IN
7	+V OUT
8	OUT COM
9	-V OUT
10	TRIM
11	SHARE
12	INH2

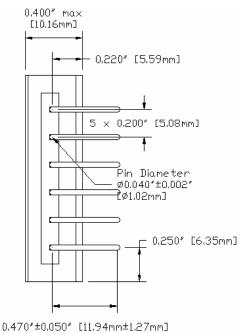
**Figure 14** – Package and Pinout (Pin Length is ±0.01", Other Dimensional Limits are ±0.005" Unless Otherwise Stated)



#### PACKAGE SPECIFICATIONS (DOWN-LEADED)



#### **TOP VIEW**



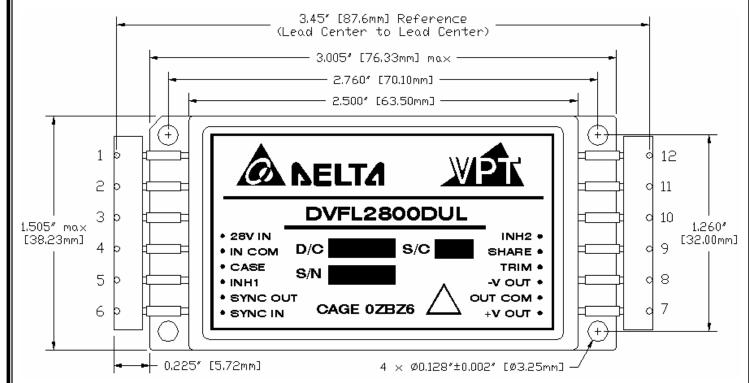
PIN	FUNCTION
1	28V IN
2	IN COM
3	N/C
4	INH1
5	SYNC OUT
6	SYNC IN
7	+V OUT
8	OUT COM
9	-V OUT
10	TRIM
11	SHARE
12	INH2

#### SIDE VIEW

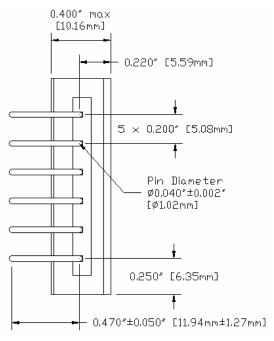
 $\begin{tabular}{ll} \textbf{Figure 15} - \textit{Package and Pinout (With Down-Leaded Pin Extensions Added)} \\ (\textit{Pin Length is $\pm 0.01"}, Other Dimensional Limits are $\pm 0.005" Unless Otherwise Stated) \\ \end{tabular}$ 



### **PACKAGE SPECIFICATIONS (UP-LEADED)**



#### **TOP VIEW**



PIN	FUNCTION
1	28V IN
2	IN COM
3	N/C
4	INH1
5	SYNC OUT
6	SYNC IN
7	+V OUT
8	OUT COM
9	-V OUT
10	TRIM
11	SHARE
12	INH2

#### **SIDE VIEW**

**Figure 16** – Package and Pinout (With Up-Leaded Pin Extensions Added) (Pin Length is ±0.01", Other Dimensional Limits are ±0.005" Unless Otherwise Stated)



### **PACKAGE PIN DESCRIPTION**

Pin	Function	Description
1	28V IN	Positive Input Voltage Connection
2	IN COM	Input Common Connection
3	N/C	No Connection
4	INH1	Logic Low = Disabled Output. Connecting the inhibit(1) pin to input common causes converter shutdown.  Logic High = Enabled Output. Unconnected or open collector TTL.
5	SYNC OUT	Output Synchronization Signal
6	SYNC IN	Input Synchronization Signal
7	+V OUT	Positive Output Voltage Connection
8	OUT COM	Output Common Connection
9	-V OUT	Negative Output Voltage Connection
10	TRIM	Trim Output Voltage to +10%, -20% of Nominal Value
11	SHARE	Current Share
12	INH2	Logic Low = Disabled Output. Connecting the inhibit(2) pin to output common causes converter shutdown.  Logic High = Enabled Output. Unconnected or open collector TTL.



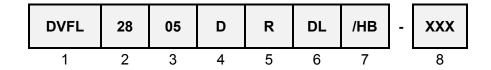
### **ENVIRONMENTAL SCREENING** (Per MIL-STD-883 as referenced to MIL-PRF-38534, Class H)

Screening	MIL-STD-883 Standard (No Suffix)		Extended /ES	HB /HB
Pre-Cap Inspection	Method 2017, 2032 Internal Procedure	•	•	•
Temperature Cycling	Method 1010, Condition C Method 1010, -55°C to 125°C		•	•
Constant Acceleration	Method 2001, Condition A Method 2001, 500g		•	•
Burn-In	Method 1015, 160 hours at +125°C 96 hours at +125°C 24 hours at +125°C	•	•	•
Hermeticity	Method 1014, Fine Leak, Condition A Method 1014, Gross Leak, Condition C Dip (1 x 10 <sup>-3</sup> )	•	•	•
Final Electrical	MIL-PRF-38534, Group A <sup>1</sup> 100% at 25°C	•	•	•
Final Inspection	Method 2009	•	•	•

Note: 1. 100% R&R testing at –55°C, +25°C, and +125°C with all test data included in product shipment.



#### ORDERING INFORMATION



(1) (2) (3)

Product Series	Nominal Input Voltage		Output Voltage		Number o	f Outputs
DVFL	28	28 Volts	05 12 15	±5 Volts ±12 Volts ±15 Volts	D	Dual

(5) (6) (7)

Rad-Hard Option		Package Option		Screening Code		Additional Screening Code
None R	Standard 100 kRad	None DL UL	Standard Down-Lead Up-Lead	None /ES /HB	Standard Extended HB	Contact Sales

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.

#### **CONTACT INFORMATION**

To request a quotation or place an order please contact your sales representative or the VPT Inc. Sales Department at:

Phone: (425) 487-4850 Fax: (425) 487-4802 E-mail: sales@vpt-inc.com

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