

SERIES EL511 ECL (10K COMPATIBLE) LOGIC DELAY MODULES – 5 TAPS

T-47-13

- 5 Taps 16 Pin DIP Package • ECL Input & Output Levels • 5 Equally Spaced Taps
- 70 ECL DC Fan-out Capacity

Specifications:

- Supply Voltage : -5.2VDC to Vee
- Logic 1 Voltage(3) : -.96v
- Logic 1 Current : .26mA
- Logic 0 Current : .5 μ A
- Logic 0 Voltage : -1.65v
- Operating Temperature : -30°C to 85°C
- Power Dissipation : 200mW Typ. (No load)

Test Conditions:

- Input Pulse-Width : Min, 40% of Total Delay
- Input Pulse Rise Time : 2NSEC Max
- Input Pulse Voltage : 1v (-0.75vH to -1.75vL)
- Vee Supply Voltage : -5.2VDC
- Vee Supply Current : 40mA Typ.

Electrical Specifications at 25°C

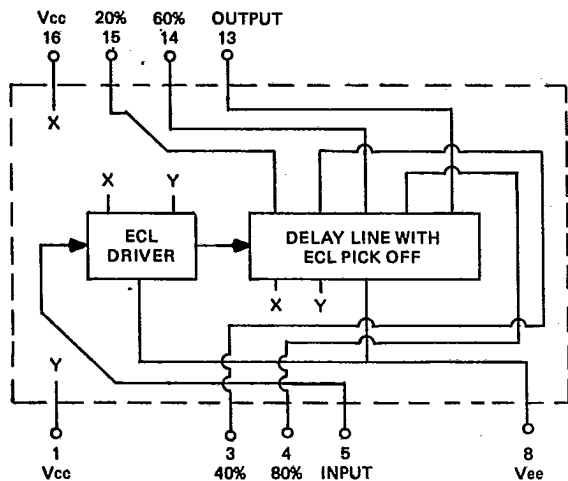
Part Number	Total Delay NSEC (1)	Tap to Tap Delay NS (1)	Rise Time NS Max (2)
EL511-5*	4	1	2.5
EL511-10*	8	2	2.5
EL511-16*	16	4	2.5
EL511-20	20	4	2.5
EL511-25	25	5	2.5
EL511-30	30	6	2.5
EL511-35	35	7	3
EL511-40	40	8	4
EL511-45	45	9	4
EL511-50	50	10	4
EL511-60	60	12	4
EL511-75	75	15	4.5
EL511-100	100	20	5
EL511-125	125	25	6
EL511-150	150	30	7
EL511-200	200	40	8
EL511-250	250	50	10
EL511-300	300	60	16
EL511-400	400	80	18
EL511-500	500	100	20

Note: (1) Measured at -1.3v level leading edge, Tol. $\pm 5\%$ or ± 1 NS whichever is greater.

(2) Measured from 20% to 80% Pulse Amplitude.

*Time Delay measurements referenced to 1st Tap.

(3) Outputs terminated through 270 ohms to -5.2VDC.



SCHEMATIC

