

SERIES EL514 ECL (10K COMPATIBLE) TRIPLE LOGIC DELAY MODULES

- 16 Pin DIP Package • Three Independent Equal Delays
- ECL Input and Output Levels • 70 ECL DC Fan-out Capacity

Specifications:

- Supply Voltage : -5.2VDC to Vee
- Logic 1 Voltage : $-.96\text{v}$ Min
- Logic 1 Current : $265\mu\text{A}$ Max
- Logic 0 Voltage : -1.65v Max
- Logic 0 Current : $.5\mu\text{A}$ Min
- Storage Temperature : -55°C to $+125^\circ\text{C}$
- Operating Temperature : -30°C to $+85^\circ\text{C}$

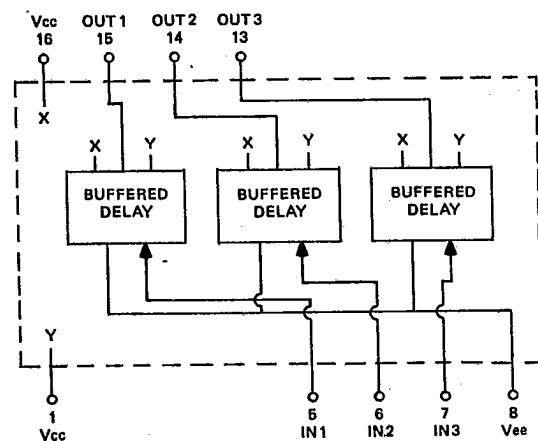
Test Conditions:

- Input Pulse-Width : Min 150% of Total Delay
- Input Pulse Rise Time : 2NSEC Max
- Input Pulse Voltage : 1v (-0.75vH to -1.75vL)
- Vee Supply Voltage : -5.2VDC

Electrical Specifications at 25°C

Part Number	Delay Time NSEC (1)	Rise Time NSEC Max (2)	Part Number	Delay Time NSEC (1)	Rise Time NSEC Max (2)
EL514-4	4 ± 1	3NS	EL514-25	25 ± 1	7NS
EL514-5	5 ± 1	3NS	EL514-30	30 ± 1.5	8NS
EL514-6	6 ± 1	3NS	EL514-35	35 ± 1.5	8NS
EL514-7	7 ± 1	3NS	EL514-40	40 ± 1.5	8NS
EL514-8	8 ± 1	3NS	EL514-45	45 ± 2	9NS
EL514-9	9 ± 1	3NS	EL514-50	50 ± 2	10NS
EL514-10	10 ± 1	4NS	EL514-55	55 ± 2	10NS
EL514-11	11 ± 1	4NS	EL514-60	60 ± 2	10NS
EL514-12	12 ± 1	4NS	EL514-65	65 ± 2.5	10NS
EL514-13	13 ± 1	4NS	EL514-70	70 ± 2.5	10NS
EL514-14	14 ± 1	4NS	EL514-75	75 ± 2.5	10NS
EL514-15	15 ± 1	4NS	EL514-80	80 ± 2.5	12NS
EL514-16	16 ± 1	5NS	EL514-85	85 ± 3	12NS
EL514-17	17 ± 1	5NS	EL514-90	90 ± 3	12NS
EL514-18	18 ± 1	5NS	EL514-95	95 ± 3	12NS
EL514-19	19 ± 1	5NS	EL514-100	100 ± 3	15NS
EL514-20	20 ± 1	5NS	EL514-125	125 ± 4	18NS
EL514-21	21 ± 1	6NS	EL514-150	150 ± 4	20NS
EL514-22	22 ± 1	6NS	EL514-200	200 ± 5	25NS
EL514-23	23 ± 1	6NS	EL514-250	250 ± 5	28NS
EL514-24	24 ± 1	6NS	EL514-300	300 ± 6	30NS

Note: (1) Measured at -1.3v level leading edge.
 (2) Measured from 20% to 80% Pulse Amplitude.
 (3) Outputs terminated through 270 ohms to -5.2VDC .



SCHEMATIC

