

SERIES EL5150 ECL DIGITAL DELAY MODULES

- 10 Taps 32 Pin DIP Package
- Input & Output Buffered
- 10 Equally Spaced Taps

Specifications:

- Supply Voltage : -5.2v
- Logic 1 Voltage : -.96v
- Logic 1 Current : .52mA
- Logic 0 Voltage : -1.65v
- Logic 0 Current : 1.0 μ A
- Operating Temperature : -30°C to 85°C
- Power Dissipation : 400mW Typ. (No load)
- Temperature Coefficient : ≥ 300 PPM/°C
- 70 ECL Loads DC Fan-out capacity per phase

**Input Test Conditions:**

- Input Pulse-Width : $\geq$ Min. 40% of Total Delay
- Input Pulse Rise : 2NS
- Input Pulse Voltage : -.7v
- Vee Supply Voltage : -5.2v
- Vee Supply Current : 40mA Typ.

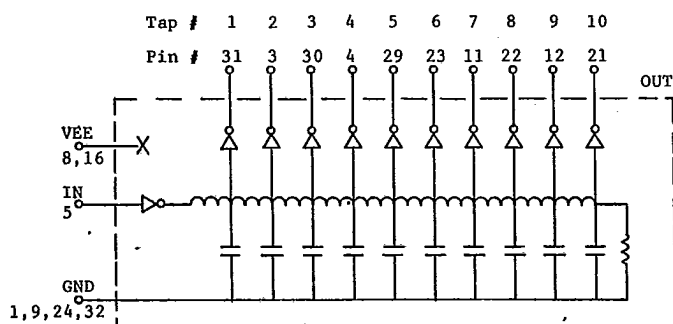
Electrical Specifications at 25°C

Part Number	Total Delay NSEC $\pm 5\%$	Tap to Tap Delay NS(1)	Rise Time NS Max(2)
EL5151*	10	1	4
EL5152*	20	2	4
EL5153*	25	2.5	4
EL5154*	40	4	4
EL5155*	50	5	5
EL5156	60	6	5
EL5157	75	7.5	8
EL5158	100	10	8
EL5159	150	15	15
EL5160	200	20	15
EL5161	250	25	15
EL5162	300	30	28
EL5163	400	40	28
EL5164	500	50	38
EL5165	750	75	38
EL5166	1000	100	40
EL5167	1500	150	40

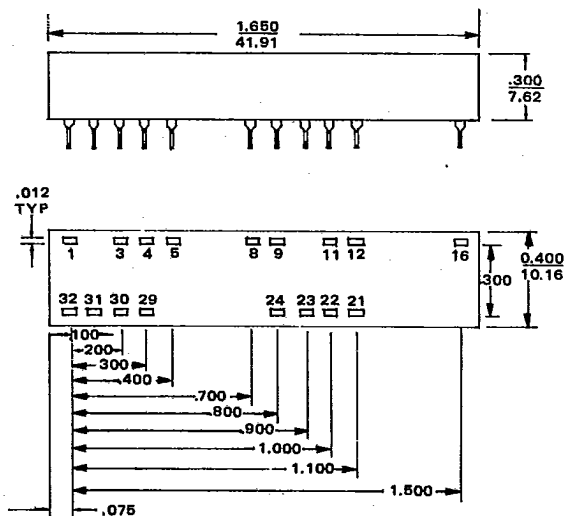
Note: (1) Measured at 50% leading edge, Tol. $\pm 5\%$ or ± 2 NS whichever is greater.

(2) Measured from 20% to 80% of leading edge.

* Time Delay measurements referenced to 1st Tap.



SCHEMATIC



INCHES .XXX $\pm .010$
MILLIMETERS .XX $\pm .25$