

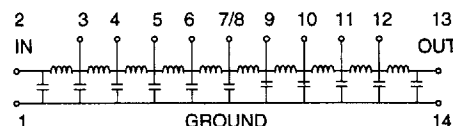
14 Pin DIP Passive Delay Lines

Zo OHMS ±10%	DELAY nS ±5% or ±2 nS	TAP DELAYS nS	RISE TIME nS Max.	ATTEN % Max.	PCA PART NUMBER	Zo OHMS ±10%	DELAY nS ±5%	TAP DELAYS ±10% or ±2 nS†	RISE TIME nS Max.	ATTEN % Max.	PCA PART NUMBER
50	16	1.6 ± 0.6	3.0	1.6	EP19830	100	60	6.0	11	2.0	EP19836
50	20	2.0 ± 0.6	3.5	1.6	EP6889	100	75	7.5	15	4.0	EP8476
50	24	2.4 ± 0.6	4.5	1.6	EP19831	100	100	10.0	17	4.0	EP19825
50	50	5.0 ± 2	8.5	1.6	EP19821	100	125	12.5	21	7.0	EP19826
50	100	10.0 ± 2	17.0	3.8	EP19822	100	150	15.0	25	8.0	EP19827
50	125	12.5 ± 2	21.0	6.5	EP19823	100	200	20.0	34	10.0	EP19828
100	30	3.0 ± 0.6	5.5	2.0	EP8684	100	250	25.0	42	12.0	EP19829
100	35	3.5 ± 0.8	6.0	2.0	EP19833	200	68	6.8	12	10.0	EP19838
100	37	3.7 ± 0.8	6.0	2.0	EP19834	200	75	7.5	13	10.0	EP19839
100	40	4.0 ± 1	8.0	2.0	EP7179	200	80	8.0	14	10.0	EP19840
100	47	4.7 ± 1	8.0	2.0	EP19835	200	95	9.5	17	10.0	EP19841
100	50	5.0 ± 2	8.5	2.0	EP19824	200	120	12.0	21	10.0	EP19842

† Whichever is greater.

DC Electrical Characteristics	Min	Max	Unit
Distortion		±10	%
Temperature Coefficient of Delay		100	PPM/°C
Insulation Resistance @ 100 Vdc	1K		Meg Ohms
Dielectric Strength		100	Vdc

Schematic



Recommended Operating Conditions	Min	Max	Unit
PW*	Pulse Width % of Total Delay	200	%
D*	Duty Cycle	40	%
TA	Operating Free Air Temperature	0	70 °C

*These two values are inter-dependent.

Input Pulse Test Conditions @ 25°C		
V _{IN}	Pulse Input Voltage	3 Volts
PW	Pulse Width % of Total Delay	300 %
T _{RI}	Input Rise Time (10 - 90%)	2.0 nS
PRR	Pulse Repetition Rate @ T _d ≤ 150 nS	1.0 MHz
	Pulse Repetition Rate @ T _d > 150 nS	300 KHz

Package Dimensions

