

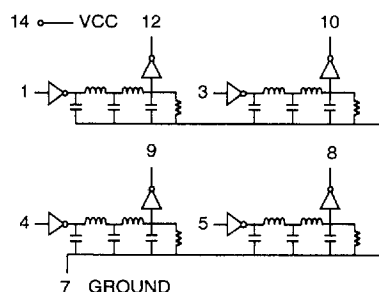
14 Pin DIP Quad TTL Compatible Active Delay Lines

DELAY TIME ±5% or ±2 nS†	PART NUMBER	DELAY TIME ±5% or ±2 nS†	PART NUMBER	DELAY TIME ±5% or ±2 nS†	PART NUMBER
5	EP9734-5	16	EP9734-16	35	EP9734-35
6	EP9734-6	17	EP9734-17	40	EP9734-40
7	EP9734-7	18	EP9734-18	45	EP9734-45
8	EP9734-8	19	EP9734-19	50	EP9734-50
9	EP9734-9	20	EP9734-20	55	EP9734-55
10	EP9734-10	21	EP9734-21	60	EP9734-60
11	EP9734-11	22	EP9734-22	65	EP9734-65
12	EP9734-12	23	EP9734-23	70	EP9734-70
13	EP9734-13	24	EP9734-24	75	EP9734-75
14	EP9734-14	25	EP9734-25		
15	EP9734-15	30	EP9734-30		

†Whichever is greater. Delay times referenced from input to leading edges at 25°C, 5.0V, with no load.

DC Electrical Characteristics					
Parameter	Test Conditions	Min	Max	Unit	
V _{OH}	High-Level Output Voltage	V _{CC} = min. V _{IL} = max. I _{OH} = max	2.7	V	
V _{OL}	Low-Level Output Voltage	V _{CC} = min. V _{IH} = min. I _{OL} = max	0.5	V	
V _{IK}	Input Clamp Voltage	V _{CC} = min. I _I = I _{IK}	-1.2V	V	
I _{IH}	High-Level Input Current	V _{CC} = max. V _{IN} = 2.7V	50	µA	
I _{IL}	Low-Level Input Current	V _{CC} = max. V _{IN} = 5.25V	1.0	mA	
I _{OS}	Short Circuit Output Current	V _{CC} = max. V _{OUT} = 0.	-40	mA	
	(One output at a time)				
I _{CC}	High-Level Supply Current	V _{CC} = max. V _{IN} = OPEN	150	mA	
I _{CCL}	Low-Level Supply Current	V _{CC} = max. V _{IN} = 0	150	mA	
T _{RO}	Output Rise Time		4	nS	
N _H	Fanout High-Level Output	V _{CC} = max. V _{OH} = 2.7V	20 TTL LOAD		
N _L	Fanout Low-Level Output	V _{CC} = max. V _{OL} = 0.5V	10 TTL LOAD		

Schematic



Recommended Operating Conditions		Min	Max	Unit
V _{CC}	Supply Voltage	4.75	5.25	V
V _{IH}	High-Level Input Voltage	2.0		V
V _{IL}	Low-Level Input Voltage		0.8	V
I _{IK}	Input Clamp Current		-18	mA
I _{OH}	High-Level Output Current		-1.0	mA
I _{OL}	Low-Level Output Current		20	mA
PW*	Pulse Width of Total Delay	40		%
d*	Duty Cycle		40	%
T _A	Operating Free-Air Temperature	0	+70	°C

*These two values are inter-dependent.

Input Pulse Test Conditions @ 25° C				Unit
E _{IN}	Pulse Input Voltage	3.2		Volts
PW	Pulse Width % of Total Delay	110		%
T _{RI}	Pulse Rise Time (0.75 - 2.4 Volts)	2.0		nS
P _{RR}	Pulse Repetition Rate @ T _d ≤ 200 nS	1.0		MHz
	Pulse Repetition Rate @ T _d > 200 nS	100		KHz
V _{CC}	Supply Voltage	5.0		Volts

Package Dimensions

