

## ■ Description

The FA7611P(M) is a bipolar IC containing basic circuit necessary for PWM-type switching power supply control.

## ■ Features

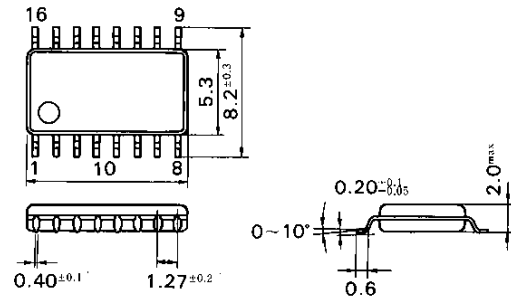
- Low-voltage operation ( $V_{CC} = 3.6$  to  $22V$ )
- Predrivers: Totem-pole output or open-collector for CH1 and open-collector output for CH2
- Latch-mode short-circuit protection function (no malfunction due to electrical noise)
- soft-start function
- Undervoltage lock-out function
- One capacitor shared for short circuit protection and for soft start to minimize the number of external discrete components

## ■ Applications

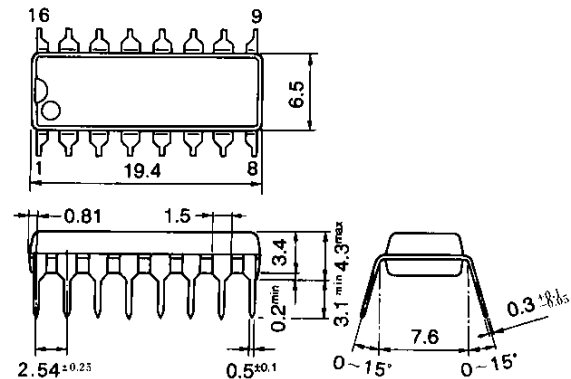
- Battery power supply for portable equipment

## ■ Dimensions, mm

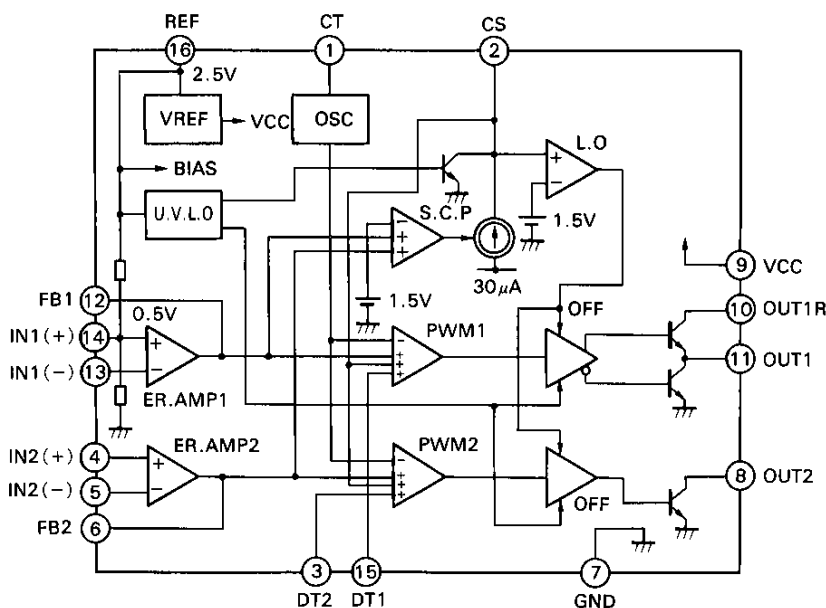
### • SOP-16



### • DIP-16



## ■ Block diagram



| Pin No. | Pin symbol | Description  |
|---------|------------|--|
| 1       | CT         | Oscillator timing capacitor  |
| 2       | CS         | Capacitor for soft-start, short-circuit protection and delay       |
| 3       | DT2        | Dead time adjustment   |
| 4       | IN2 (+)    | Non-inverting input to error amplifier                             |
| 5       | IN2 (-)    | Inverting input to error amplifier                                 |
| 6       | FB2        | Error amplifier output   |
| 7       | GND        | Ground   |
| 8       | OUT2       | CH. 2 Output   |
| 9       | VCC        | Power supply   |
| 10      | OUT1R      | CH. 1 Current limiting resistor                                    |
| 11      | OUT1       | CH. 1 Output   |
| 12      | FB1        | Error amplifier output   |
| 13      | IN1 (-)    | Inverting input to error amplifier                                 |
| 14      | IN1 (+)    | Non-inverting input to error amplifier<br>Reference voltage (0.5V) |
| 15      | DT1        | Dead time adjustment   |
| 16      | REF        | Reference voltage output (2.5V)                                    |

## ■ Absolute maximum ratings

| Item                     | Symbol           | Rating      | Unit |
|--------------------------|------------------|-------------|------|
| Supply voltage           | V <sub>CC</sub>  | 22          | V    |
| Reference voltage output | I <sub>OR</sub>  | 5           | mA   |
| Output current           | I <sub>O</sub>   | ±50         | mA   |
| Total power dissipation  | P <sub>d</sub>   | 400         | mW   |
| Operating temperature    | T <sub>opr</sub> | −20 to +85  | °C   |
| Storage temperature      | T <sub>stg</sub> | −40 to +150 | °C   |

## ■ Recommended operating conditions

| Item                         | Symbol           | Min. | Max.   | Unit |
|------------------------------|------------------|------|--------|------|
| Supply voltage               | V <sub>CC</sub>  | 3.6  | 20     | V    |
| Feedback resistance          | R <sub>NF</sub>  | 100  |        | kΩ   |
| Oscillator timing capacitor  | C <sub>T</sub>   | 220  | 22,000 | pF   |
| Oscillator timing resistance | R <sub>T</sub>   | 10   | 100    | kΩ   |
| Oscillation frequency        | f <sub>osc</sub> | 5    | 200    | kHz  |

■ Electrical characteristics (T<sub>a</sub> = 25°C, V<sub>CC</sub> = 6V, R<sub>T</sub> = 33kΩ, C<sub>T</sub> = 1000pF)

## Reference voltage section

| Item   | Symbol           | Test condition                | Min. | Typ. | Max. | Unit |
|--|------------------|-------------------------------|------|------|------|------|
| Output voltage                                     | V <sub>REF</sub> | I <sub>OR</sub> = 1mA         | 2.38 | 2.50 | 2.62 | V    |
| Line regulation                                    | LINE             | V <sub>CC</sub> = 3.6 to 20V  |      | 4    | 12   | mV   |
| Load regulation                                    | LOAD             | I <sub>OR</sub> = 0.1 to 1mA  |      | 1    | 6    | mV   |
| Output voltage variation due to temperature change | V <sub>TC1</sub> | T <sub>a</sub> = −20 to +25°C | −1   |      | 1    | %    |
|  | V <sub>TC2</sub> | T <sub>a</sub> = +25 to +85°C | −1   |      | 1    | %    |

## Oscillator section

| Item   | Symbol           | Test condition                                 | Min. | Typ. | Max. | Unit |
|--|------------------|--|------|------|------|------|
| Oscillation frequency                                | f <sub>osc</sub> | C <sub>T</sub> = 1000pF, R <sub>T</sub> = 33kΩ | 95   | 115  | 135  | kHz  |
| Frequency variation 1 (due to supply voltage change) | f <sub>dV</sub>  | V <sub>CC</sub> = 3.6 to 20V                   |      | 1    |      | %    |
| Frequency variation 2 (due to temperature change)    | f <sub>dT</sub>  | T <sub>a</sub> = −20 to +85°C                  |      | 5    |      | %    |

## Error amplifier section

| Item                                      | Symbol           | Test condition          | Min.                  | Typ. | Max. | Unit |
|---|------------------|-------------------------|-----------------------|------|------|------|
| Reference voltage                         | V <sub>B</sub>   |                         | 0.45                  | 0.50 | 0.55 | V    |
| Input bias current                        | I <sub>B</sub>   |                         |                       | 5    | 100  | nA   |
| Open-loop voltage gain                    | A <sub>v</sub>   |                         | 70                    |      |      | dB   |
| Unity-gain bandwidth                      | G <sub>B</sub>   |                         |                       | 0.6  |      | MHz  |
| Maximum output voltage (Pin 6 and Pin 12) | V <sub>OM+</sub> | R <sub>NF</sub> = 100kΩ | V <sub>REF</sub> −0.2 |      |      | V    |
|   | V <sub>OM−</sub> | R <sub>NF</sub> = 100kΩ |                       |      | 200  | mV   |
| Output source current (Pin 6 and Pin 12)  | I <sub>OM+</sub> | V <sub>OM</sub> = 1V    | 40                    | 85   | 200  | μA   |

## PWM comparator section

| Item                                       | Symbol            | Test condition   | Min. | Typ. | Max. | Unit |
|--|-------------------|------------------|------|------|------|------|
| Input threshold voltage (Pin 6 and Pin 12) | V <sub>TH0</sub>  | Duty cycle = 0%  |      | 0.85 | 0.95 | V    |
| Input threshold voltage (Pin 6 and Pin 12) | V <sub>TH50</sub> | Duty cycle = 50% |      | 1.1  |      | V    |

**Dead time adjustment circuit section**

| Item                                       | Symbol               | Test condition   | Min. | Typ. | Max. | Unit |
|--|----------------------|------------------|------|------|------|------|
| Input bias current (Pin 3 and Pin 15)      | I <sub>BDT</sub>     |                  |      | 80   | 300  | nA   |
| Input threshold voltage (Pin 3 and Pin 15) | V <sub>TH DT0</sub>  | Duty cycle = 0%  |      | 0.22 | 0.32 | V    |
| Input threshold voltage (Pin 3 and Pin 15) | V <sub>TH DT50</sub> | Duty cycle = 50% |      | 0.46 |      | V    |

**Short-circuit protection circuit section**

| Item                                       | Symbol             | Test condition                 | Min. | Typ. | Max. | Unit |
|--|--------------------|--------------------------------|------|------|------|------|
| Input threshold voltage (Pin 6 and Pin 12) | V <sub>TH PC</sub> |                                | 1.20 | 1.50 | 1.80 | V    |
| Charge current (Pin 2)                     | I <sub>CHG</sub>   | Pin 2 = 0V, Pin 6, Pin 12 = 2V | 10   | 30   | 50   | μA   |
| Latch-mode threshold voltage (Pin 2)       | V <sub>TH LA</sub> |                                | 1.20 | 1.50 | 1.80 | V    |

**Undervoltage lockout circuit section**

| Item                        | Symbol              | Test condition | Min. | Typ. | Max. | Unit |
|-----------------------------|---------------------|----------------|------|------|------|------|
| OFF-to-ON threshold voltage | V <sub>TH ON</sub>  |                |      | 2.65 |      | V    |
| ON-to-OFF threshold voltage | V <sub>TH OFF</sub> |                |      | 2.60 |      | V    |
| Voltage hysteresis          | V <sub>HYS</sub>    |                |      | 50   |      | mV   |

**Output section**

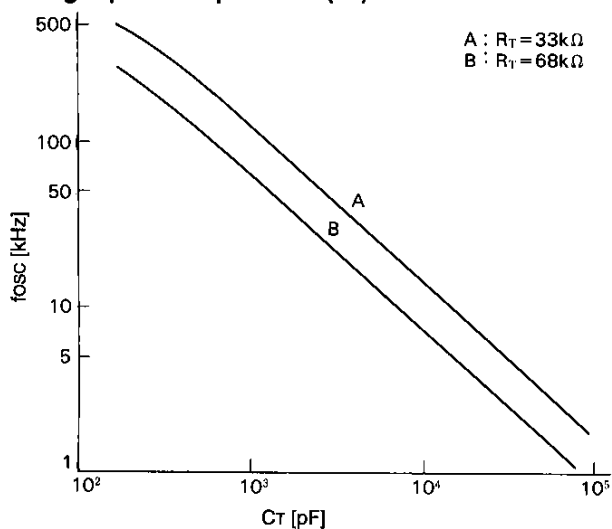
| Item                                  | Symbol               | Test condition                             | Min. | Typ. | Max. | Unit |
|---------------------------------------|----------------------|--|------|------|------|------|
| CH. 1 H-level output voltage (Pin 11) | V <sub>O1H</sub>     | R <sub>L</sub> = 10kΩ                      | 3.5  | 4.0  |      | V    |
| CH. 1 L-level output voltage (Pin 11) | V <sub>O1L</sub>     | Output sink current = 20mA                 |      | 0.25 | 0.65 | V    |
| CH. 1 Output source current (Pin11)   | I <sub>SOURCE1</sub> | R <sub>OUT1</sub> = 680 Ω<br>(Pin 11) = 0V | 6.5  | 9    |      | mA   |
| CH. 2 L-level output voltage (Pin 8)  | V <sub>O2L</sub>     | Output sink current = 20mA                 |      | 1.0  | 1.5  | V    |

**Overall device**

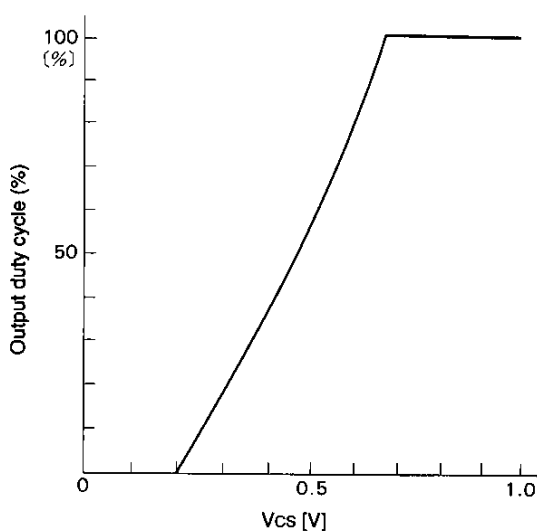
| Item                           | Symbol             | Test condition                      | Min. | Typ. | Max. | Unit |
|--------------------------------|--------------------|-------------------------------------|------|------|------|------|
| Supply current                 | I <sub>CC LA</sub> | Latch mode                          |      | 2.0  | 3.0  | mA   |
| Operating-state supply current | I <sub>CC AV</sub> | R <sub>L</sub> = ∞ Duty cycle = 50% |      | 3.5  | 6.0  | mA   |

# ■ Characteristic curves ( $T_a = 25^\circ\text{C}$ )

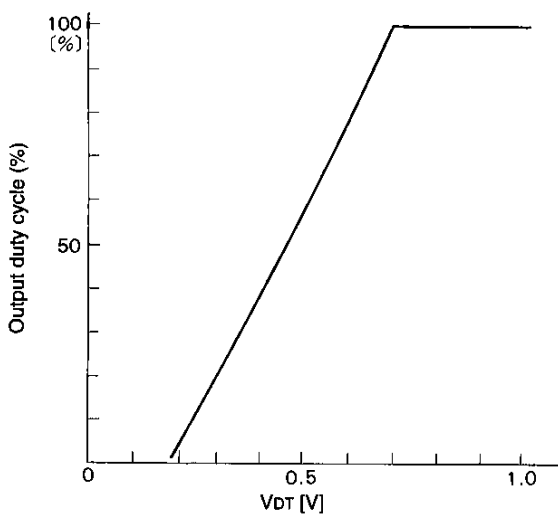
Oscillation frequency ( $f_{osc}$ ) vs.  
timing capacitor capacitance ( $C_T$ )



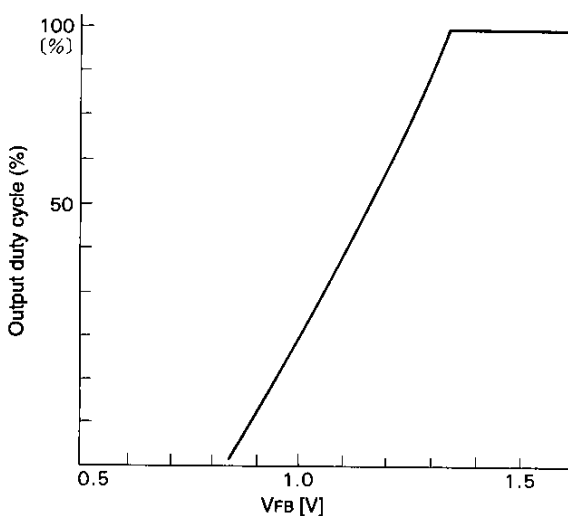
Output duty cycle vs. CS terminal voltage ( $V_{CS}$ )



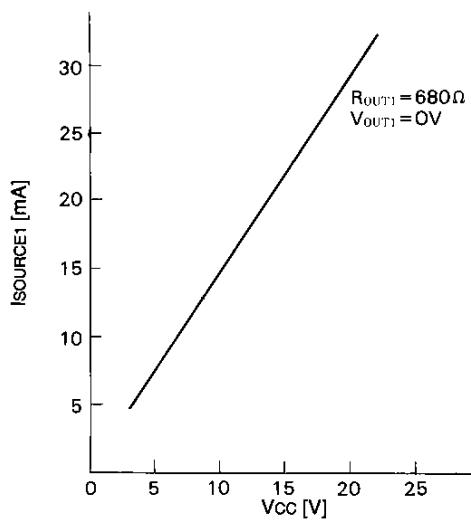
Output duty cycle vs. DT terminal voltage ( $V_{DT}$ )



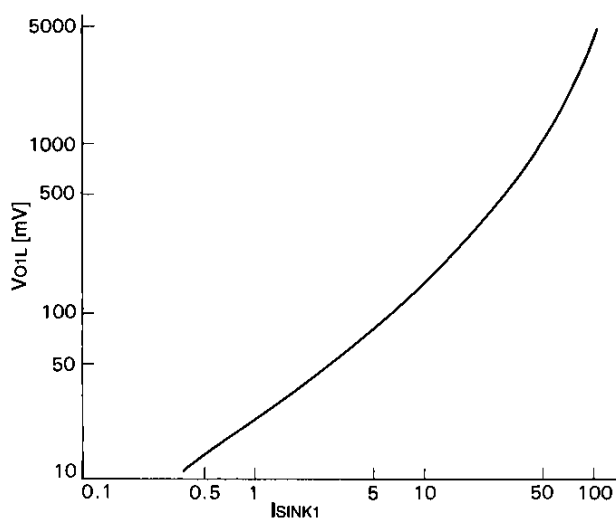
Output duty cycle vs. FB terminal voltage ( $V_{FB}$ )



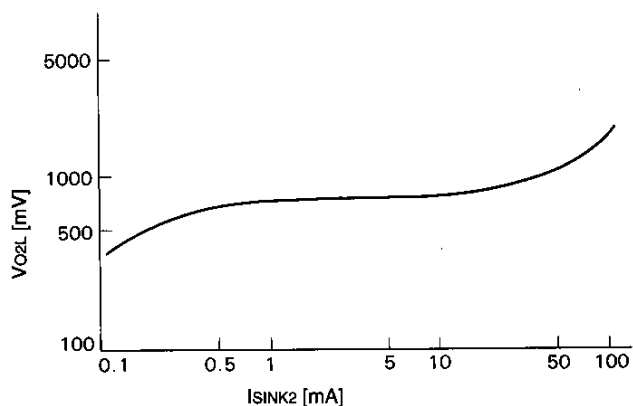
CH-1 output source current ( $I_{SOURCE1}$ ) vs.  
supply voltage ( $V_{CC}$ )



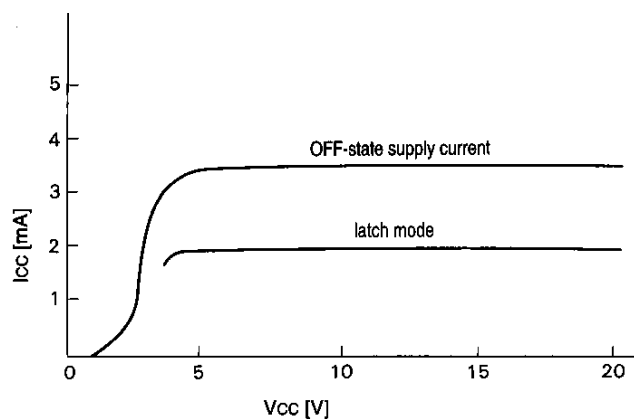
L-level output voltage ( $V_{O1L}$ ) vs.  
CH. 1 output sink current ( $I_{SINK1}$ )



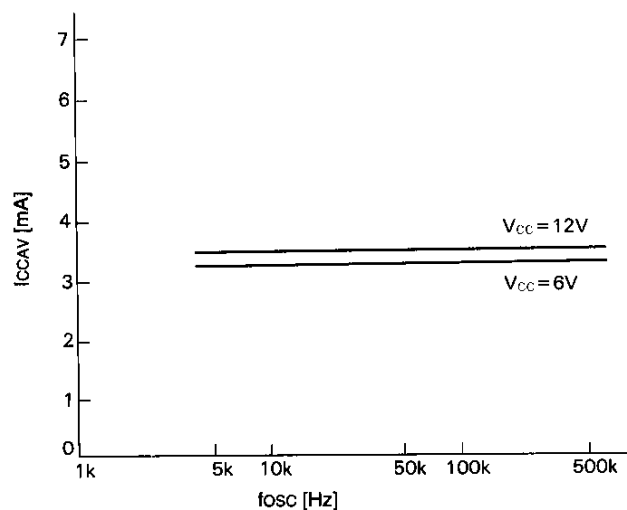
**L-level output voltage ( $V_{OL}$ )  
vs. CH. 2 output sink current ( $I_{SINK2}$ )**



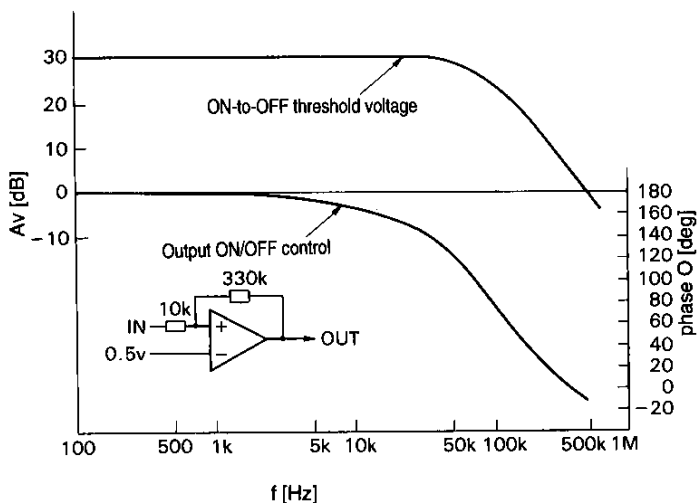
**Supply current ( $I_{CC}$ ) vs. supply voltage ( $V_{CC}$ )**



**Operating-state supply current ( $I_{CCAV}$ ) vs.  
oscillation frequency ( $f_{osc}$ )**

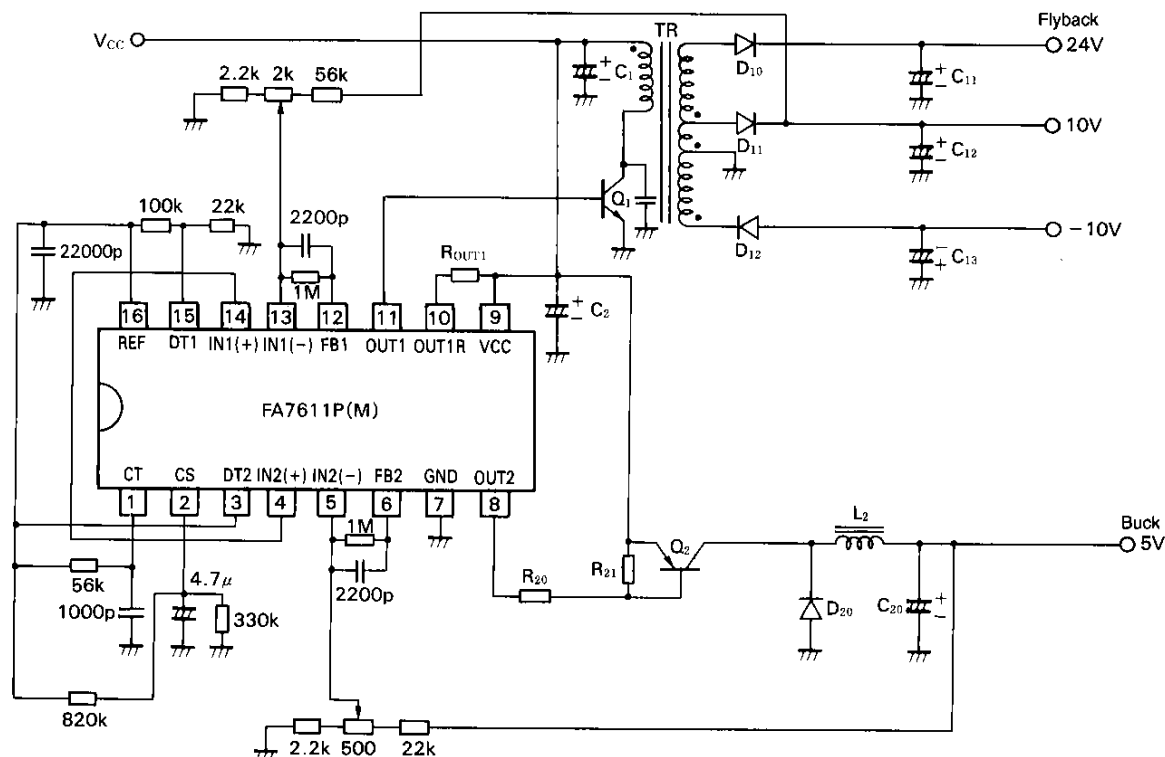


**Error amplifie frequency (f) vs. valtage gain ( $A_v$ ) / phase**

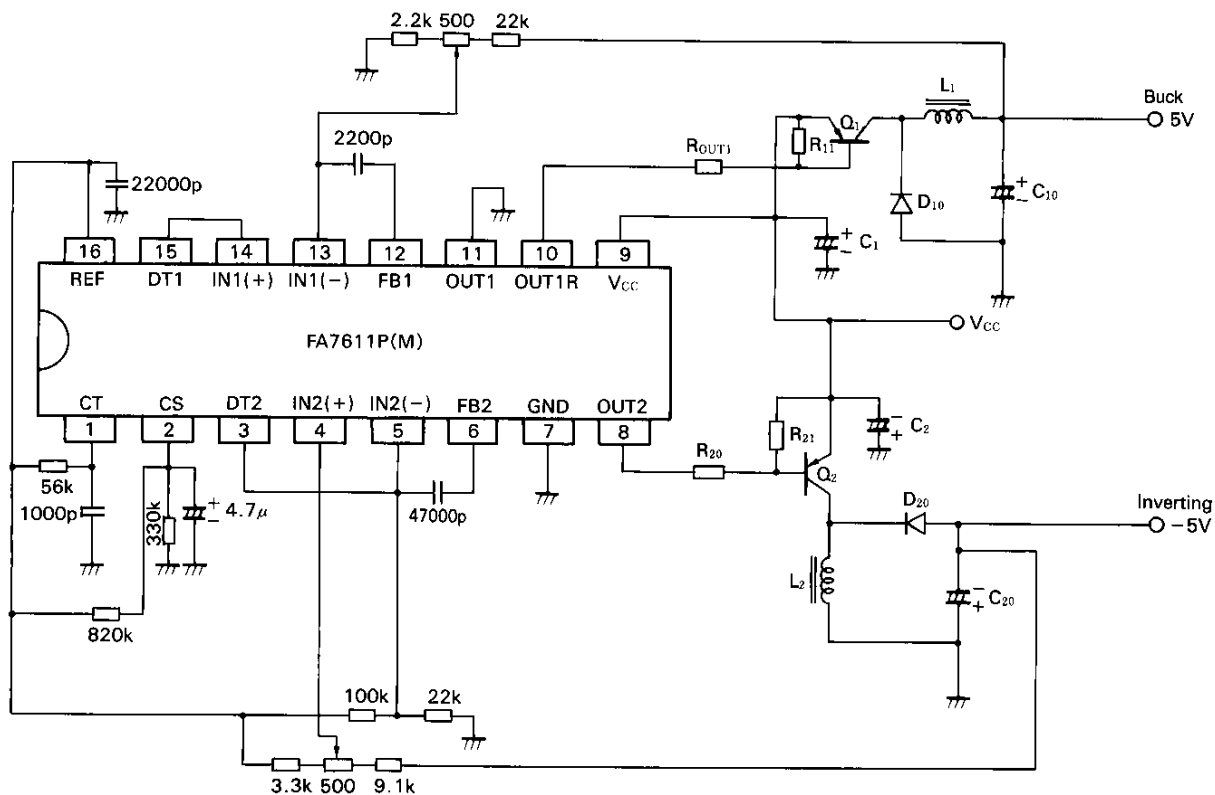


■ Application circuit

● Flyback-transformer type and chopper type buck converter circuit



● Chopper type buck converter and inverting converter circuit



Parts tolerances characteristics are not defined in the circuit design sample shown above.  
When designing an actual circuit for a product, you must determine parts tolerances and characteristics for safe and economical operation.

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