



FAIRCHILD INTEGRATED CIRCUITS PMOS

These integrated circuits, which were originally introduced by Fairchild, utilize PMOS silicon gate technology.

FUNCTIONS AND CHARACTERISTICS

MAXIMUM RATINGS

Rating	Value	Unit
Storage Temperature	-65 to +150	°C
Operating Temperature	-55 to +125	°C
V_{GG}	-20 to 0.3	V

PACKAGING AVAILABLE

(See page 88):

E - 16-LEAD DUAL INLINE

J - 24-LEAD DUAL INLINE

Y - 28-LEAD DUAL INLINE

PMOS

FUNCTION	TYPE/PKG	POWER DISSIPATION	OPERATING FREQUENCY Max/kHz
TV Sync Generator	3262A/E	750	—
TV Sync Generator for Generator Lock	3262B/E	750	—
64 x 4 FIFO Serial Memory	3341/E	450	700
64 x 4 FIFO Serial Memory	3341A/E	450	1000
Quad 64-Bit Static Shift Register	3342/E	380	1.5
Hex 32-Bit Static Shift Register	3348/J	—	1.0
Hex 32-Bit Static Shift Register	3349/E	—	1.0
40 x 9 FIFO Memory	3351/Y	520	2.0
Quad 80-Bit Static Shift Register	3357/E	375	4.0
8-Channel Multiplex Switch	3708/E	200	—