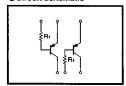
Digital Transistor (Dual Digital Transistors for Inveter Driver)

Features

1) Two DTA143T transistors are housed in an SMT package.

●Circuit schematic



■Absolute maximum ratings (Ta=25℃)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	-50	٧	
Collector-emitter voltage	VCEO	-50	٧	
Emitter-base voltage	VEBO	-5	٧	
Collector current	lc	-100	mΑ	
Collector power dissipation	Pc	300 (TOTAL)	mW	*
Junction temperature	Tj	150	Č	
Storage temperature	Tstg	-55~150	Č	

^{* 200} mW per element must not be exceeded.

Package, marking, and packaging specifications

Туре	IMB7A
Package	SMT6
Marking	B7
Code	T110
Basic ordering unit (pieces)	3000

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	_	_	V	Ic=-50 μA
Collector-emitter breakdown voltage	BVceo	-50		_	V	Ic=-1mA
Emitter-base breakdown voltage	BVebo	-5	_	_	V	l∈=-50 μ A
Collector cutoff current	Ісво	-		-0.5	μA	V _{CB} =-50V
Emitter cutoff current	Ієво	_	-	-0.5	μA	V _{EB} =-4V
DC current transfer ratio	hre	100	250	600	_	Vce/lc=-5V/-1mA
Collector-emitter saturation voltage	VCE(sat)	T -	_	-0.3	٧	Ic/Is=-5mA/-0.25mA
Input resistance	Rı	3.29	4.7	6.11	kΩ	_

94S-849-A143T)

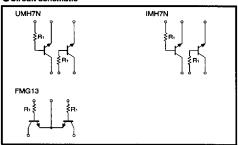
Unit

Digital Transistor (Dual Digital Transistors for Inveter Driver)

UMH7N / FMG13 / IMH7A

1) Two DTA143T transistors are housed in an UMT package.

Circuit schematic



■Absolute maximum ratings (Ta=25℃)

Param	neter	Symbol	Limits		
Collector-base v	oltage	Vcво	50	· ·	
Collector-emitter	voltage	VCEO	50	mA	
Emitter-base vol	tage	VEBO	5	ma	
Callector current		lc	100	mW #P	
Collector power	UMH7N	D-	Pc	150 (TOTAL)	η.
dissipation	FMG13, IMH7A	PC	300 (TOTAL)	٠ *	
Junction tempera	ature	Tj	150		
Storage tempera	ture	Tstg	-55~150		

- *1 120 mW per element must not be exceeded

Package, marking, and pa	IMH7A		
Туре	UMH7N	FMG13	UNTO
Package	UMT6	SMT5	T108
Marking	H7	G13	3000
Code	TR	T148	3000
Basic ordering unit (pieces)	3000	3000	

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	_	_	V	Ic=50 μA
Collector-emitter breakdown voltage	BVCEO	50	_ ·		V	Ic=1mA
Emitter-base breakdown voltage	BVeso	5	_	_	V	I∈=50 μ A
Collector cutoff current	Iceo	T -	_	0.5	μΑ	Vcs=50V
Emitter cutoff current	IEBO	—	_	0.5	μA	VE8=4V
DC current transfer ratio	hre	100	250	600	_	Vce/lc=5V/1mA
Collector-emitter saturation voltage	VCE(sat)	T -	_	0.3	٧	Ic/Is=5mA /0.25mA
Input resistance	Rı	3.29	4.7	6.11	kΩ	_ ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` ` `

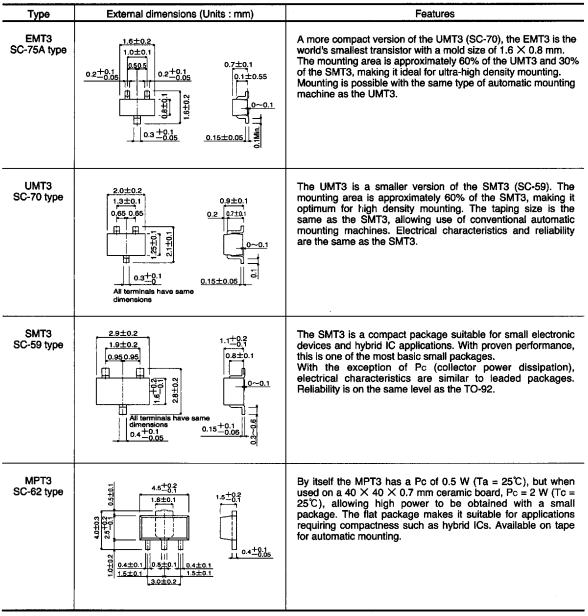
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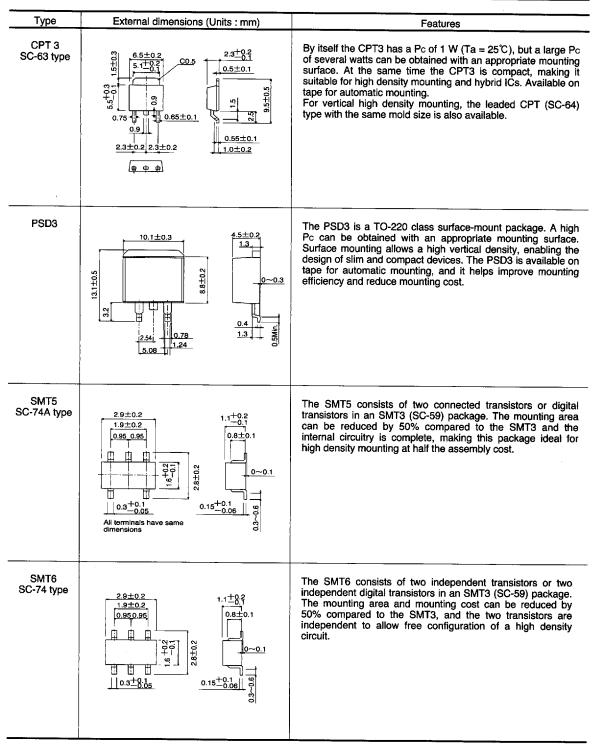
(94S-877-C143T)

Packages

ROHM has been manufacturing transistors since 1975. In the development of products, we constantly strive to anticipate the needs of our customers. Regarding packages, the demands of the market for compactness, low power consumption, low power dissipation and automatic mounting support are becoming ever greater, and we are strengthening our product development system to meet these needs.

Types and features of surface-mount packages

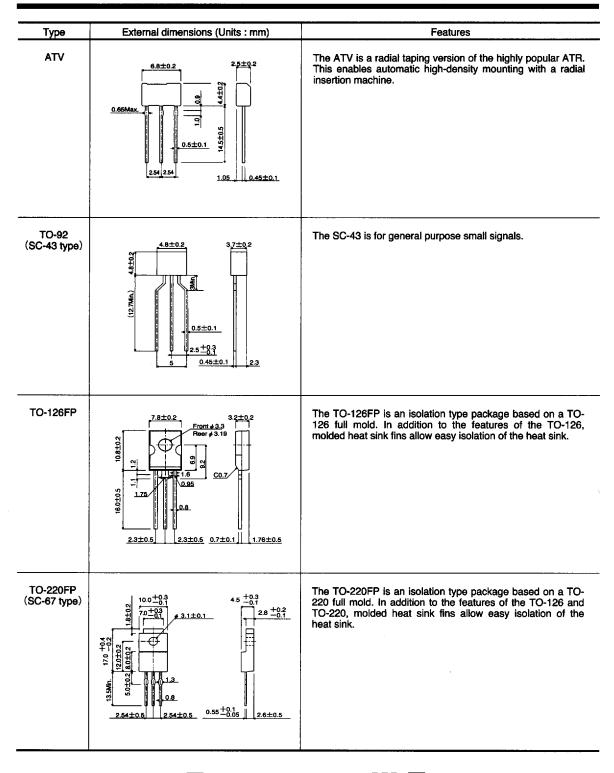




Туре	External dimensions (Units : mm)	Features
UMT5 SC-88A type	2.0±0.2 1,3±0.1 0,65 0,95 1,3±0.1 0,7 1,3±0.1 0,7 1,3±0.1 0,7 1,3±0.1 0,7 1,3±0.1 0,7 1,3±0.1 1,3±0.1 1,0,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0,0,0 1,0,0 1,0,0	The UMT5 consists of two connected transistors or digital transistors in a UMT3 (SC-70) package. The mounting area can be reduced by 50% compared to the UMT3 and the internal circuitry is completed, making this package ideal for high density mounting at half the assembly cost.
UMT6 SC-88 type	2.0±0.2 1.3±0.1 0.65 0.65 0.7 0.7 0.7 0.7 0.7 0.7 0.7 0.7	The UMT6 consists of two independent transistors or two independent digital transistors in a UMT (SC-70) package. The mounting area and mounting cost can be reduced by 50% compared to the UMT3, and the two transistors are independent to allow free configuration of a high density circuit.

●Types and features of leaded packages

Туре	External dimensions (Units : mm)	Features
SPT (SC-72 type)	2±0,2 0.45±0.15 0.45±0.15 0.5 0.45±0.15 0.5 0.45±0.15	The SPT is a smaller version of the conventional TO-92 type. The body size (3×4×2 mm³) has been reduced to 1/4 that of the TO-92 (5×5×4 mm³). The SPT is available on tape for automatic insertion, and less space is occupied on the printed circuit board than the TO-92. Reliability is the same as the TO-92.
FTR	0.65±0.1 2.4±0.2 0.55±0.1 0.55±0.1 0.45±0.1 0.45±0.1	SIL type with a height of 3.4 mm and a lead pitch of 2.54 mm.
FTL	0.65Max 2.4±0.2 0.65Max 2.4±0.2 0.5±0.1 0.5±0.1	The FTL is a radial taping version of the highly popular FTR. This enables automatic high-density mounting with a radial insertion machine.
ATR (SC-71 type)	0.65Max	SC-71type with a height of 4.4 mm and a Pc=1W type.



Type	External dimensions (Units : mm)	Features
TO-220FN	\$3.2±0.2	The TO-220FN features the same performance as the TO-220FP with approximately 2 mm less height, allowing the design of slimmer devices. Furthermore, the elimination of support pins in the fin (collector electrode) solves short-circuiting problems with neighboring components and the chassis. To make the height to the installation hole the same as the TO-220FP, it can be replaced as is from the TO-220FP.