HALOGEN

FREE



## Vishay General Semiconductor

## **Surface Mount Trench MOS Barrier Schottky Rectifier**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	3.0 A			
V <sub>RRM</sub>	60 V			
I <sub>FSM</sub>	60 A			
V <sub>F</sub> at I <sub>F</sub> = 3.0 A	0.48 V			
T <sub>J</sub> max.	150 °C			

### **FEATURES**

- Low profile package
- Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- · Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Not recommended for PCB bottom side wave mounting
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

### **MECHANICAL DATA**

Case: DO-220AA (SMP)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS compliant, and

commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test **Polarity:** Color band denotes the cathode end

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V3P6	UNIT	
Device marking code		V36		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	60	V	
Maximum DC forward current	I <sub>F</sub> <sup>(1)</sup>	3.0	А	
	I <sub>F</sub> <sup>(2)</sup>	2.4		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	60	А	
Voltage rate of change (rated V <sub>R</sub> )	dV/dt	10 000	V/µs	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150	°C	

#### Notes

- (1) Mounted on 8 mm x 8 mm pad areas, 1 oz. FR4 PCB
- (2) Free air, mounted on recommended copper pad area

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 3.0 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.53	0.63	- V
		T <sub>A</sub> = 125 °C		0.48	0.59	
Reverse current	V <sub>R</sub> = 60 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	900	μΑ
		T <sub>A</sub> = 125 °C		4	15	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	250	-	pF

#### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise specified)				
PARAMETER	SYMBOL	V3P6	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)	125	°C/W	
	R <sub>0JM</sub> (2)	15	]	

#### **Notes**

 $^{(1)}$  Free air, mounted on recommended PCB, 1 oz. pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

Units mounted on PCB with specific copper pad areas;  $R_{\theta JM}$  - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V3P6-M3/84A	0.024	84A	3000	7" diameter plastic tape and reel	
V3P6-M3/85A	0.024	85A	10 000	13" diameter plastic tape and reel	

## **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

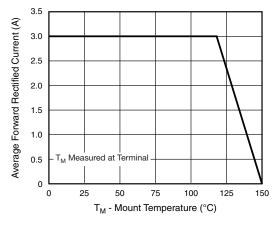


Fig. 1 - Maximum Forward Current Derating Curve

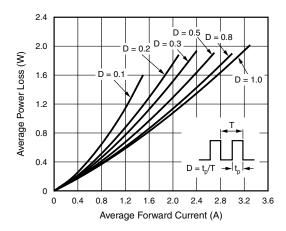


Fig. 2 - Forward Power Loss Characteristics



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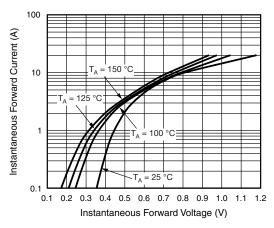


Fig. 3 - Typical Instantaneous Forward Characteristics

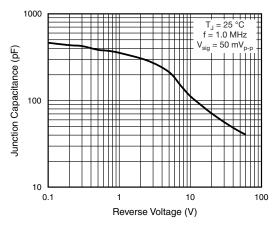


Fig. 5 - Typical Junction Capacitance

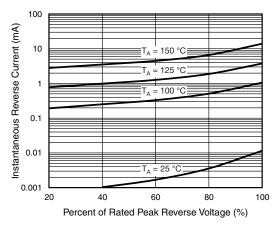


Fig. 4 - Typical Reverse Characteristics

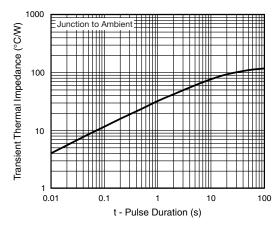
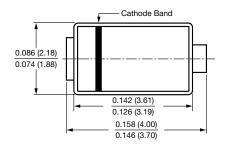
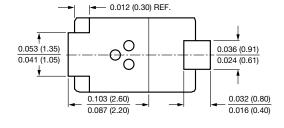


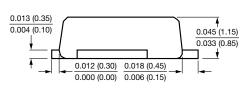
Fig. 6 - Typical Transient Thermal Impedance

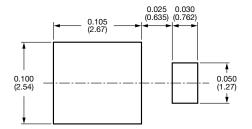
## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

## DO-220AA (SMP)













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