

Data selector/multiplexer

74ALS257/74ALS258

74ALS257 Quad 2-input data selector, non-inverting (3-State)

74ALS258 Quad 2-input data selector, inverting (3-State)

DESCRIPTION

The 74ALS257 is a quad 2-input multiplexer which selects 4 bits of data from one of two sources under the control of a common select input (S). The output enable input ($\overline{OE}$) is active when Low. When $\overline{OE}$ is High, all of the outputs (Y_n) are forced to a High impedance state (3-State) regardless of all other input conditions.

Moving data from two registers to a common output bus is a typical use of the 74ALS257. The state of the select input determines the particular register from which data comes.

The device is the logic implementation of 4-pole, 2-position switch where the position of the switch is determined by the logic levels supplied to the select input. The 74ALS258 is similar but has inverting outputs ($\overline{Y}_n$).

TYPE

TYPICAL PROPAGATION DELAY

TYPICAL SUPPLY CURRENT (TOTAL)

74ALS257

7.0ns

7mA

74ALS258

7.0ns

7mA

ORDERING INFORMATION

DESCRIPTION

ORDER CODE

COMMERCIAL RANGE
 $V_{CC} = 5V \pm 10\%$,
 $T_{amb} = 0^{\circ}C \text{ to } +70^{\circ}C$

DRAWING NUMBER

16-pin plastic DIP

74ALS257N, 74ALS258

SOT38-4

16-pin plastic SO

74ALS257D, 74ALS258D

SOT109-1

16-pin plastic SSOP
Type II

74ALS257DB, 74ALS258DB

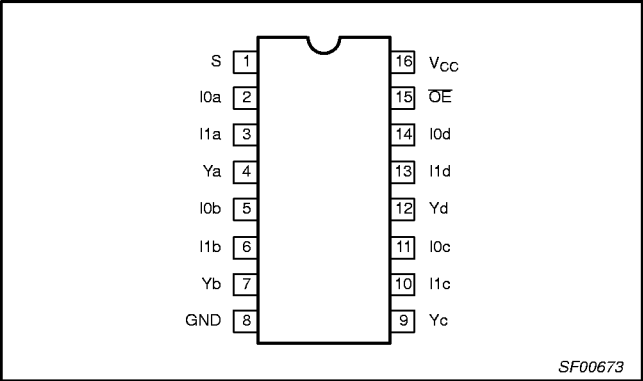
SOT338-1

INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

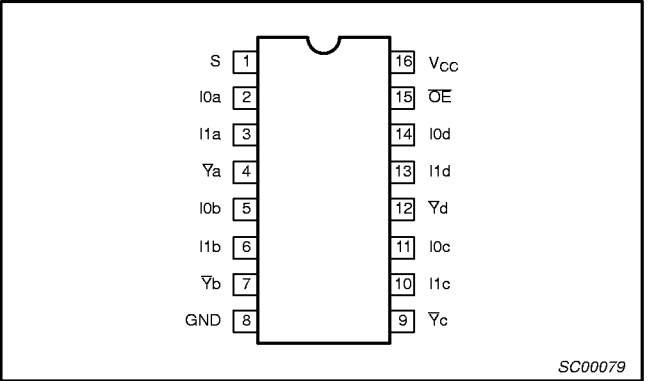
PINS	DESCRIPTION	74ALS (U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
I _{na} , I _{nb} , I _{nc} , I _{nd}	Data inputs	1.0/1.0	20μA/0.1mA
S	Select input	1.0/1.0	20μA/0.1mA
$\overline{OE}$	Enable input	1.0/1.0	20μA/0.1mA
Y _a – Y _d , $\overline{Y}_a$ – $\overline{Y}_d$	Data outputs	20/240	0.4mA/24mA

NOTE: One (1.0) ALS unit load is defined as: 20μA in the High state and 0.1mA in the Low state.

PIN CONFIGURATION – 74ALS257



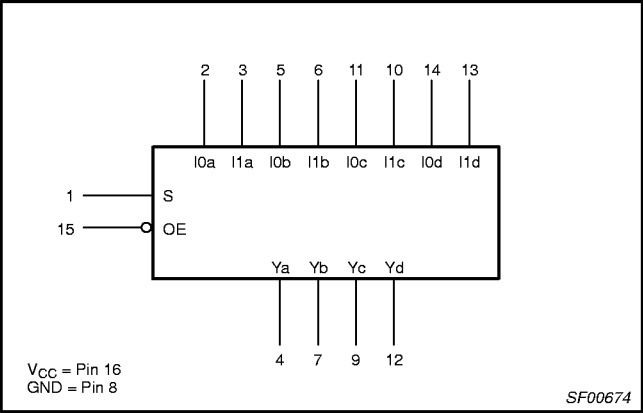
PIN CONFIGURATION – 74ALS258



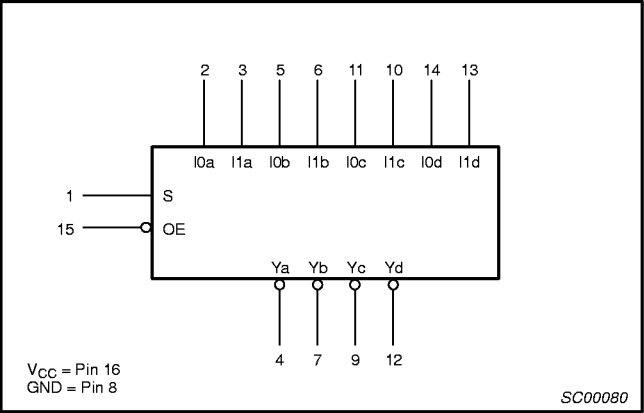
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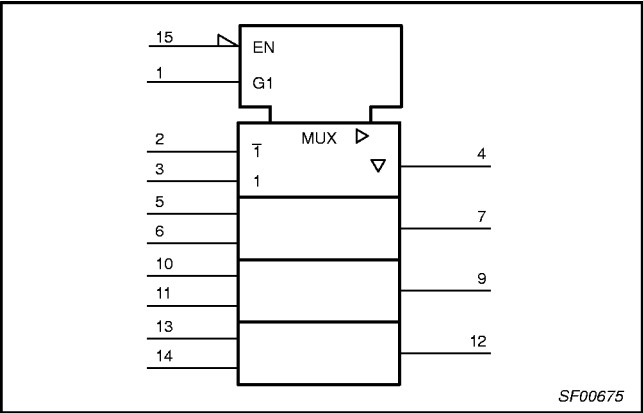
LOGIC SYMBOL – 74ALS257



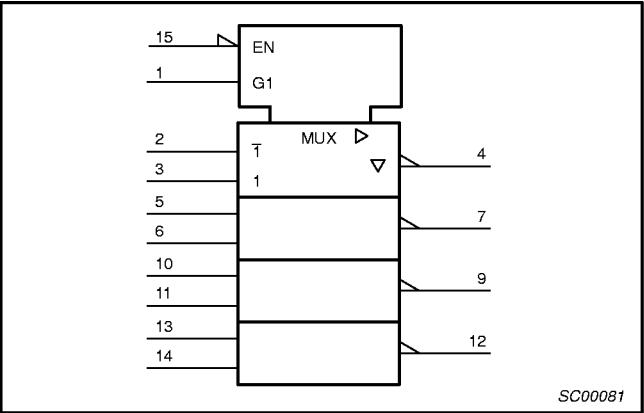
LOGIC SYMBOL – 74ALS258



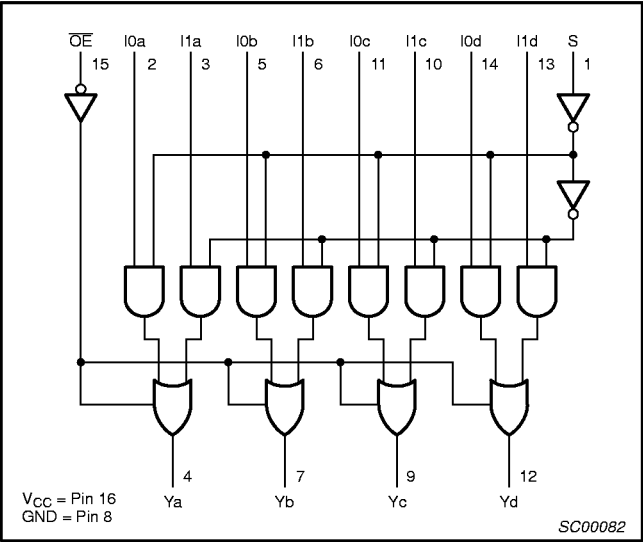
IEC/IEEE SYMBOL – 74ALS257



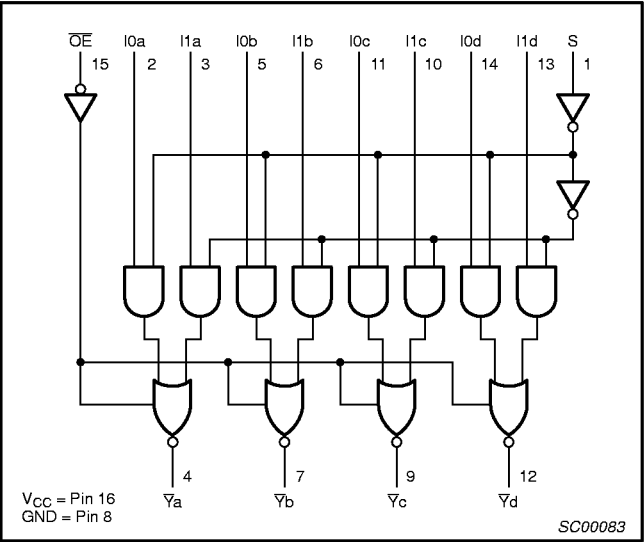
IEC/IEEE SYMBOL – 74ALS258



LOGIC DIAGRAM – 74ALS257



LOGIC DIAGRAM – 74ALS258



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FUNCTION TABLE – 74ALS257

INPUTS				OUTPUT
$\overline{OE}$	S	I0n	I1n	Yn
H	X	X	X	Z
L	L	L	X	L
L	L	H	X	H
L	H	X	L	L
L	H	X	H	H

H = High voltage level
 L = Low voltage level
 X = Don't care
 Z = High impedance "off" state

FUNCTION TABLE – 74ALS258

INPUTS				OUTPUT
$\overline{OE}$	S	I0n	I1n	$\overline{Yn}$
H	X	X	X	Z
L	L	L	X	H
L	L	H	X	L
L	H	X	L	H
L	H	X	H	L

H = High voltage level
 L = Low voltage level
 X = Don't care
 Z = High impedance "off" state

ABSOLUTE MAXIMUM RATINGS

(Operation beyond the limit set forth in this table may impair the useful life of the device.

Unless otherwise noted these limits are over the operating free air temperature range.)

SYMBOL	PARAMETER	RATING	UNIT
V_{CC}	Supply voltage	–0.5 to +7.0	V
V_{IN}	Input voltage	–0.5 to +7.0	V
I_{IN}	Input current	–30 to +5	mA
V_{OUT}	Voltage applied to output in High output state	–0.5 to V_{CC}	V
I_{OUT}	Current applied to output in Low output state	48	mA
T_{amb}	Operating free-air temperature range	0 to +70	°C
T_{stg}	Storage temperature range	–65 to +150	°C

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	LIMITS			UNIT
		MIN	NOM	MAX	
V_{CC}	Supply voltage	4.5	5.0	5.5	V
V_{IH}	High-level input voltage	2.0			V
V_{IL}	Low-level input voltage			0.8	V
I_{IK}	Input clamp current			–18	mA
I_{OH}	High-level output current			–2.6	mA
I_{OL}	Low-level output current			24	mA
T_{amb}	Operating free-air temperature range	0		+70	°C

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DC ELECTRICAL CHARACTERISTICS

(Over recommended operating free-air temperature range unless otherwise noted.)

SYMBOL	PARAMETER			TEST CONDITIONS ¹	LIMITS			UNIT
					MIN	TYP ²	MAX	
V_{OH}	High-level output voltage			$V_{CC} = \pm 10\%$, $V_{IL} = \text{MAX}$, $V_{IH} = \text{MIN}$	$I_{OH} = -0.4\text{mA}$	$V_{CC} - 2$		V
					$I_{OH} = \text{MAX}$	2.4	3.2	V
V_{OL}	Low-level output voltage			$V_{CC} = \text{MIN}$, $V_{IL} = \text{MAX}$, $V_{IH} = \text{MIN}$	$I_{OL} = 12\text{mA}$		0.25	0.40
					$I_{OL} = 24\text{mA}$		0.35	0.50
V_{IK}	Input clamp voltage			$V_{CC} = \text{MIN}$, $I_I = I_{IK}$		-0.73	-1.5	V
I_I	Input current at maximum input voltage			$V_{CC} = \text{MAX}$, $V_I = 7.0\text{V}$			0.1	mA
I_{IH}	High-level input current			$V_{CC} = \text{MAX}$, $V_I = 2.7\text{V}$			20	μA
I_{IL}	Low-level input current			$V_{CC} = \text{MAX}$, $V_I = 0.4\text{V}$			-0.1	mA
I_{OZH}	Off-state output current, High-level voltage applied			$V_{CC} = \text{MAX}$, $V_I = 2.7\text{V}$			20	μA
I_{OZL}	Off-state output current, Low-level voltage applied			$V_{CC} = \text{MAX}$, $V_I = 0.4\text{V}$			-20	μA
I_O	Output current ³			$V_{CC} = \text{MAX}$, $V_O = 2.25\text{V}$	-30		-112	mA
I_{CC}	Supply current (total)	74ALS257	I_{CCH}	$V_{CC} = \text{MAX}$		3	6	mA
			I_{CCL}			8	12	mA
			I_{CCZ}			9	14	mA
		74ALS258	I_{CCH}	$V_{CC} = \text{MAX}$		2.5	4	mA
			I_{CCL}			7	11	mA
			I_{CCZ}			9	13	mA

NOTES:

- For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
- All typical values are at $V_{CC} = 5\text{V}$, $T_{\text{amb}} = 25^\circ\text{C}$.
- The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS} .

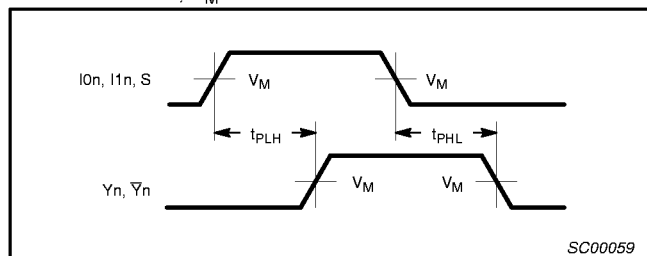
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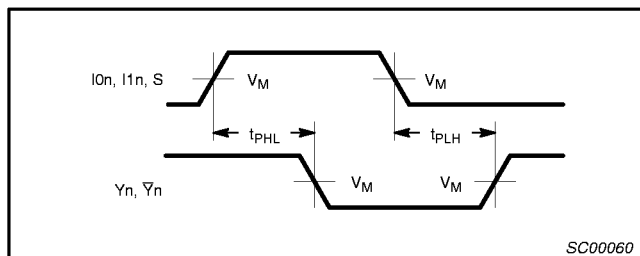
AC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER		TEST CONDITION	LIMITS		UNIT
				$T_{amb} = 0^{\circ}\text{C to } +70^{\circ}\text{C}$ $V_{CC} = +5.0\text{V} \pm 10\%$ $C_L = 50\text{pF}, R_L = 500\Omega$		
				MIN	MAX	
t_{PLH} t_{PHL}	Propagation delay I_{0n} or I_{1n} to Y_n	74ALS257	Waveform 1	2.0 2.0	9.0 9.0	ns
t_{PLH} t_{PHL}	Propagation delay S to Y_n		Waveform 1, 2	4.0 4.0	12.0 12.0	ns
t_{PZH} t_{PZL}	Output enable time $\overline{OE}$ to Y_n		Waveform 3 Waveform 4	3.0 4.0	11.0 12.0	ns
t_{PHZ} t_{PLZ}	Output disable time $\overline{OE}$ to Y_n		Waveform 3 Waveform 4	2.0 5.0	9.0 12.0	ns
t_{PLH} t_{PHL}	Propagation delay I_{0n} or I_{1n} to $\overline{Y}_n$	74ALS258	Waveform 1	2.0 2.0	8.0 8.0	ns
t_{PLH} t_{PHL}	Propagation delay S to $\overline{Y}_n$		Waveform 1, 2	4.0 4.0	12.0 12.0	ns
t_{PZH} t_{PZL}	Output enable time $\overline{OE}$ to Y_n		Waveform 3 Waveform 4	3.0 4.0	11.0 12.0	ns
t_{PHZ} t_{PLZ}	Output disable time $\overline{OE}$ to $\overline{Y}_n$		Waveform 3 Waveform 4	2.0 5.0	9.0 12.0	ns

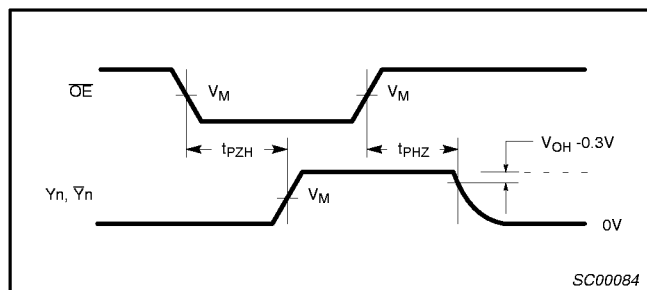
AC WAVEFORMS

For all waveforms, $V_M = 1.3\text{V}$.

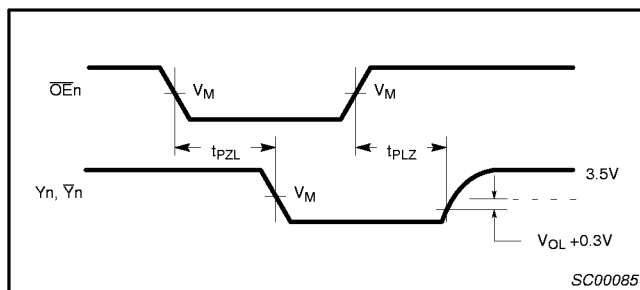
Waveform 1. Propagation Delay for Data and Select to Outputs



Waveform 2. Propagation Delay for Data and Select to Outputs



Waveform 3. 3-State Output Enable Time to High Level and Output Disable Time from High Level

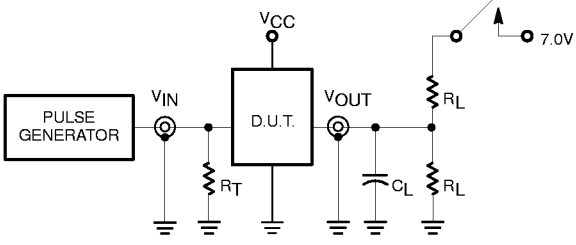


Waveform 4. 3-State Output Enable Time to Low Level and Output Disable Time from Low Level

Data selector/multiplexer

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TEST CIRCUIT AND WAVEFORMS

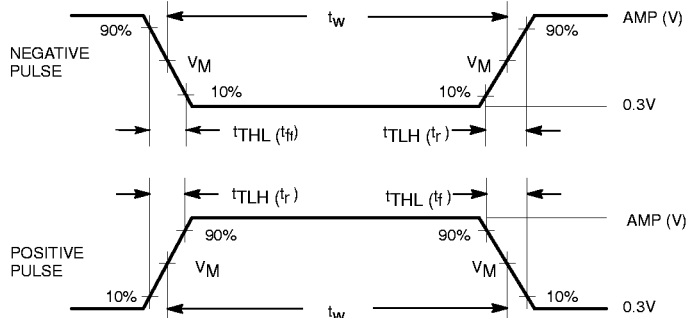


The diagram shows a test circuit for a 3-state output. A PULSE GENERATOR provides an input signal V_{IN} to the D.U.T. (Device Under Test). The input line is terminated with a resistor R_T to ground. The output of the D.U.T. is V_{OUT} , which is connected to a load resistor R_L and a load capacitor C_L to ground. A switch is connected to the output line, controlled by a 7.0V signal. The supply voltage V_{CC} is connected to the D.U.T. and the load resistor R_L .

Test Circuit for 3-State Outputs

SWITCH POSITION	
TEST	SWITCH
t_{PLZ} , t_{PZL}	closed
All other	open

DEFINITIONS:
 R_L = Load resistor; see AC electrical characteristics for value.
 C_L = Load capacitance includes jig and probe capacitance; see AC electrical characteristics for value.
 R_T = Termination resistance should be equal to Z_{OUT} of pulse generators.



The waveforms show the input pulse definition for negative and positive pulses. For a negative pulse, the signal transitions from 90% V_M to 10% V_M and back. The pulse width is t_w . The rise time $t_{TLH}(t_r)$ is from 10% to 90% V_M , and the fall time $t_{THL}(t_f)$ is from 90% to 10% V_M . The signal levels are 90% V_M and 10% V_M , with a reference level of 0.3V. For a positive pulse, the signal transitions from 10% V_M to 90% V_M and back. The pulse width is t_w . The rise time $t_{TLH}(t_r)$ is from 10% to 90% V_M , and the fall time $t_{THL}(t_f)$ is from 90% to 10% V_M . The signal levels are 90% V_M and 10% V_M , with a reference level of 0.3V.

Input Pulse Definition

Family	INPUT PULSE REQUIREMENTS					
	Amplitude	V_M	Rep.Rate	t_w	t_{TLH}	t_{THL}
74ALS	3.5V	1.3V	1MHz	500ns	2.0ns	2.0ns

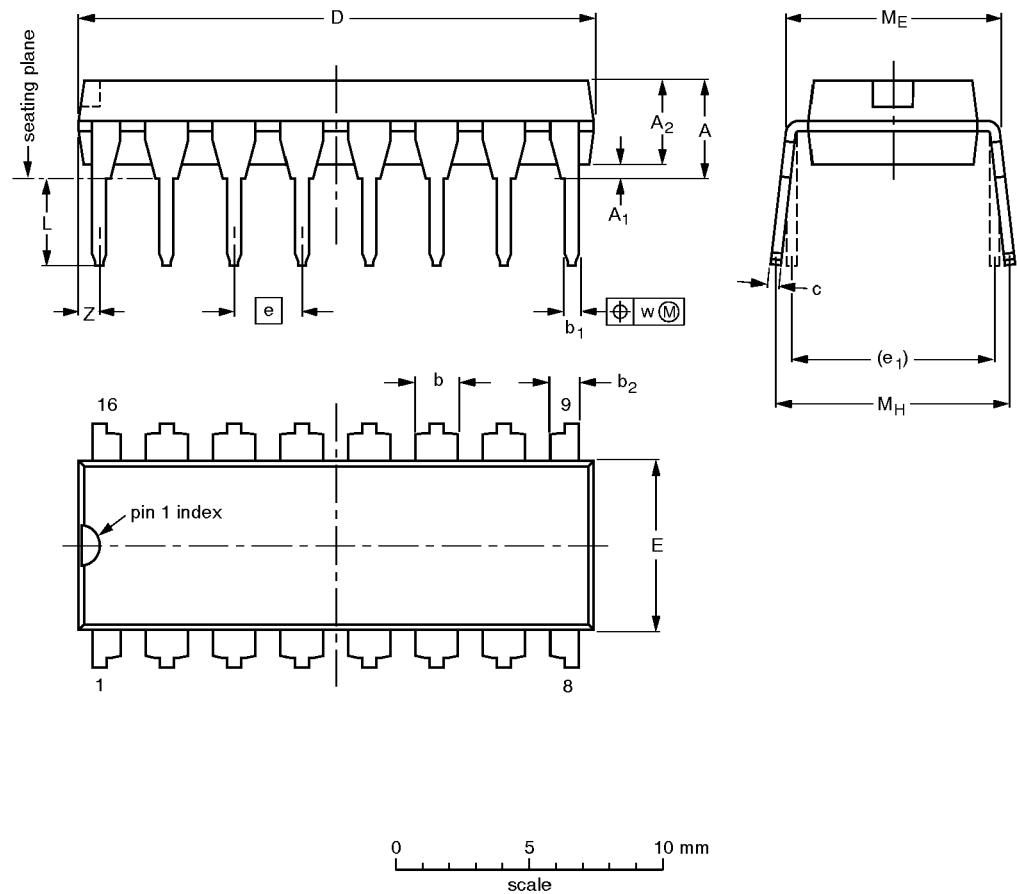
SC00072

Data selector/multiplexer

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DIP16: plastic dual in-line package; 16 leads (300 mil)

SOT38-4



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	b ₂	c	D ⁽¹⁾	E ⁽¹⁾	e	e ₁	L	M _E	M _H	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.30	0.53 0.38	1.25 0.85	0.36 0.23	19.50 18.55	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	0.76
inches	0.17	0.020	0.13	0.068 0.051	0.021 0.015	0.049 0.033	0.014 0.009	0.77 0.73	0.26 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.030

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

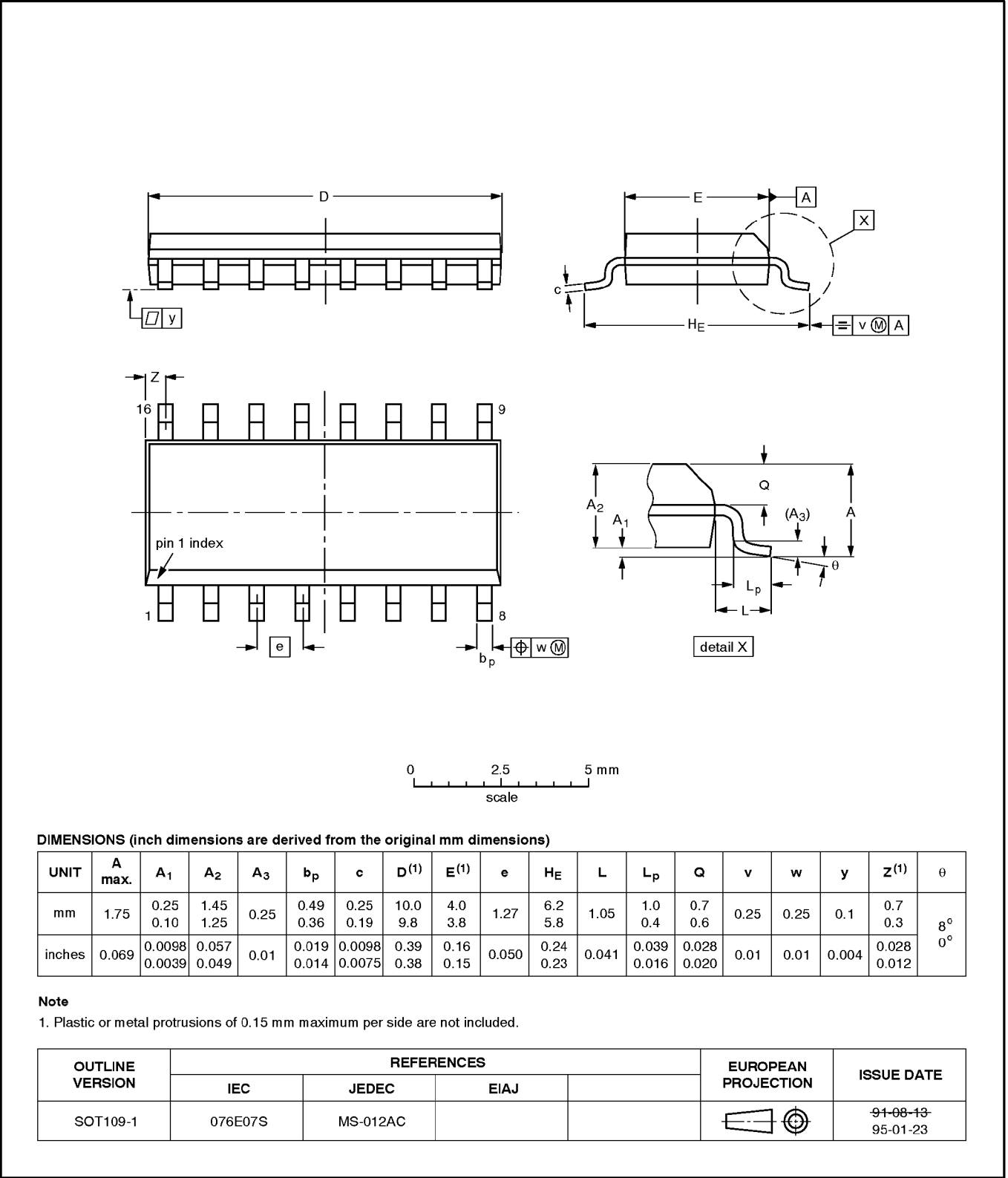
OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT38-4						92-11-17 95-01-14

Data selector/multiplexer

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SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1

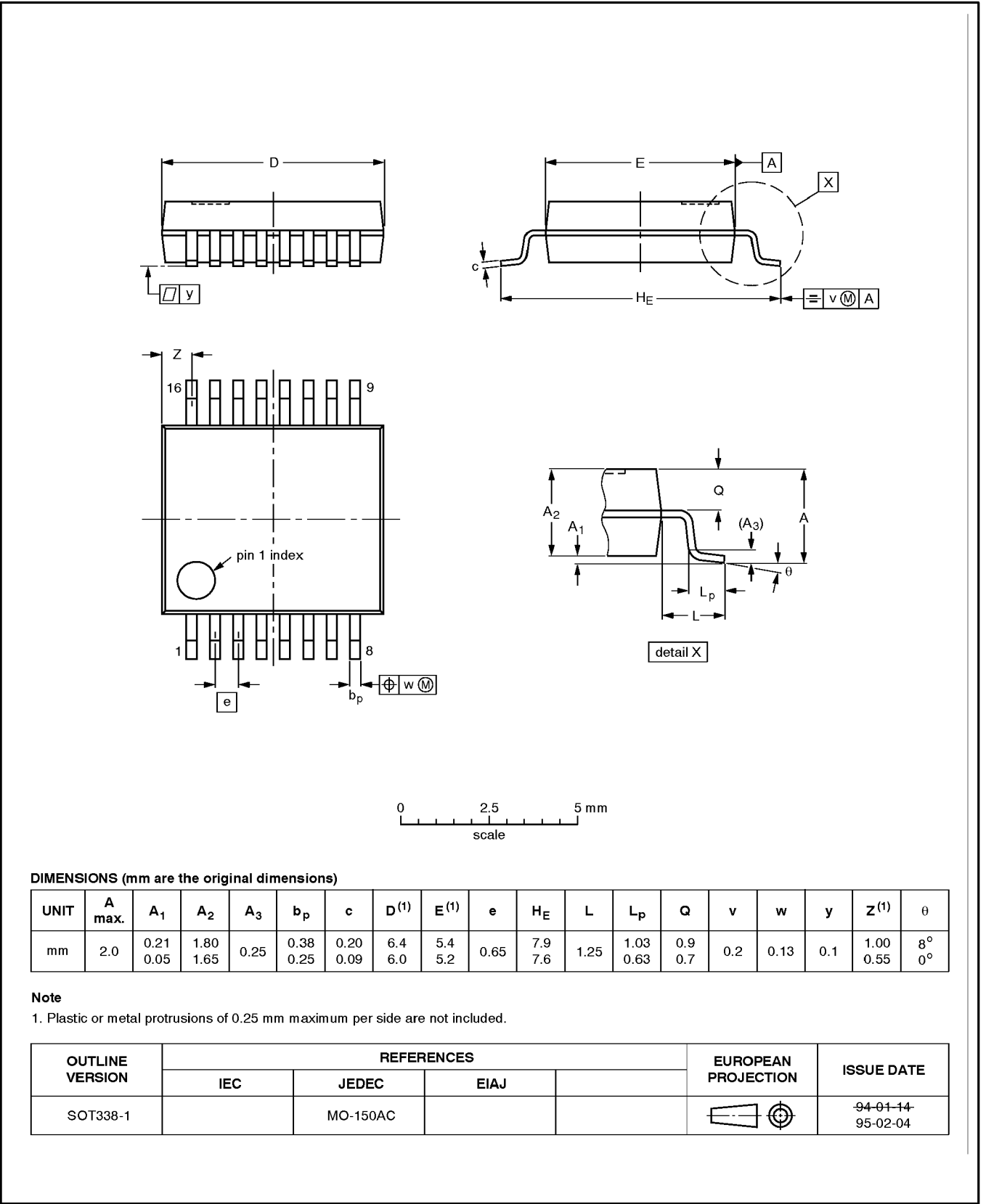


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SSOP16: plastic shrink small outline package; 16 leads; body width 5.3 mm

SOT338-1



Data selector/multiplexer

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DEFINITIONS		
Data Sheet Identification	Product Status	Definition
Objective Specification	Formative or in Design	This data sheet contains the design target or goal specifications for product development. Specifications may change in any manner without notice.
Preliminary Specification	Preproduction Product	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
Product Specification	Full Production	This data sheet contains Final Specifications. Philips Semiconductors reserves the right to make changes at any time without notice, in order to improve design and supply the best possible product.

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811 East Arques Avenue
P.O. Box 3409
Sunnyvale, California 94088-3409
Telephone 800-234-7381

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