

High-Performance Surface-Mount TTL Delay Modules

High-Performance 5-Tap TTL Delay Modules—1/8" Height

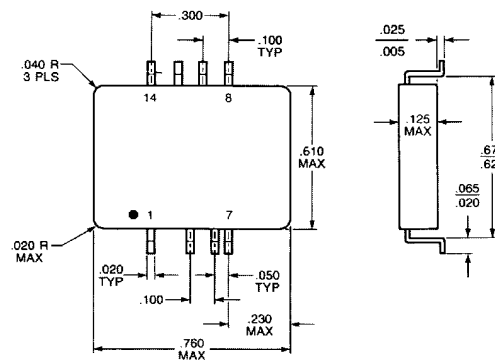
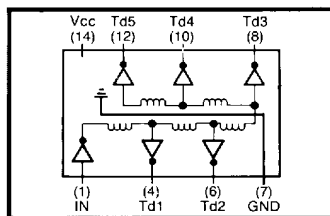
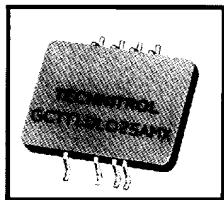
Part No.	Tap Delays (ns)					All Taps (Max.)	
	T _{D1}	T _{D2}	T _{D3}	T _{D4}	T _{D5}	T _{RO}	T _{FO}
GCTTLDL025AMX	5.0	10.0	15.0	20.0	25.0	2.0	2.0
GCTTLDL050AMX	10.0	20.0	30.0	40.0	50.0	2.0	2.0
GCTTLDL075AMX	15.0	30.0	45.0	60.0	75.0	2.0	2.0
GCTTLDL100AMX	20.0	40.0	60.0	80.0	100.0	2.0	5.0
GCTTLDL125AMX	25.0	50.0	75.0	100.0	125.0	2.0	6.0
GCTTLDL150AMX	30.0	60.0	90.0	120.0	150.0	2.0	7.0
GCTTLDL200AMX	40.0	80.0	120.0	160.0	200.0	2.0	8.0

Delay Characteristics measured @ V_{CC} = 5.0V, 25°C no load.

Delay Tolerance ± 2 ns or 5% (whichever is greater).

Rise time measured @ 0.8V to 2.0V levels.

For minimum input-pulse width, contact factory.



Notes

Only the pins specified in the schematics are provided with each package.

Pin numbers shown are for reference only and are not necessarily marked on unit.

Lead material is electro tin plated (alloy 42) or solder dipped.

All specifications are subject to change without notice.

High-Performance 5-Tap TTL Delay Modules—Gull Wing

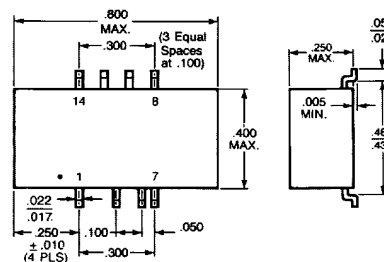
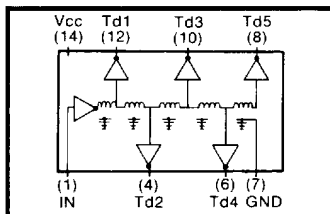
Part No.	Tap Delays (ns)					All Taps (Max.)	
	T _{D1}	T _{D2}	T _{D3}	T _{D4}	T _{D5}	T _{RO}	T _{FO}
GTTLDL025MX	5.0	10.0	15.0	20.0	25.0	2.0	2.0
GTTLDL050MX	10.0	20.0	30.0	40.0	50.0	2.0	2.0
GTTLDL075MX	15.0	30.0	45.0	60.0	75.0	2.0	2.0
GTTLDL100MX	20.0	40.0	60.0	80.0	100.0	2.0	2.0
GTTLDL125MX	25.0	50.0	75.0	100.0	125.0	2.0	2.0
GTTLDL150MX	30.0	60.0	90.0	120.0	150.0	2.0	5.0
GTTLDL200MX	40.0	80.0	120.0	160.0	200.0	2.0	5.0
GTTLDL250MX	50.0	100.0	150.0	200.0	250.0	2.0	9.0
GTTLDL500MX	100.0	200.0	300.0	400.0	500.0	2.0	9.0

Delay Characteristics measured @ V_{CC} = 5.0V, 25°C no load.

Delay Tolerance ± 2 ns or 5% (whichever is greater).

Rise time measured @ 0.8V to 2.0V levels.

For minimum input-pulse width, contact factory. Designs with pulse widths of less than 15% of delay are available.



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High-Performance Surface-Mount TTL Delay Modules—Cont'd.

High-Performance 5-Tap TTL Delay Modules—1/2" Sq.

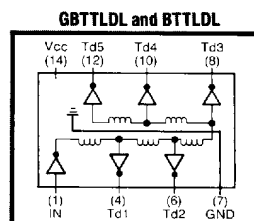
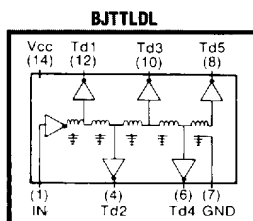
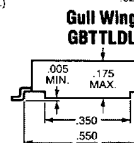
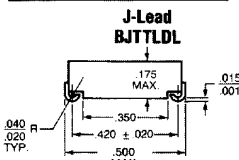
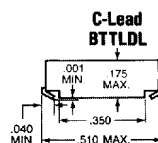
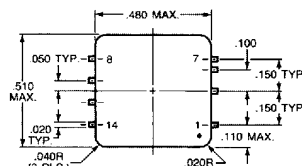
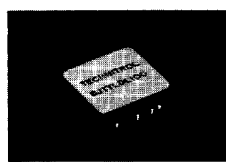
Part No.	Part No.	Part No.	Tap Delays (ns)					All Taps (Max.)	
			T _{D1}	T _{D2}	T _{D3}	T _{D4}	T _{D5}	T _{RO}	T _{FO}
BJTTLDL025	GBTTLDL025	BTTLDL025	5.0	10.0	15.0	20.0	25.0	2.0	2.0
BJTTLDL050	GBTTLDL050	BTTLDL050	10.0	20.0	30.0	40.0	50.0	2.0	2.0
BJTTLDL075	GBTTLDL075	BTTLDL075	15.0	30.0	45.0	60.0	75.0	2.0	2.0
BJTTLDL100	GBTTLDL100	BTTLDL100	20.0	40.0	60.0	80.0	100.0	2.0	5.0
BJTTLDL125	GBTTLDL125	BTTLDL125	25.0	50.0	75.0	100.0	125.0	2.0	6.0
BJTTLDL150	GBTTLDL150	BTTLDL150	30.0	60.0	90.0	120.0	150.0	2.0	7.0
BJTTLDL200	GBTTLDL200	BTTLDL200	40.0	80.0	120.0	160.0	200.0	2.0	8.0

Delay Characteristics measured @ V_{CC} = 5.0V, 25°C, no load.

Delay Tolerance ±2 ns or 5% (whichever is greater).

Rise time measured @ 0.8V to 2.0V levels.

For minimum input-pulse width, contact factory.



- Supports Schottky TTL, FAST and FACT-logics.
- Transfer-molded packaging—for highest reliability.
- Designed for leading edge timing. Trailing edge timing available.
- Compatible with Schottky TTL, FAST, FACT, TTL, ALS, AS and low-power Schottky TTL circuits.
- Pin compatible to 74/54 series 14—PIN DIP.
- Military models with temperature range -55 to +125°C and ceramic package IC to meet Mil-Std-883C but not screened to that specification, add Suffix "M" to part number.
- Military models as above, but with ceramic package IC screened to Mil-Std-883C and 38510. Add suffix "MX" to part number.
- Military models as "MX" above, but with in-house burn-in and thermal shock, add suffix "MY".
- Fanout: Logic 1—20 loads; logic 0—10 loads.
- Temperature coefficient ±2 ns or ±4% (whichever is greater) at maximum delay, 0 to 70°C.

High-Performance 5-Tap TTL Delay Modules—3/4" Sq.

Part No.	Tap Delays (ns)					All Taps (Max.)	
	T _{D1}	T _{D2}	T _{D3}	T _{D4}	T _{D5}	T _{RO}	T _{FO}
CTTLDL025	5.0	10.0	15.0	20.0	25.0	2.0	2.0
CTTLDL050	10.0	20.0	30.0	40.0	50.0	2.0	2.0
CTTLDL075	15.0	30.0	45.0	60.0	75.0	2.0	2.0
CTTLDL100	20.0	40.0	60.0	80.0	100.0	2.0	5.0
CTTLDL125	25.0	50.0	75.0	100.0	125.0	2.0	5.0
CTTLDL150	30.0	60.0	90.0	120.0	150.0	2.0	6.0
CTTLDL200	40.0	80.0	120.0	160.0	200.0	2.0	7.0

Delay Characteristics measured @ V_{CC} = 5.0V, 25°C, no load.

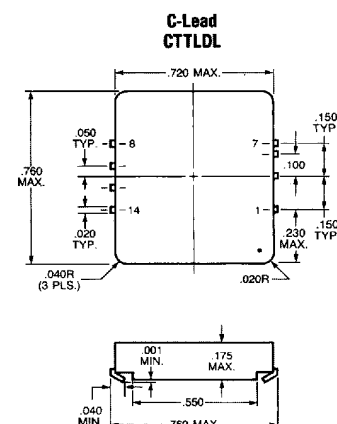
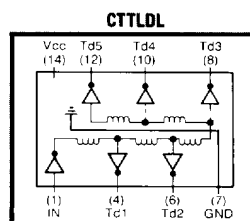
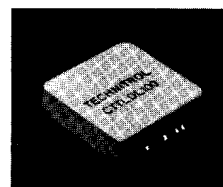
Delay Tolerance ±2 ns or 5% (whichever is greater).

Rise time measured @ 0.8V to 2.0V levels.

For minimum input-pulse width, contact factory.

Notes

Only the pins specified in the schematics are provided with each package.
Pin numbers shown are for reference only and are not necessarily marked on unit.
Lead material is electro tin plated (alloy 42) or solder dipped.
All specifications are subject to change without notice.



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