



## Description

The GD74F125 contains 4 buffers with 3-State outputs and is provided with an output control input ( $\bar{C}$ ) which is independent for each buffer.

## Function Table (each buffer)

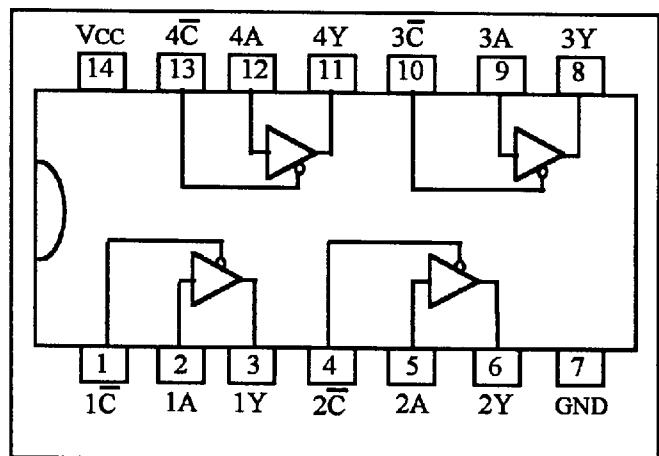
Inputs		Outputs
$\bar{C}$	A	Y
L	L	L
L	H	H
H	X	Z

X : Immaterial

Z : High Impedance

Output is off (disabled) when  $\bar{C}$  is high

## Pin Configuration



## Absolute Maximum Ratings

Storage Temperature .....	-65 °C ~ 150 °C
Ambient Temperature Under Bias.....	-55 °C ~ 125 °C
Junction Temperature Under Bias .....	-0.5 °C ~ 175 °C
Vcc Voltage .....	-0.5 V ~ 7.0 V
Input Voltage .....	-0.5 V ~ 7.0 V
Input Current .....	-30 mA ~ 5.0 mA
Output Voltage .....	-0.5 V ~ 5.5 V

Note : Absolute Maximum ratings are values beyond which the device maybe damaged or have its useful life impaired. Functional operation under these conditions is not implied.

## Recommended Operating Conditions

Free Air Ambient Temperature .....	0 °C ~ 70 °C
Supply Voltage .....	4.5 V ~ 5.5 V



## DC Electrical Characteristics over recommended operating free-air temperature range

SYMBOL	PARAMETER	Min	Typ	Max	UNIT	V <sub>CC</sub>	CONDITION	TEST CIRCUIT
V <sub>IH</sub>	Input High Voltage	2.0			V		-----	
V <sub>IL</sub>	Input Low Voltage			0.8	V		-----	
V <sub>CD</sub>	Input Clamp Diode Voltage			-1.2	V	Min	I <sub>IN</sub> = -18mA	See FIG. 18
V <sub>OH</sub>	Output High Voltage	2.4			V	4.5	I <sub>OH</sub> = -3 mA	See FIG. 19
		2.0				4.5	I <sub>OH</sub> = -12 mA	
		2.7				4.75	I <sub>OH</sub> = -3 mA	
		2.0				4.75	I <sub>OH</sub> = -15 mA	
V <sub>OL</sub>	Output Low Voltage			0.55	V	Min	I <sub>OL</sub> = 64 mA	
I <sub>I</sub>	Input High Current Breakdown Test			100	μA	0.0	V <sub>IN</sub> = 7.0 V	See FIG. 20
I <sub>IH</sub>	Input High Current			20	μA	Max	V <sub>IN</sub> = 2.7 V	
I <sub>IL</sub>	Input Low Current			-20	μA	Max	V <sub>IN</sub> = 0.5 V	
I <sub>OZH</sub>	Tri-State Output Off Current (High)			50	μA	Max	V <sub>OUT</sub> = 2.7 V	See FIG. 23
I <sub>OZL</sub>	Tri-State Output Off Current (Low)			-50	μA	Max	V <sub>OUT</sub> = 0.5 V	
I <sub>OS</sub>	Output Short Circuit Current	-100		-225	mA	Max	V <sub>OUT</sub> = 0 V	See FIG. 24
I <sub>CCH</sub> I <sub>CCL</sub> I <sub>CCZ</sub>	Supply Current		18.5 31.7 27.6	24.0 40.0 35.0	mA	Max	V <sub>OUT</sub> = High V <sub>OUT</sub> = Low V <sub>OUT</sub> = High Z	See FIG. 25

\* For I<sub>OS</sub>, Not more than one output should be shorted at a time, and duration should not exceed one second.



## AC Characteristics

SYMBOL	PARAMETER	TEST CONDITION						UNIT	
		TA = 25 °C		TA = 0 ~ 70°C		Vcc = 5.0 V			
		CL	= 50 pF	CL	= 50pF	CL	= 50pF		
		Min	Typ	Max	Min	Typ	Max		
tPLH tPHL	Propagation Delay	2.0 3.0	4.0 4.6	6.0 7.5	2.0 3.0	-- --	6.5 8.0	ns ns	
tpZH tpZL	Output Enable Time	3.5 3.5	4.7 5.3	7.5 8.0	3.0 3.5	-- --	8.5 9.0	ns ns	
tPHZ tPLZ	Output Disable Time	1.5 1.5	3.9 4.0	5.5 6.0	1.5 1.5	-- --	6.0 6.5	ns ns	



**LG Semicon. Co., LTD.**

