

## ECL Surface-Mount Delay Modules

T-47-17

## 10k ECL Programmable Delay Modules

Part No.	Step Delay ns $\pm$ ns	Max. Delay ns $\pm$ ns	Output Rise Time Max. (ns)
GECLPG301MX	1.0 $\pm$ .4	10.0 $\pm$ 1.5	3.6
GECLPG302MX	2.0 $\pm$ .6	17.0 $\pm$ 1.5	3.6
GECLPG303MX	3.0 $\pm$ 1.0	24.0 $\pm$ 1.5	3.6
GECLPG304MX	4.0 $\pm$ 1.0	31.0 $\pm$ 2.0	3.6
GECLPG305MX	5.0 $\pm$ 1.5	38.0 $\pm$ 2.0	3.6
GECLPG306MX	6.0 $\pm$ 1.5	45.0 $\pm$ 2.5	3.6
GECLPG307MX	7.0 $\pm$ 1.5	52.0 $\pm$ 2.5	3.6
GECLPG308MX	8.0 $\pm$ 1.5	59.0 $\pm$ 3.0	3.6
GECLPG309MX	9.0 $\pm$ 1.5	66.0 $\pm$ 3.5	3.6
GECLPG310MX	10.0 $\pm$ 1.5	73.0 $\pm$ 4.0	3.6

Delay characteristics measured at  $V_{ee} = -5.2 \pm .01$  Vdc,  $T_a = 25^\circ\text{C}$ , no load.

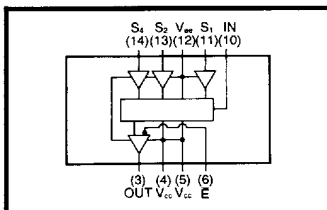
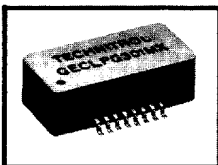
All delay times measured @  $-1.3\text{V}$  level.

500 linear FPM airflow and output terminated with 50 ohm to  $-2.0$  Vdc.

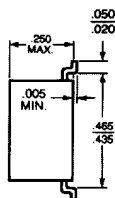
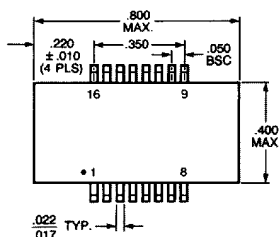
Rise time measured from 20 to 80% of output pulse..

For minimum input-pulse width, contact factory.

Input is internally terminated, therefore the delay line driver does not require a pull-down resistor.



Note: All unused pins shall have no electrical or capacitive type of connection.



- ▶ 10k ECL logic levels.
- ▶ 3-bit binary gives 7 equal step delays.
- ▶ Available in 10 step delays from 1 ns to 10 ns.
- ▶ Low inherent delay ( $T_o$ );  $T_o = 3.0 \pm 1.5$  ns.
- ▶ Temperature range:  $-30$  to  $+85^\circ\text{C}$ .
- ▶ Fanout: 70 ECL loads.
- ▶ MX suffix designates military models with temperature range  $-55$  to  $+125^\circ\text{C}$  and ceramic package IC screened to Mil-Std-883C and 38510.
- ▶ Military models as "MX" above, but with in-house burn-in and thermal shock, add suffix "MY".

**Notes**

Pin numbers shown are for reference only and are not necessarily marked on unit.

Lead material is electro tin plated (alloy 42) or solder dipped.

All specifications are subject to change without notice.

**Technitrol®**