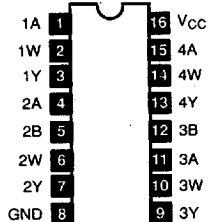


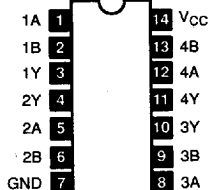
INTEGRATED CIRCUITS – TTL

(TRANSISTOR TRANSISTOR LOGIC)

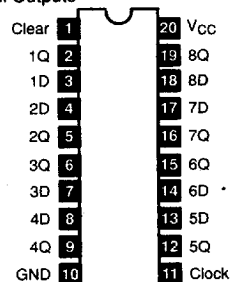
NTE74265 16-Lead DIP, See Diag. 249
Quad Complementary Output Elements



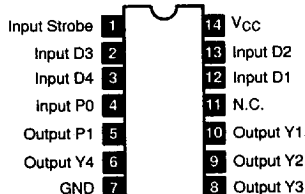
NTE74LS266 14-Lead DIP, See Diag. 247
Quad 2-Input Exclusive NOR Gate
*/Open Collector Outputs



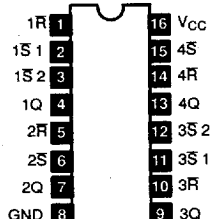
NTE74HC273, NTE74LS273 20-Lead DIP, See Diag. 294
Octal D-Type Flip-Flop */Common Clock
& Single Rail Outputs



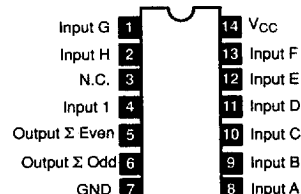
NTE74278 14-Lead DIP, See Diag. 247
4-Bit Cascadable Priority Register
*/Latched Data Inputs & Priority Output Gating



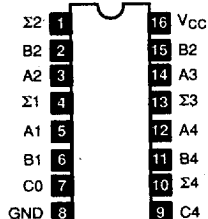
NTE74279, NTE74LS279 16-Lead DIP, See Diag. 249
Quad Set-Reset Latch */Diode-Clamped
Inputs & Totem Pole Outputs



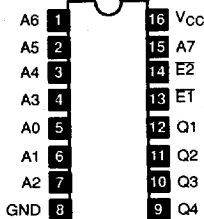
NTE74LS280, NTE74S280 14-Lead DIP, See Diag. 247
9-Bit Odd/Even Parity Generator/Checker
(N-Bit Cascadable)



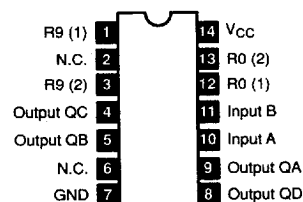
NTE74LS283 16-Lead DIP, See Diag. 249
4-Bit Binary Full Adder



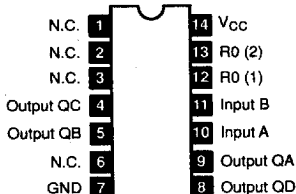
NTE74S287 16-Lead DIP, See Diag. 249
Bipolar 1024-Bit PROM, 50ns Max,
Addressable Access



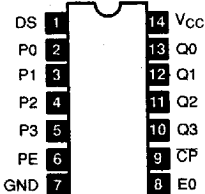
NTE74LS290 14-Lead DIP, See Diag. 247
Decade Counter (Divide by 2 & 5)



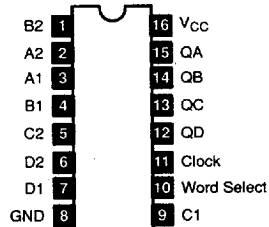
NTE74LS293 14-Lead DIP, See Diag. 247
4-Bit Binary Counter (Divide by 2 & 8)



NTE74LS295, NTE74LS295A 14-Lead DIP, See Diag. 247
4-Bit Bidirectional Universal Shift Register



NTE74298, NTE74LS298 16-Lead DIP, See Diag. 249
Quad 2-Input Multiplexer */Storage



See Diagrams, beginning on Page 1-293