# **NTGS3446**

# Power MOSFET 5 Amps, 20 Volts

# N-Channel TSOP-6

#### **Features**

- Ultra Low R<sub>DS(on)</sub>
- Higher Efficiency Extending Battery Life
- Logic Level Gate Drive
- Diode Exhibits High Speed, Soft Recovery
- Avalanche Energy Specified
- IDSS and VDS(on) Specified at Elevated Temperature

#### **Applications**

- Power Management in portable and battery–powered products, i.e. computers, printers, PCMCIA cards, cellular and cordless
- Lithium Ion Battery Applications
- Note Book PC

#### **MAXIMUM RATINGS** (T<sub>C</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-Source Voltage	VDSS	20	Vdc
Gate-Source Voltage - Continuous	VGS	±20	Vdc
Drain – Continuous – Continuous @ 70°C – Single Pulse (t <sub>p</sub> ≤10 μs)	I <sub>D</sub> I <sub>DM</sub>	5.8 TBD 20	Adc
Total Power Dissipation	PD	1.6	Watts
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to 150	°C
Single Drain–to–Source Avalanche Energy – Starting $T_J$ = 25°C ( $V_{DD}$ = 20 Vdc, $V_{GS}$ = 4.5 Vdc, $I_L$ = 5.8 A, L = TBD mH, $R_G$ = 25 $\Omega$ )	E <sub>AS</sub>	TBD	mJ
Thermal Resistance Junction-to-Ambient (Note 1.) Steady State Junction-to-Ambient (Note 2.) Junction-to-Lead Steady State	R <sub>θ</sub> JA R <sub>θ</sub> JA R <sub>θ</sub> JL	TBD TBD TBD	°C/W

- When surface mounted to Min Pad.
- 2. When surface mounted to 1" x 1" FR4 Board.

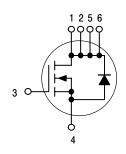


# ON Semiconductor™

http://onsemi.com

# 5 AMPERES 20 VOLTS RDS(on) = 45 m $\Omega$

#### N-Channel



#### MARKING DIAGRAM

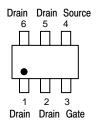


TSOP-6 CASE 318G STYLE 1



W = Work Week

#### **PIN ASSIGNMENT**



#### **ORDERING INFORMATION**

Device	Package	Shipping
NTGS3446T1	TSOP-6	3000 Tape & Reel

## **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

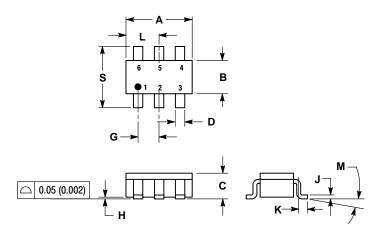
С	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Drain-to-Source Breakdown V (VGS = 0 Vdc, I <sub>D</sub> = 0.25 mAr Temperature Coefficient (Pos	V(BR)DSS	20 -	– TBD	- -	Vdc mV/°C	
Zero Gate Voltage Collector Cu (V <sub>DS</sub> = 20 Vdc, V <sub>GS</sub> = 0 Vdc (V <sub>DS</sub> = 20 Vdc, V <sub>GS</sub> = 0 Vdc	I <sub>DSS</sub>	- -	_ _	1.0 25	μAdc	
Gate-Body Leakage Current (\	IGSS(f) IGSS(r)	_ _	- -	100 100	nAdc	
ON CHARACTERISTICS (Note	1.)	•		•		•
Gate Threshold Voltage I <sub>D</sub> = 0.25 mA, V <sub>DS</sub> = V <sub>GS</sub> Temperature Coefficient (Neg	VGS(th)	0.6 -	0.9 TBD	1.2 -	Vdc mV/°C	
Static Drain-to-Source On-Resistance (VGS = 4.5 Vdc, ID = 5.3 Adc) (VGS = 2.5 Vdc, ID = 4.4 Adc)		V <sub>DS(on)</sub>	- -	36 44	45 55	mΩ
Forward Transconductance (V	9FS	10	17	-	mhos	
DYNAMIC CHARACTERISTICS						
Input Capacitance		C <sub>iss</sub>	-	930	TBD	pF
Output Capacitance	$(V_{DS} = 10 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, $ f = 1.0 MHz)	Coss	-	370	TBD	_
Transfer Capacitance	]	C <sub>rss</sub>	-	105	TBD	
SWITCHING CHARACTERISTIC	<b>CS</b> (Note 2.)					
Turn-On Delay Time		<sup>t</sup> d(on)	_	8.6	TBD	ns
Rise Time	$(V_{DD} = 10 \text{ Vdc}, I_D = 1.0 \text{ Adc},$	t <sub>r</sub>	_	14	TBD	
Turn-Off Delay Time	$V_{GS}$ = 4.5 Vdc, R <sub>L</sub> = 10 Ω $R_{G}$ = 6.0 Ω)	t <sub>d</sub> (off)	-	57	TBD	
Fall Time	]	t <sub>f</sub>	_	54	TBD	
Gate Charge		QT	-	11	15	nC
	$(V_{DS} = 10 \text{ Vdc}, I_{D} = 5.8 \text{ Adc}, V_{GS} = 4.5 \text{ Vdc})$	Q <sub>1</sub>	_	2.4	-	
	VGS = 4.0 Vd6/	Q <sub>2</sub>	_	2.4	-	
SOURCE-DRAIN DIODE CHAR	ACTERISTICS	•		•		•
Forward On–Voltage (Note 1.)	$(I_S = 1.7 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 1.7 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 85^{\circ}\text{C})$	V <sub>SD</sub>	- -	0.74 TBD	1.1 -	Vdc
Reverse Recovery Time		t <sub>rr</sub>	_	30	_	ns
		ta	_	14.5	_	1
	$(I_S = 1.7 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, \\ \text{dig/dt} = 100 \text{ A/}\mu\text{s})$	t <sub>b</sub>	_	15.5	_	1
Reverse Recovery Stored Charge	]	Q <sub>RR</sub>	_	0.01	_	μС

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.

## **NTGS3446**

## **PACKAGE DIMENSIONS**

#### TSOP-6 CASE 318G-02 ISSUE G



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETER.
  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
Α	2.90	3.10	0.1142	0.1220
В	1.30	1.70	0.0512	0.0669
С	0.90	1.10	0.0354	0.0433
D	0.25	0.50	0.0098	0.0197
G	0.85	1.05	0.0335	0.0413
Н	0.013	0.100	0.0005	0.0040
J	0.10	0.26	0.0040	0.0102
K	0.20	0.60	0.0079	0.0236
L	1.25	1.55	0.0493	0.0610
M	0 °	10°	0 °	10°
S	2.50	3.00	0.0985	0.1181

- STYLE 1:
  PIN 1. DRAIN
  2. DRAIN
  3. GATE
  4. SOURCE
  5. DRAIN
  6. DRAIN

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