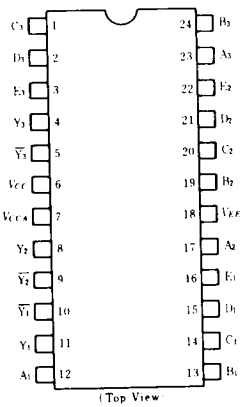
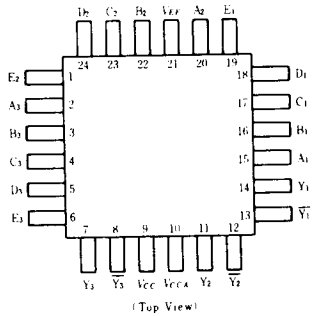
Triple 5-input OR/NOR Gates

■PIN ARRANGEMENT

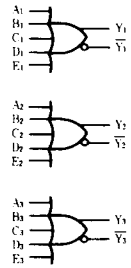
● HD100101



● HD100101F



■LOGIC DIAGRAM



■DC CHARACTERISTICS (V_{EE} = −4.2 to −4.8V, V_{CC} = V_{CCA} = GND, Ta = 0 to +85℃)

Item	Symbol	Test Condition	min	typ	max	Unit
Supply Current	I_{EE}	All input open	18	26	38	mA
Input Current	I_{IH}	V_{IN} = $V_{IH\ max}$	—	—	350	μA

Note) As for other items, refer to the "Common DC characteristics".

■AC CHARACTERISTICS (V_{EE} = −2.2 to −2.8V, V_{CC} = V_{CCA} = 2.0V)

● HD100101

Item	Symbol	Test Condition	0℃		25℃			85℃		Unit
			min	max	min	typ	max	min	max	
Propagation Delay Time	t_{PLH} t_{PHL}	See test circuit and waveforms	0.45	1.15	0.50	0.75	1.15	0.50	1.20	ns
Transition Time	t_{TLH} t_{THL}		0.30	1.10	0.35	0.70	1.10	0.35	1.10	ns

● HD100101F

Item	Symbol	Test Condition	0℃		25℃			85℃		Unit
			min	max	min	typ	max	min	max	
Propagation Delay Time	t_{PLH} t_{PHL}	See test circuit and waveforms	0.45	0.90	0.45	0.75	0.95	0.45	1.05	ns
Transition Time	t_{TLH} t_{THL}		0.40	1.10	0.45	0.70	1.10	0.45	1.10	ns

Notes) The circuits in a test socket or mounted on a printed circuit board and transverse air flow greater than 2.5m s (500 linear fpm) is maintained.