

HD100122

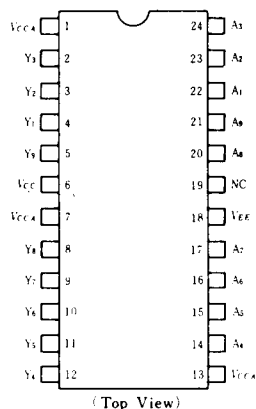
9-bit Buffer

The HD100122 contains nine independent, high speed, buffer gates each with a single input and a single output. The gates are non-inverting. These

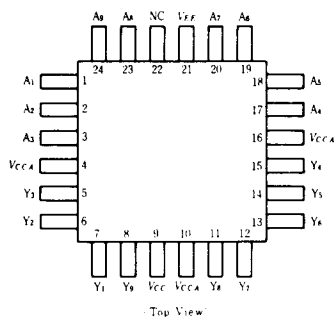
buffers are useful in bus oriented systems where minimal output loading or bus isolation is desired.

PIN ARRANGEMENT

● HD100122

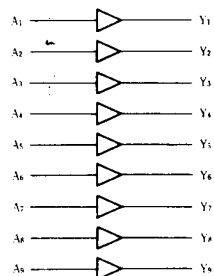


● HD100122F



Note) NC : No connection

LOGIC DIAGRAM



DC CHARACTERISTICS ($V_{EE} = -4.2$ to $-4.8V$, $V_{CC} = V_{CCA} = GND$, $T_a = 0$ to $+85^\circ C$)

Item	Symbol	Test Condition	min	typ	max	Unit
Supply Current	I_{EE}	All input open	47	70	95.5	mA
Input Current	I_{IH}	$V_{IN} = V_{IH\ max}$	—	—	350	μA

Note) As for other items, refer to the "Common DC Characteristics".

AC CHARACTERISTICS ($V_{EE} = -2.2$ to $-2.8V$, $V_{CC} = V_{CCA} = 2.0V$)

● HD100122

Item	Symbol	Test Condition	0°C		25°C			85°C		Unit
			min	max	min	typ	max	min	max	
Propagation Delay Time	t_{PLH} t_{PHL}	See test circuit and waveform	0.45	1.40	0.45	0.90	1.45	0.55	1.50	ns
Transition Time	t_{TLH} t_{THL}		0.45	1.40	0.45	0.90	1.40	0.45	1.40	ns

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Item	Symbol	Test Condition	0°C		25°C			85°C		Unit
			min	max	min	typ	max	min	max	
Propagation Delay Time	t_{PLH} t_{PHL}	See test circuit and waveform	0.45	1.10	0.45	0.70	1.10	0.45	1.15	ns
Transition Time	t_{TLH} t_{THL}		0.45	1.10	0.45	0.70	1.10	0.45	1.10	ns

Notes) The circuits in a test socket or mounted on a printed circuit board and transverse air flow greater than 2.5m/s (500 linear fpm) is maintained.