**Preferred Devices** 

# Product Preview

# Power MOSFET 6 Amps, 500 Volts

# N-Channel TO-220

Designed for high voltage, high speed switching applications in power supplies, converters, power motor controls and bridge circuits.

#### Features

- Higher Current Rating
- Lower RDS(on)
- Lower Capacitances
- Lower Total Gate Charge
- Tighter V<sub>SD</sub> Specifications
- Avalanche Energy Specified

## **Typical Applications**

- Switch Mode Power Supplies
- PWM Motor Controls
- Converters
- Bridge Circuits

#### **MAXIMUM RATINGS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-Source Voltage	VDSS	500	Vdc
Drain–Gate Voltage ( $R_{GS} = 1.0 M\Omega$ )	V <sub>DGR</sub>	500	Vdc
Gate–Source Voltage  - Continuous  - Non–Repetitive (t <sub>p</sub> ≤ 10 ms)	V <sub>GS</sub> V <sub>GS</sub>	±20 ±40	Vdc
Drain− Continuous @ T <sub>A</sub> 25°C − Continuous @ T <sub>A</sub> 100°C − Single Pulse (t <sub>p</sub> ≤ 10 μs)	I <sub>D</sub>	6 5.4 21	Adc Apk
Total Power Dissipation @ T <sub>A</sub> 25°C Derate above 25°C Total Power Dissipation @ T <sub>A</sub> 25°C (Note NO TAG)	PD	96 0.77 1.75	Watts W/°C Watts
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C
Single Drain-to-Source Avalanche Energy – Starting T <sub>J</sub> = 25°C (V <sub>DD</sub> = 100 V, V <sub>GS</sub> = 10 Vdc, I <sub>L</sub> (pk) = 6 A, L = 10 mH, V <sub>DS</sub> = 500 Vdc, R <sub>G</sub> = 25 Ω)	E <sub>AS</sub>	125	mJ
Thermal Resistance  – Junction–to–Case  – Junction–to–Ambient	R <sub>θ</sub> JC R <sub>θ</sub> JA	1.3 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C

<sup>1.</sup> Repetitive rating; pulse width limited by maximum junction temperature.

This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.



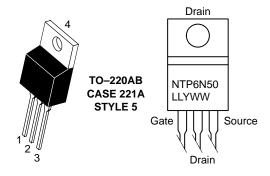
# ON Semiconductor™

http://onsemi.com

# 6 AMPERES 500 VOLTS RDS(on) = 1700 m $\Omega$

# N-Channel DO

# MARKING DIAGRAMS AND PIN ASSIGNMENTS



 NTP6N50
 = Device Code

 LL
 = Location Code

 Y
 = Year

 WW
 = Work Week

#### ORDERING INFORMATION

Device	Package	Shipping
NTP6N50	TO-220AB	50 Units/Rail

**Preferred** devices are recommended choices for future use and best overall value.

# **ELECTRICAL CHARACTERISTICS** ( $T_C = 25$ °C unless otherwise noted)

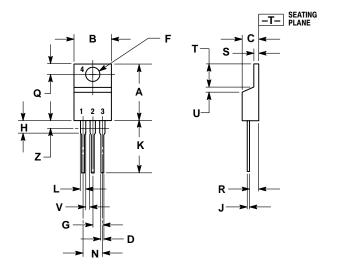
Ch	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Vo (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 250 μAdd Temperature Coefficient (Posi	V(BR)DSS	500 -	_ 590	_ _	Vdc mV/°C	
Zero Gate Voltage Drain Current (VDS = 500 Vdc, VGS = 0 Vdc) (VDS = 500 Vdc, VGS = 0 Vdc, TJ =125°C)		IDSS	- -	- -	10 100	μAdc
Gate-Body Leakage Current (V	GS = ±20 Vdc, V <sub>DS</sub> = 0 Vdc)	IGSS	-	_	±100	nAdc
ON CHARACTERISTICS (Note 2	.)					
Gate Threshold Voltage (VDS = VGS, ID = 250 μAdc) Temperature Coefficient (Neg	VGS(th)	2.0 -	2.7 6.4	4.0 -	Vdc mV/°C	
Static Drain-to-Source On-Res	sistance (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 3 Adc)	R <sub>DS(on)</sub>	-	1300	1700	mOhm
Static Drain-to-Source On-Res (VGS = 10 Vdc, $I_D$ = 6 Adc) (VGS = 10 Vdc, $I_D$ = 3 Adc, T		V <sub>DS(on)</sub>	- -	- -	12.2 11.0	V
Forward Transconductance (VD	S = 15 Vdc, I <sub>D</sub> = 3 Adc)	9FS	2.0	4.0	-	mhos
DYNAMIC CHARACTERISTICS						
Input Capacitance		C <sub>iss</sub>	_	520	730	pF
Output Capacitance	$(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C <sub>oss</sub>	-	170	240	
Transfer Capacitance	,	C <sub>rss</sub>	-	11	20	
SWITCHING CHARACTERISTIC	<b>S</b> (Note 3.)					
Turn-On Delay Time		<sup>t</sup> d(on)	-	7.0	10	ns
Rise Time	$(V_{DD} = 250 \text{ Vdc}, I_{D} = 6 \text{ Adc},$	t <sub>r</sub>	-	9.0	20	
Turn-Off Delay Time	$V_{GS} = 10 \text{ Vdc},$ $R_G = 9.1 \Omega)$	td(off)	-	20	40	
Fall Time		t <sub>f</sub>	-	10	20	
Gate Charge	(V <sub>DS</sub> = 400 Vdc, I <sub>D</sub> = 6 Adc, V <sub>GS</sub> = 10 Vdc)	Q <sub>T</sub>	-	10	20	nC
		Q <sub>1</sub>	-	2.0	-	1
		Q <sub>2</sub>	-	3.0	-	1
SOURCE-DRAIN DIODE CHARA	ACTERISTICS	•	•	•	•	•
Forward On-Voltage (Note 2.)	$(I_S = 6 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_S = 6 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_J = 125^{\circ}\text{C})$	V <sub>SD</sub>	- -	0.9 0.8	1.0	Vdc
Reverse Recovery Time	(I <sub>S</sub> = 6 Adc, V <sub>G</sub> S = 0 Vdc, dis/dt = 100 A/μs)	t <sub>rr</sub>	_	415		ns
		ta	-	100	-	
		t <sub>b</sub>	-	315	-	
Reverse Recovery Stored Charge		Q <sub>RR</sub>	_	1.8	_	μС

Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperature.

### **PACKAGE DIMENSIONS**

#### TO-220 THREE-LEAD TO-220AB

CASE 221A-09 **ISSUE AA** 



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

- STYLE 5:
  PIN 1. GATE
  2. DRAIN
  3. SOURCE
  4. DRAIN

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