

HD-6433

CMOS Quad Bus Separator/Driver

> Pinout TOP VIEW

> > 16 Dvcc 15 144

14 114

13 74B

11 34

10 3_B

Features

- SINGLE POWER SUPPLY
- HIGH NOISE IMMUNITY
- INDUSTRIAL AND MILITARY GRADES
- DRIVE CAPACITY
- SOURCE CURRENT
- SINK CURRENT
- PROPAGATION DELAY

300pF

4mA

6mA

50nsec MAX,

Truth Table

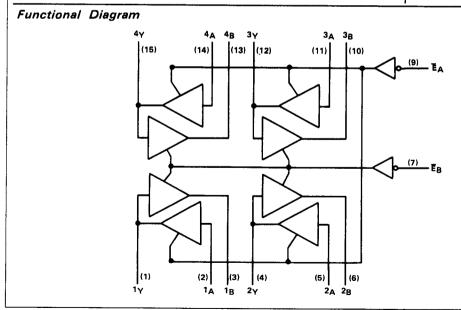
| CONTROL INPUTS | | FUNCTION | | |
|-------------------|----|----------|---|-----|
| ĒĄ | ĒB | А | В | Y |
| L | L | 1 | 0 | 0 |
| L | н | 1 | D | 0 |
| н | L | Þ | 0 | · · |
| н | н | ISOLATED | | |

i = Input, O = Output, D = Disconnected

Description

The HD-6433 is a self-aligned silicon gate CMOS bus separator/driver. This circuit consists of 8 drivers organized as 4 pairs of bus separators which allow a unidirectional input bus and a unidirectional output bus to be interfaced with a bi-directional bus.

Outputs guaranteed valid at VCC 2.0V for Battery Backup Applications.



CAUTION: Electronic devices are sensitive to electrostatic discharge. Proper I.C. handling procedures should be followed.