

# HD29413

Quadruple Differential Line Receivers With 3 State Outputs

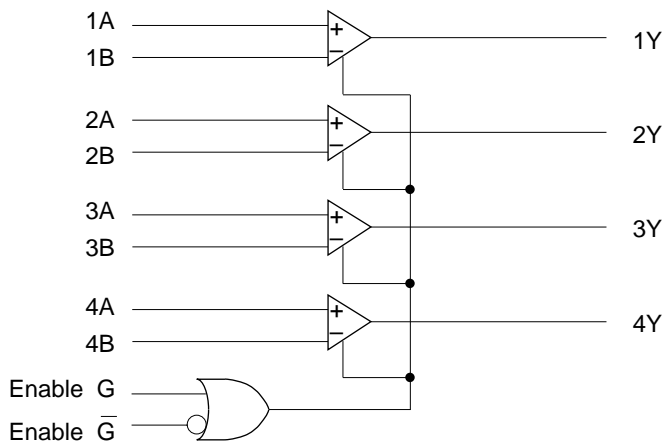
# HITACHI

ADE-205-582 (Z)  
1st. Edition  
Dec. 2000

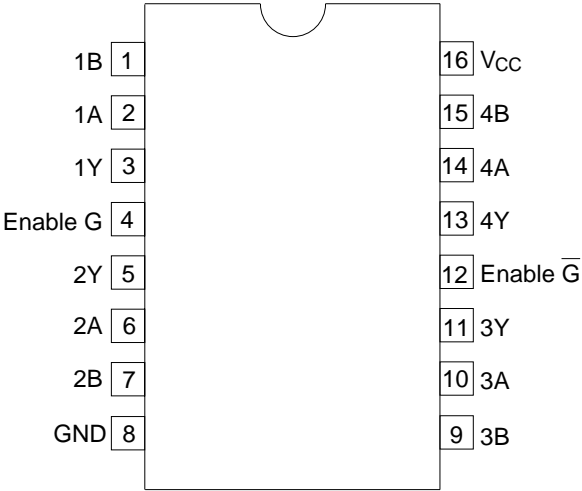
## Description

The HD29413 features quadruple differential line receivers designed to meet the spec of EIA RS-422A and RS-423A. The device operates from a single 5 V power supply. The enable function is common to all four receivers and offer a choice of active high or active low inputs. (Complementary output enable input.) Fail safe circuit guarantees the outputs always at the high level when the inputs are open.

## Logic Diagram



Pin Arrangement



(Top view)

Function Table

Differential Input $V_{IA} - V_{IB}$	Enable		Output
	$G$	$\bar{G}$	$Y$
+	H	X	H
	X	L	H
—	H	X	L
	X	L	L
X	L	H	Z

- H : High level  
L : Low level  
X : Irrelevant  
Z : High impedance

## Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply Voltage	$V_{CC}^{*1}$	+7	V
In Phase Input Voltage	$V_{IC}^{*2}$	−25 to +25	V
Differential Input Voltage	$V_{ID}^{*3}$	0 to +25	V
Enable Input Voltage	$V_{IN}$	+7	V
Output Sink Current	$I_O$	+50	mA
Operating Temperature	Topr	0 to +70	°C
Storage Temperature	Tstg	−65 to +150	°C

- Notes: 1. All voltage values except for differential input voltage are with respect to ground terminal.  
 2.  $V_{IC} = 1/2 (V_{IA} + V_{IB})$   $|V_{ID}| = |V_{IA} - V_{IB}|$   
 3. Differential input voltage is measured at the noninverting input with respect to the corresponding inverting input.  
 4. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

## Recommended Operating Conditions

Item	Symbol	Min	Typ	Max	Unit
Supply Voltage	$V_{CC}$	4.75	5.0	5.25	V
In Phase Input Voltage	$V_{IC}$	−7	—	+7	V
Differential Input Voltage	$V_{ID}$	+0.3	—	+6.0	V
Output Current	$I_{OH}$	—	—	−440	μA
	$I_{OL}$	—	—	8	mA
Operating Temperature	Topr	0	—	70	°C

Electrical Characteristics (Ta = 0 to + 70°C)

Item	Symbol	Min	Typ*1	Max	Unit	Conditions
Differential Input High Threshold Voltage	V <sub>TH</sub>	—	—	0.3	V	V <sub>CC</sub> = 5 V ±5 %, V <sub>OH</sub> ≥ 2.7 V, I <sub>OH</sub> = −440 μA V <sub>IC</sub> = −7 to +7 V
Differential Input Low Threshold Voltage	V <sub>TL</sub>	—	—	−0.3	V	
Enable Input Voltage	V <sub>IH</sub>	2.0	—	—	V	V <sub>OL</sub> ≤ 0.4 V, I <sub>OL</sub> = 4 mA
	V <sub>IL</sub>	—	—	0.8	V	
Enable Input Clamp Voltage	V <sub>IK</sub>	—	—	−1.5	V	V <sub>CC</sub> = 4.75 V, I <sub>IN</sub> = −18 mA
Output Voltage	V <sub>OH</sub>	2.7	—	—	V	V <sub>CC</sub> = 4.75 V V <sub>ID</sub> = 0.3 to 6 V I <sub>OH</sub> = −440 μA
	V <sub>OL</sub>	—	—	0.4	V	V <sub>IL</sub> ( $\overline{G}$ ) = 0.8 V V <sub>ID</sub> = I <sub>OL</sub> = 4mA
		—	—	0.45	V	V <sub>IH</sub> (G) = 2 V −0.3 to −6 V I <sub>OL</sub> = 8 mA
Off State (High impedance) Output Current	I <sub>OZ</sub>	—	—	20	μA	V <sub>CC</sub> = 5.25 V V <sub>O</sub> = 2.4 V
		—	—	−20	μA	V <sub>IL</sub> (G) = 0.8 V, V <sub>IH</sub> ( $\overline{G}$ ) = 2 V V <sub>O</sub> = 0.4 V
Line Input Current	I <sub>IN</sub>	—	—	2.2	mA	V <sub>CC</sub> = 5.25 V or V <sub>CC</sub> = 0 V V <sub>I</sub> = −10 V
		0	—	1.0	mA	
		0	—	−1.0	mA	
		—	—	−2.2	mA	
Enable Input Current	I <sub>I(EN)</sub>	—	—	100	μA	V <sub>CC</sub> = 5.25 V V <sub>I</sub> = 5.5 V
	I <sub>IH</sub>	—	—	20	μA	
	I <sub>IL</sub>	—	—	−0.36	mA	
Short Circuit Output Current	I <sub>OS</sub> *2	−15	—	−85	mA	V <sub>CC</sub> = 5.25 V, V <sub>O</sub> = 0 V
Supply Current	I <sub>CC</sub>	—	—	70	mA	V <sub>CC</sub> = 5.25 V, V <sub>I</sub> = 0 V (All Output Disable)

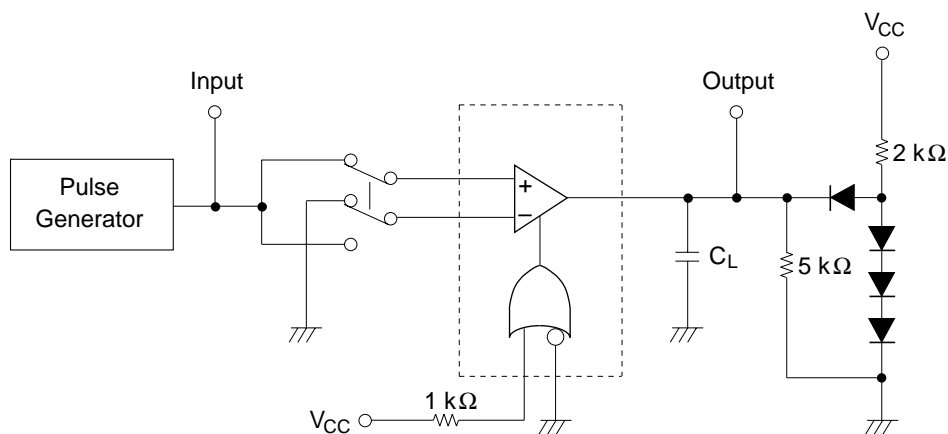
Notes: 1. All typical values are at V<sub>CC</sub> = 5 V, Ta = 25°C, V<sub>IC</sub> = 0  
2. Not more than one output should be shorted at a time.

Switching Characteristics (V<sub>CC</sub> = 5 V, Ta = 25°C)

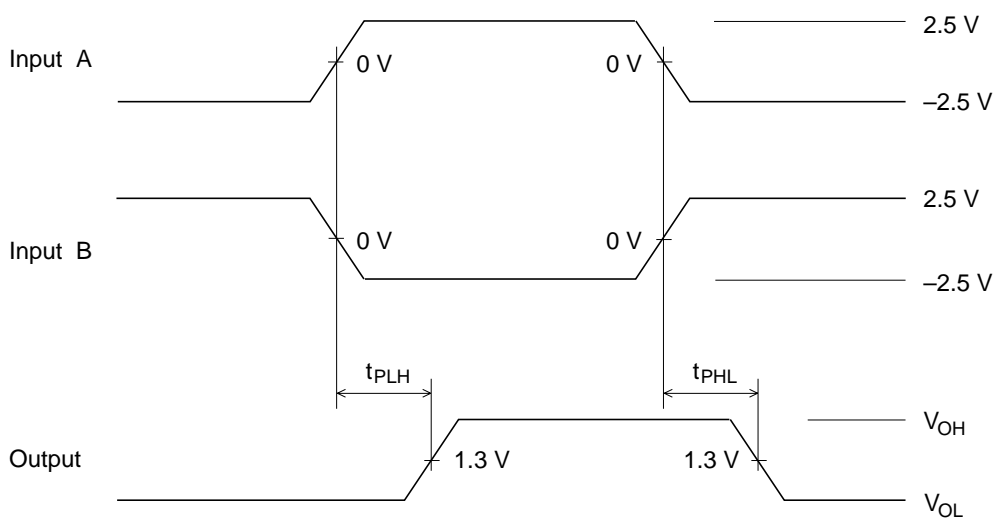
Item	Symbol	Min	Typ	Max	Unit	Conditions
Propagation Delay Time	t <sub>PLH</sub> , t <sub>PHL</sub>	—	17	25	ns	C <sub>L</sub> = 15 pF
Output Enable Time	t <sub>ZH</sub> , t <sub>ZL</sub>	—	15	22	ns	
Output Disable Time	t <sub>HZ</sub>	—	15	22	ns	C <sub>L</sub> = 5 pF
	t <sub>LZ</sub>	—	20	30	ns	

1.  $t_{PLH}$ ,  $t_{PHL}$ 

## Test Circuit

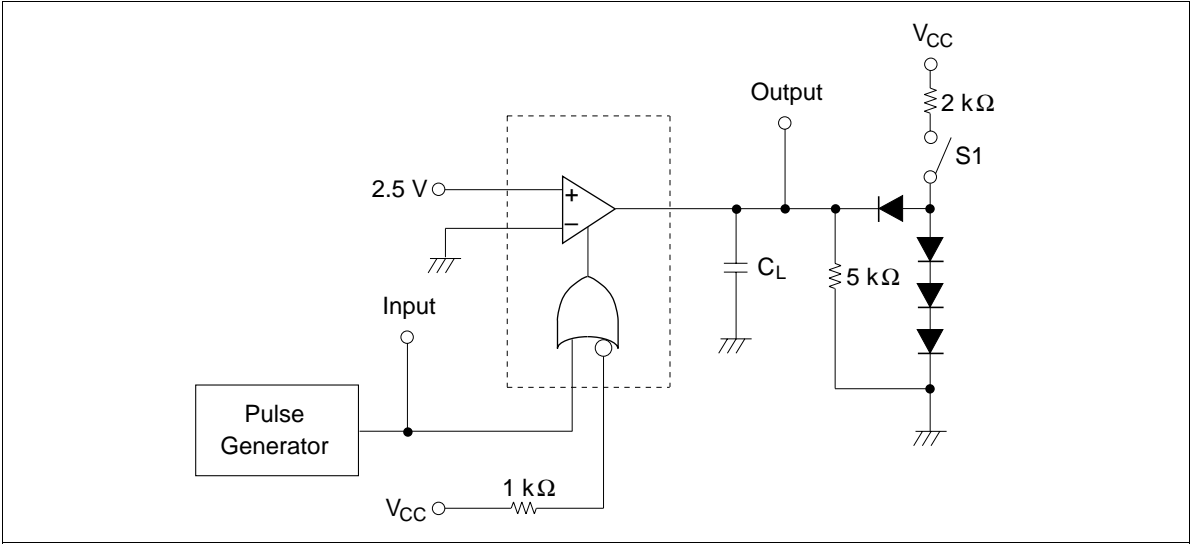


## Waveforms

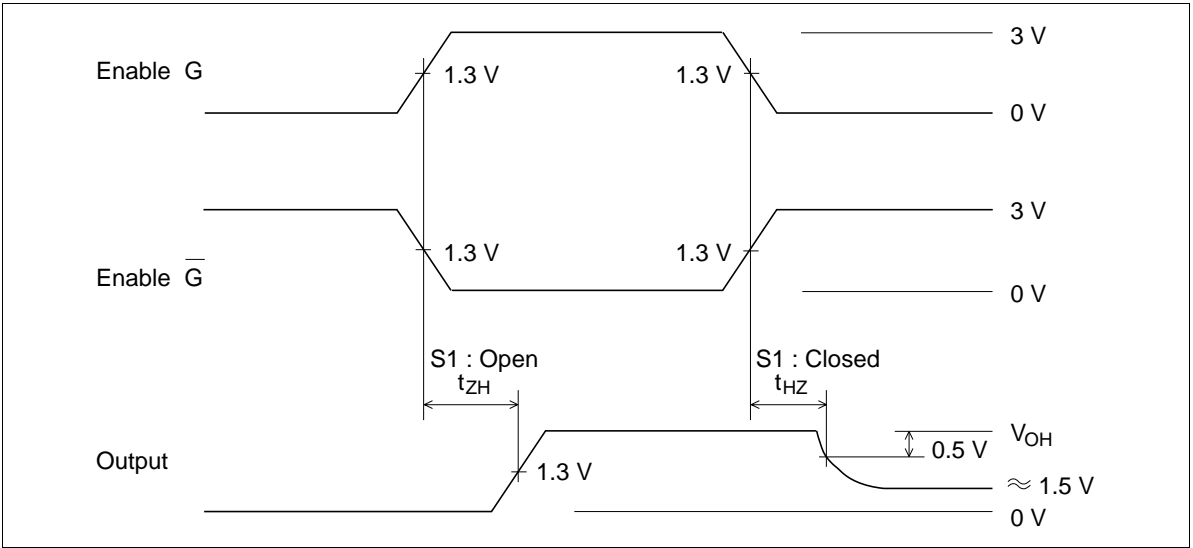


2.  $t_{HZ}$ ,  $t_{ZH}$

Test Circuit

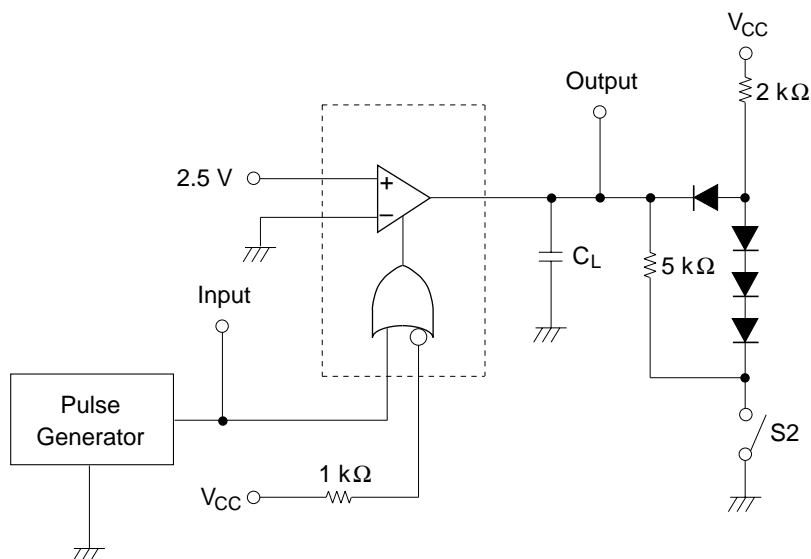


Waveforms

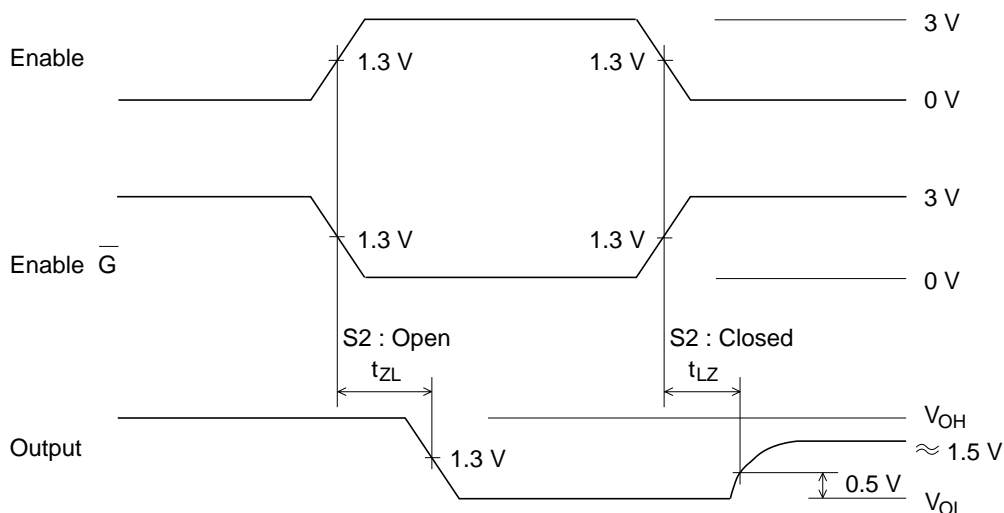


3.  $t_{LZ}$ ,  $t_{ZL}$ 

## Test Circuit



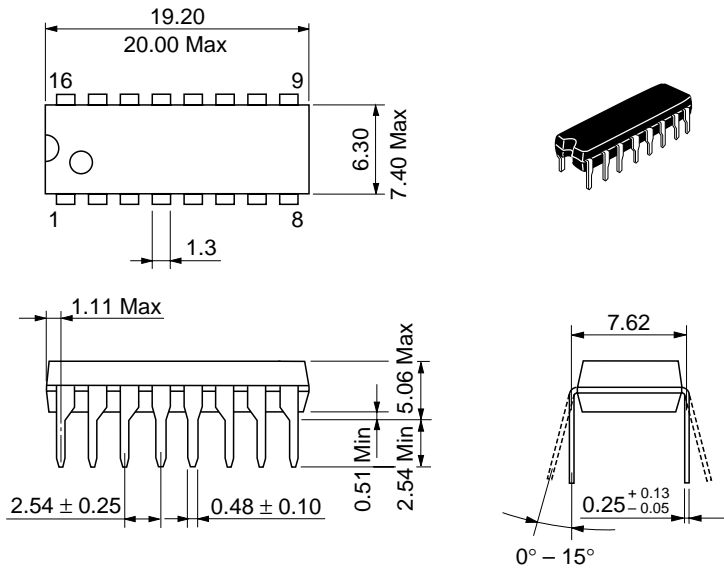
## Waveforms



- Notes:
1. The pulse generator has the following characteristics: PRR = 1 MHz duty cycle 50%,  $t_r = \leq 15$  ns,  $t_f = \leq 6$  ns,  $Z_{out} = 50 \Omega$ .
  2.  $C_L$  include probe and jig capacitance.
  3. All diodes are 1S2074(H)
  4. To test G input, ground G input and apply an inverted input waveform.

Package Dimensions

Unit: mm



Hitachi Code	DP-16
JEDEC	Conforms
EIAJ	Conforms
Mass (reference value)	1.07 g



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## Hitachi, Ltd.

Semiconductor & Integrated Circuits.

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL	North America	: <a href="http://semiconductor.hitachi.com/">http://semiconductor.hitachi.com/</a>
	Europe	: <a href="http://www.hitachi-eu.com/hel/ecg">http://www.hitachi-eu.com/hel/ecg</a>
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## For further information write to:

Hitachi Semiconductor  
(America) Inc.  
179 East Tasman Drive,  
San Jose, CA 95134  
Tel: <1> (408) 433-1990  
Fax: <1> (408) 433-0223

Hitachi Europe GmbH  
Electronic Components Group  
Dornacher Straße 3  
D-85622 Feldkirchen, Munich  
Germany  
Tel: <49> (89) 9 9180-0  
Fax: <49> (89) 9 29 30 00

Hitachi Europe Ltd.  
Electronic Components Group.  
Whitebrook Park  
Lower Cookham Road  
Maidenhead  
Berkshire SL6 8YA, United Kingdom  
Tel: <44> (1628) 585000  
Fax: <44> (1628) 585160

Hitachi Asia Ltd.  
Hitachi Tower  
16 Collyer Quay #20-00,  
Singapore 049318  
Tel: <65>-538-6533/538-8577  
Fax: <65>-538-6933/538-3877  
URL: <http://www.hitachi.com.sg>

Hitachi Asia Ltd.  
(Taipei Branch Office)  
4/F, No. 167, Tun Hwa North Road,  
Hung-Kuo Building,  
Taipei (105), Taiwan  
Tel: <886>-(2)-2718-3666  
Fax: <886>-(2)-2718-8180  
Telex: 23222 HAS-TP  
URL: <http://www.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.  
Group III (Electronic Components)  
7/F., North Tower,  
World Finance Centre,  
Harbour City, Canton Road  
Tsim Sha Tsui, Kowloon,  
Hong Kong  
Tel: <852>-(2)-735-9218  
Fax: <852>-(2)-730-0281  
URL: <http://www.hitachi.com.hk>

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