

HD74AC620/HD74ACT620

•Octal Bus Transceiver
with 3-State Output

Description

The 'AC620 is an octal transceiver featuring inverting 3-state bus compatible outputs in both send and receive directions.

These octal bus transceivers are designed for asynchronous two-way data flow between data buses. The control function implementation allows for maximum flexibility in timing.

These devices allow data transmission from the A bus to the B bus or from the B bus to the A Bus depending upon the logic levels at the enable inputs($\overline{\text{GBA}}$ and GAB).

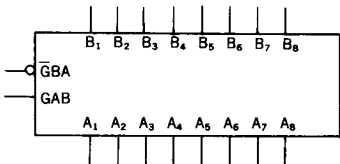
The enable inputs can be used to disable the device so that the buses are effectively isolated.

The dual-enable configuration gives the 'AC62: the capability to store data by simultaneous enabling of $\overline{\text{GBA}}$ and GAB . Each output reinforces its input in this transceiver configuration. Thus, when both control inputs are enabled and all other data sources to the two sets of the bus lines are at high impedance, both sets of bus lines(sixteen in all)will remain at their last states.

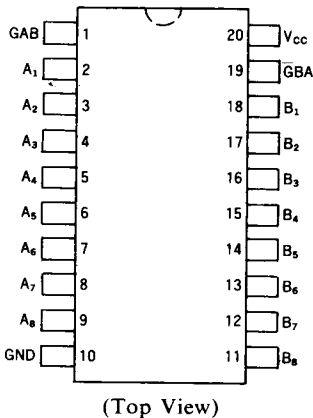
Features

- Octal Bidirectional Bus Interface
- Output Source/Sink 24mA

Logic Symbol



Pin Assignment



Pin Names

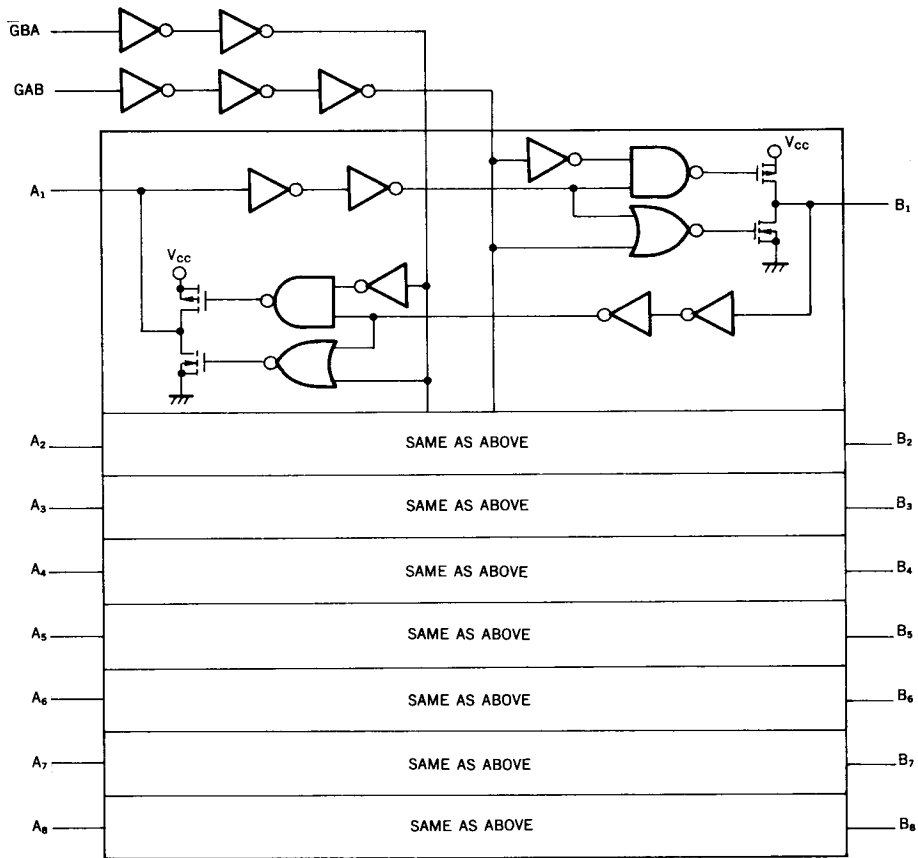
- $\overline{\text{GBA}}, \text{GAB}$ Enable Inputs
- $\text{A}_1 - \text{A}_8$ A Inputs or 3-State Ourputs
- $\text{B}_1 - \text{B}_8$ B Inputs or 3-State Outputs

Function Table

Enable	Inputs	Operation
	$\overline{\text{GBA}}$ GAB	
L	L	$\overline{\text{B}}$ data to A bus
H	H	$\overline{\text{A}}$ data to B bus
H	L	Z
L	H	$\overline{\text{B}}$ data to A bus A data to B bus

H = High Voltage Level
L = Low Voltage Level
Z = High Impedance

Logic Diagram



DC Characteristics (unless otherwise specified)

Symbol	Parameter	Max	Unit	Condition
I_{CC}	Maximum Quiescent Supply Current	80	μA	$V_{IN} = V_{CC}$ Ground, $V_{CC} = 5.5 V$ $T_a = \text{Worst Case}$
I_{CC}	Maximum Quiescent Supply Current	8.0	μA	$V_{IN} = V_{CC}$ Ground, $V_{CC} = 5.5 V$ $T_a = 25^\circ C$
I_{CCT}	Maximum Additional $I_{CC}/\text{Input (HD74ACT620)}$	1.5	mA	$V_{IN} = V_{CC} - 2.1 V$, $V_{CC} = 5.5 V$ $T_a = \text{Worst Case}$

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AC Characteristics : HD74AC620

Symbol	Parameter	Vcc* (V)	Ta = +25°C C _L = 50pF			Ta = -40°C to +85°C C _L = 50pF		Unit
			Min	Typ	Max	Min	Max	
t _{PLH}	Propagation Delay Data to Output	3.3 5.0	1.0 1.0	6.5 5.5	9.0 7.0	1.0 1.0	10.0 7.5	ns
t _{PHL}	Propagation Delay Data to Output	3.3 5.0	1.0 1.0	6.0 5.0	9.0 7.0	1.0 1.0	10.5 7.5	ns
t _{PZH}	Output Enable Time	3.3 5.0	1.0 1.0	8.0 6.5	12.5 9.0	1.0 1.0	13.0 9.5	ns
t _{PZL}	Output Enable Time	3.3 5.0	1.0 1.0	7.0 5.5	12.0 9.0	1.0 1.0	13.0 9.5	ns
t _{PHZ}	Output Disable Time	3.3 5.0	1.0 1.0	7.5 6.5	12.0 10.0	1.0 1.0	12.5 10.5	ns
t _{PLZ}	Output Disable Time	3.3 5.0	1.0 1.0	7.0 6.0	12.5 10.0	1.0 1.0	13.5 10.5	ns

* Voltage Range 3.3 is 3.3V ± 0.3V
Voltage Range 5.0 is 5.0V ± 0.5V

AC Characteristics : HD74ACT620

Symbol	Parameter	Vcc* (V)	Ta = +25°C C _L = 50pF			Ta = -40°C to +85°C C _L = 50pF		Unit
			Min	Typ	Max	Min	Max	
t _{PLH}	Propagation Delay Data to Output	5.0	1.0	7.0	9.0	1.0	10.0	ns
t _{PHL}	Propagation Delay Data to Output	5.0	1.0	5.5	9.0	1.0	10.0	ns
t _{PZH}	Output Enable Time	5.0	1.0	7.5	9.0	1.0	10.0	ns
t _{PZL}	Output Enable Time	5.0	1.0	6.0	10.0	1.0	11.0	ns
t _{PHZ}	Output Disable Time	5.0	1.0	8.0	10.5	1.0	11.5	ns
t _{PLZ}	Output Disable Time	5.0	1.0	7.5	10.5	1.0	11.5	ns

* Voltage Range 5.0 is 5.0V ± 0.5V

Capacitance

Symbol	Parameter	Typ	Unit	Condition
C _{IN}	Input Capacitance	4.5	pF	V _{cc} = 5.5 V
C _{I/O}	Input Capacitance	15.0	pF	V _{cc} = 5.5 V
C _{PD}	Power Dissipation Capacitance	30.0	pF	V _{cc} = 5.0 V