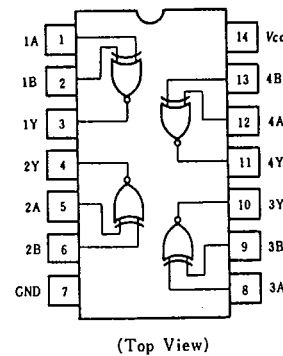


92D 10475 DT-43-21

HD74HC7266 • Quad. 2-input Exclusive-NOR Gates**FEATURES**

- High Speed Operation: $t_{pd}=12.5\text{ns}$ typ. ($C_L=50\text{pF}$)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC}=2\sim 6\text{V}$
- Low Input Current: $1\mu\text{A}$ max.
- Low Quiescent Supply Current: I_{CC} (static) = $1\mu\text{A}$ max. ($T_a=25^\circ\text{C}$)

PIN ARRANGEMENT**DC CHARACTERISTICS**

Item	Symbol	$V_{CC}(\text{V})$	Test Conditions	$T_a=25^\circ\text{C}$			$T_a=-40\sim+85^\circ\text{C}$		Unit
				min	typ	max	min	max	
Input Voltage	V_{IH}	2.0		1.5	—	—	1.5	—	V
		4.5		3.15	—	—	3.15	—	
		6.0		4.2	—	—	4.2	—	
	V_{IL}	2.0		—	—	0.5	—	0.5	V
		4.5		—	—	1.35	—	1.35	
		6.0		—	—	1.8	—	1.8	
Output Voltage	V_{OH}	2.0	$V_{iA}=V_{IH}$ or V_{iL}	$I_{OH}=-20\mu\text{A}$	1.9	2.0	—	1.9	V
		4.5			4.4	4.5	—	4.4	
		6.0			5.9	6.0	—	5.9	
		4.5		$I_{OH}=-4\text{mA}$	4.18	—	—	4.13	
		6.0		$I_{OH}=-5.2\text{mA}$	5.68	—	—	5.63	
		6.0			—	—	—	—	
	V_{OL}	2.0	$V_{iA}=V_{IH}$ or V_{iL}	$I_{OL}=20\mu\text{A}$	—	0.0	0.1	—	V
		4.5			—	0.0	0.1	—	
		6.0			—	0.0	0.1	—	
		4.5		$I_{OL}=4\text{mA}$	—	—	0.26	—	
		6.0		$I_{OL}=5.2\text{mA}$	—	—	0.26	—	
		6.0			—	—	—	—	
Input Current	I_{iA}	6.0	$V_{iA}=V_{CC}$ or GND	—	—	± 0.1	—	± 1.0	μA
Quiescent Supply Current	I_{CC}	6.0	$V_{iA}=V_{CC}$ or GND, $I_{iA}=0\mu\text{A}$	—	—	1.0	—	10	μA

AC CHARACTERISTICS ($C_L=50\text{pF}$, Input $t_r=t_f=6\text{ns}$)

Item	Symbol	$V_{CC}(\text{V})$	Test Conditions	$T_a=25^\circ\text{C}$			$T_a=-40\sim+85^\circ\text{C}$		Unit
				min	typ	max	min	max	
Propagation Delay Time	t_{PLH}	2.0		—	—	120	—	150	ns
		4.5		—	13	24	—	30	
		6.0		—	—	20	—	26	
	t_{PHL}	2.0		—	—	120	—	150	ns
		4.5		—	12	24	—	30	
		6.0		—	—	20	—	26	
Output Rise Time	t_{TLH}	2.0		—	—	75	—	95	ns
		4.5		—	7	15	—	19	
		6.0		—	—	13	—	16	
Output Fall Time	t_{THL}	2.0		—	—	75	—	95	ns
		4.5		—	7	15	—	19	
		6.0		—	—	13	—	16	
Input Capacitance	C_{iA}	—		—	5	10	—	10	pF

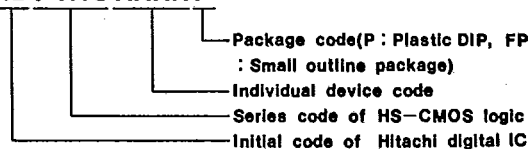
PACKAGE INFORMATION

T-90-20

In the HD74HC series of HS-CMOS logic, either of plastic DIP and small outline packages can be selected.
For your ordering, please refer to the following package code.

● Package code of HS-CMOS Logic

HD74HC XXXXP

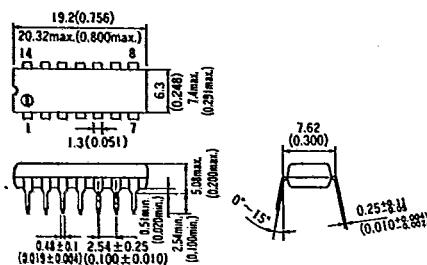


■ PLASTIC DIP PACKAGE [Unit: mm (inch), scale: 1/1]

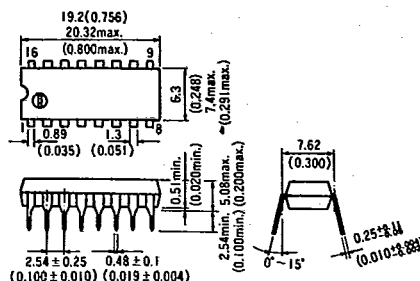
● 14-pin type

● 16-pin type

DP-14



DP-16

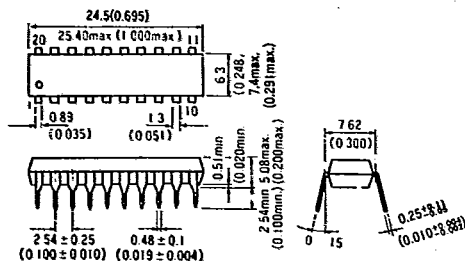


Note) For the present, both packages of 0.89 and 1.3 can be used. Please refer to the Technical Marketing Dept. for further details.

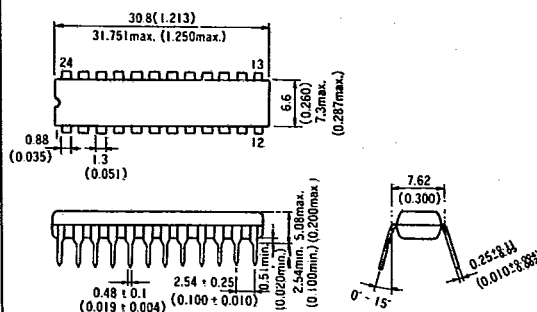
● 20-pin type

● 24-pin type

DP-20N



DP-24N



Note) Because this is under development, the dimensions may be changed partly.

0571

F-06



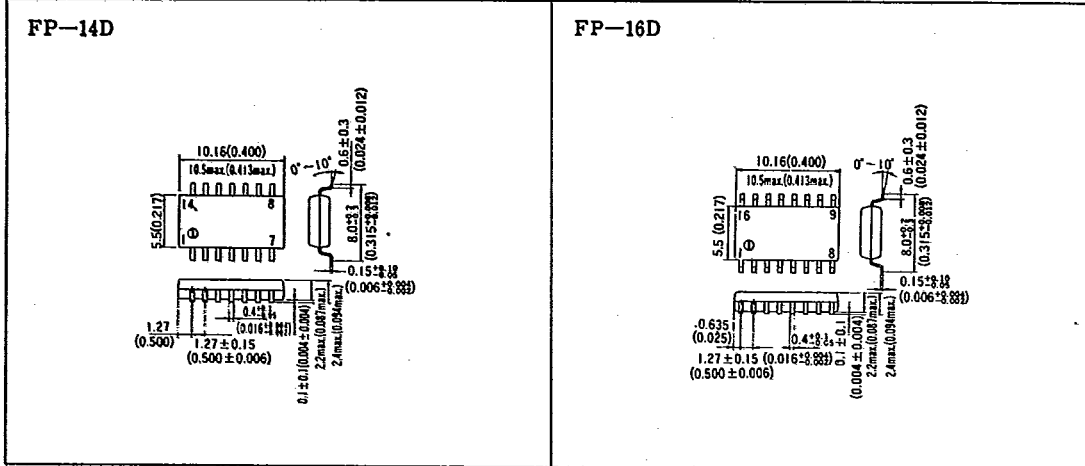
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■ SMALL OUTLINE PACKAGE [Unit: mm (inch), scale: 1½]

● 14—pin type

● 16—pin type



● 20—pin type

