
HD74LV00A

Quad. 2-input NAND Gates

HITACHI

ADE-205-240 (Z)

1st Edition

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Description

The HD74LV00A has four two-input NAND gates in a 14-pin package.

Low-voltage and high-speed operation is suitable for the battery-powered products (e.g., notebook computers), and the low-power consumption extends the battery life.

Features

- V_{CC} = 2.0 V to 5.5 V operation
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- All outputs V_O (Max.) = 5.5 V (@ V_{CC} = 0 V)
- Typical V_{OL} ground bounce < 0.8 V (@ V_{CC} = 3.3 V, T_a = 25°C)
- Typical V_{OH} undershoot > 2.3 V (@ V_{CC} = 3.3 V, T_a = 25°C)
- Output current ± 6 mA (@ V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@ V_{CC} = 4.5 V to 5.5 V)

Function Table

Inputs		
A	B	Output Y
H	H	L
L	X	H
X	L	H

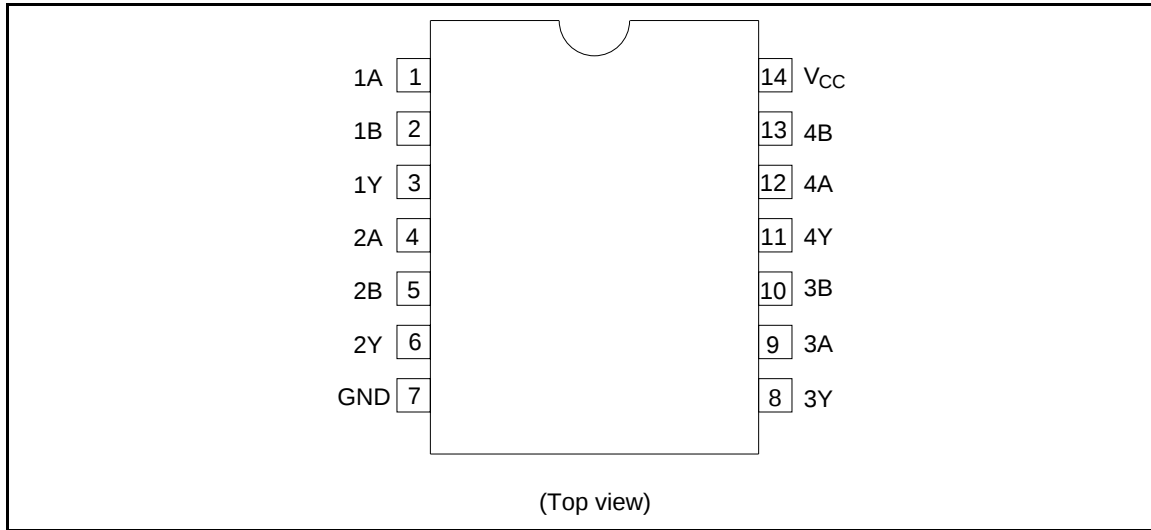
Note: H:High level

L:Low level

X:Immaterial

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Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V_{CC}	-0.5 to 7.0	V	
Input voltage range ^{*1}	V_I	-0.5 to 7.0	V	
Output voltage range ^{*1, 2}	V_O	-0.5 to $V_{CC} + 0.5$ -0.5 to 7.0	V	Output: H or L V_{CC} : OFF
Input clamp current	I_{IK}	-20	mA	$V_I < 0$
Output clamp current	I_{OK}	± 50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I_O	± 25	mA	$V_O = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	± 50	mA	
Maximum power dissipation at P_T $T_a = 25^\circ\text{C}$ (in still air) ^{*3}		785 500	mW	SOP TSSOP
Storage temperature	T_{stg}	-65 to 150	$^\circ\text{C}$	

Notes: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 5.5 V maximum.

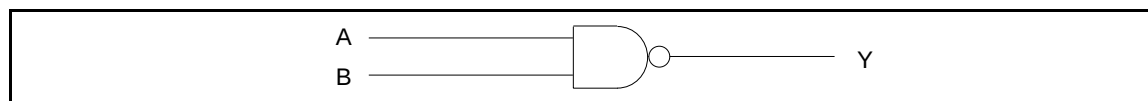
3. The maximum package power dissipation was calculated using a junction temperature of 150 $^\circ\text{C}$.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	2.0	5.5	V	
Input voltage range	V_I	0	5.5	V	
Output voltage range	V_O	0	V_{CC}	V	
Output current	I_{OH}	—	–50	μA	$V_{CC} = 2.0\text{ V}$
		—	–2	mA	$V_{CC} = 2.3\text{ to }2.7\text{ V}$
		—	–6		$V_{CC} = 3.0\text{ to }3.6\text{ V}$
		—	–12		$V_{CC} = 4.5\text{ to }5.5\text{ V}$
	I_{OL}	—	50	μA	$V_{CC} = 2.0\text{ V}$
		—	2	mA	$V_{CC} = 2.3\text{ to }2.7\text{ V}$
		—	6		$V_{CC} = 3.0\text{ to }3.6\text{ V}$
		—	12		$V_{CC} = 4.5\text{ to }5.5\text{ V}$
Input transition rise or fall rate	$\Delta t / \Delta v$	0	200	ns/V	$V_{CC} = 2.3\text{ to }2.7\text{ V}$
		0	100		$V_{CC} = 3.0\text{ to }3.6\text{ V}$
		0	20		$V_{CC} = 4.5\text{ to }5.5\text{ V}$
Operating free-air temperature	T_a	–40	85	°C	

Note: Unused or floating inputs must be held high or low.

Logic Diagram



DC Electrical Characteristics

- $T_a = -40\text{ to }85^\circ\text{C}$

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Item	Symbol	$V_{CC}\text{ (V)}^*$	Min	Typ	Max	Unit	Test Conditions
Input voltage	V_{IH}	2.0	1.5	—	—	V	
		2.3 to 2.7	$V_{CC} \times 0.7$	—	—		
		3.0 to 3.6	$V_{CC} \times 0.7$	—	—		
		4.5 to 5.5	$V_{CC} \times 0.7$	—	—		
	V_{IL}	2.0	—	—	0.5		
		2.3 to 2.7	—	—	$V_{CC} \times 0.3$		
		3.0 to 3.6	—	—	$V_{CC} \times 0.3$		
		4.5 to 5.5	—	—	$V_{CC} \times 0.3$		
Output voltage	V_{OH}	Min to Max	$V_{CC} - 0.1$	—	—	V	$I_{OH} = -50\text{ }\mu A$
		2.3	2.0	—	—		$I_{OH} = -2\text{ mA}$
		3.0	2.48	—	—		$I_{OH} = -6\text{ mA}$
		4.5	3.8	—	—		$I_{OH} = -12\text{ mA}$
	V_{OL}	Min to Max	—	—	0.1		$I_{OL} = 50\text{ }\mu A$

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Item	Symbol	V _{CC} (V)*	Min	Typ	Max	Unit	Test Conditions
		2.3	—	—	0.4		I _{OL} = 2 mA
		3.0	—	—	0.44		I _{OL} = 6 mA
		4.5	—	—	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	—	—	±1	μA	V _{IN} = 5.5 V or GND
Quiescent supply current	I _{CC}	5.5	—	—	20	μA	V _{IN} = V _{CC} or GND, I _O = 0
Output leakage current	I _{OFF}	0	—	—	5	μA	V _O = 5.5 V
Input capacitance	C _{IN}	3.3	—	3.3	—	pF	V _I = V _{CC} or GND

- Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

- V_{CC} = 2.5 ± 0.2 V

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Ta = 25°C					Ta = -40 to 85°C					
Item	Symbol	Min	Typ	Max	Min	Max	Unit	Test Condi- tions	FROM (Input)	TO (Out- put)
Propagation	t _{PLH}	—	7.1	12.9	1.0	15.0	ns	C _L = 15 pF	A or B	Y
delay time	t _{PHL}	—	9.6	16.6	1.0	20.0		C _L = 50 pF		

- V_{CC} = 3.3 ± 0.3 V

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Ta = 25°C					Ta = -40 to 85°C					
Item	Symbol	Min	Typ	Max	Min	Max	Unit	Test Condi- tions	FROM (Input)	TO (Out- put)
Propagation	t _{PLH}	—	5.0	7.9	1.0	9.5	ns	C _L = 15 pF	A or B	Y
delay time	t _{PHL}	—	6.9	11.4	1.0	13.0		C _L = 50 pF		

- V_{CC} = 5.0 ± 0.5 V

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Ta = 25°C					Ta = -40 to 85°C					
Item	Symbol	Min	Typ	Max	Min	Max	Unit	Test Condi- tions	FROM (Input)	TO (Out- put)
Propagation	t _{PLH}	—	3.6	5.5	1.0	6.5	ns	C _L = 15 pF	A or B	Y
delay time	t _{PHL}	—	4.9	7.5	1.0	8.5		C _L = 50 pF		

Operating Characteristics

- $C_L = 50 \text{ pF}$

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Ta = 25°C							
Item	Symbol	V _{CC} (V)	Min	Typ	Max	Unit	Test Conditions
Power dissipation capacitance	C _{PD}	3.3	—	9.5	—	pF	f = 10 MHz
		5.0	—	11.0	—		

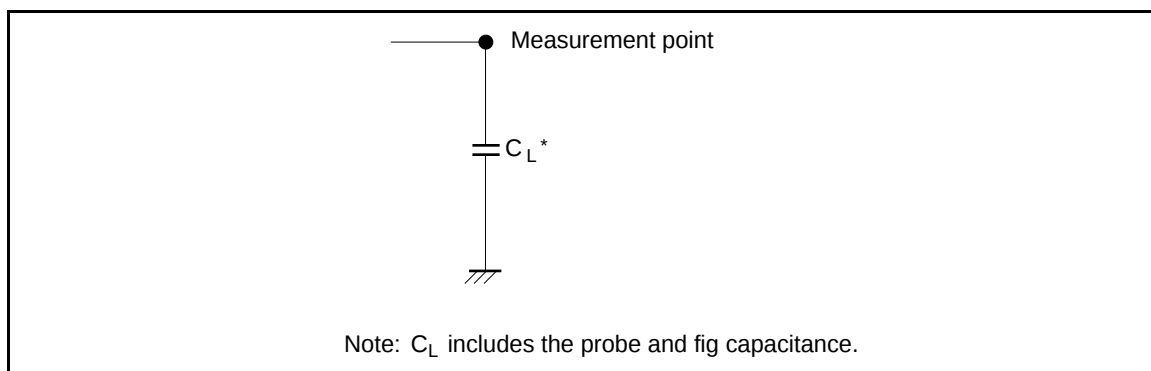
Noise Characteristics

- $C_L = 50 \text{ pF}$

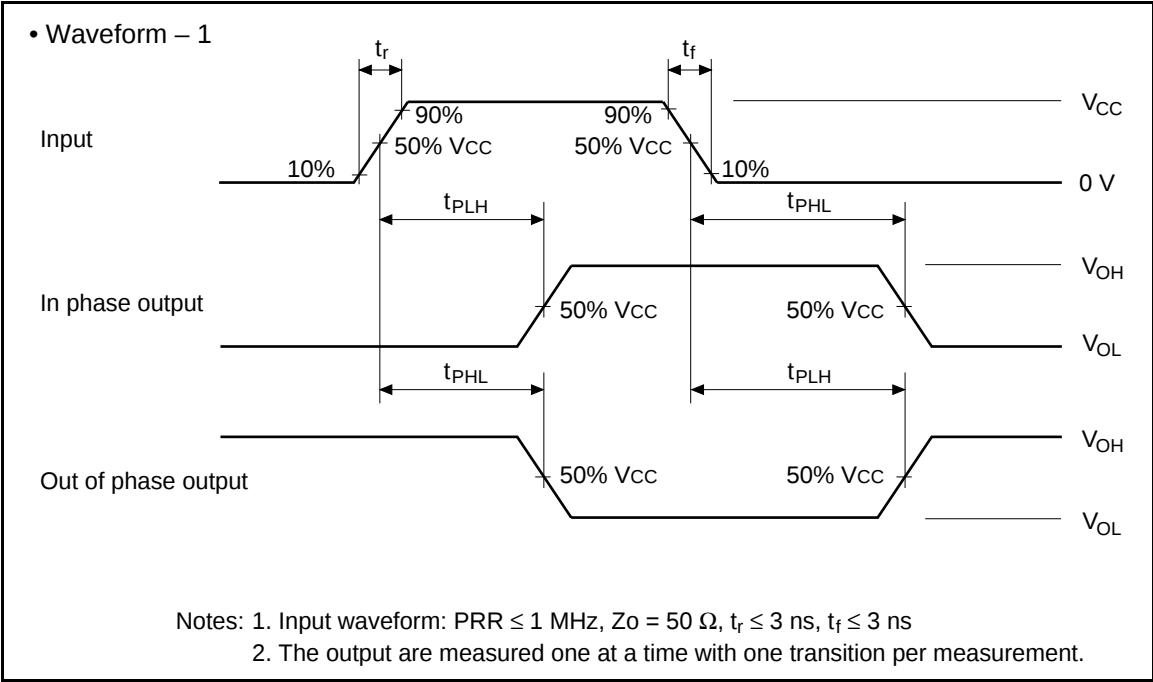
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Ta = 25°C							
Item	Symbol	V _{CC} (V)	Min	Typ	Max	Unit	Test Conditions
Quiet output, maximum dynamic V _{OL}	V _{OL} (P)	3.3	—	0.2	0.8	V	
Quiet output, minimum dynamic V _{OL}	V _{OL} (V)	3.3	—	−0.1	−0.8		
Quiet output, minimum dynamic V _{OH}	V _{OH} (V)	3.3	—	3.1	—		
High-level dynamic input voltage	V _{IH} (D)	3.3	2.31	—	—	V	
Low-level dynamic input voltage	V _{IL} (D)	3.3	—	—	0.99		

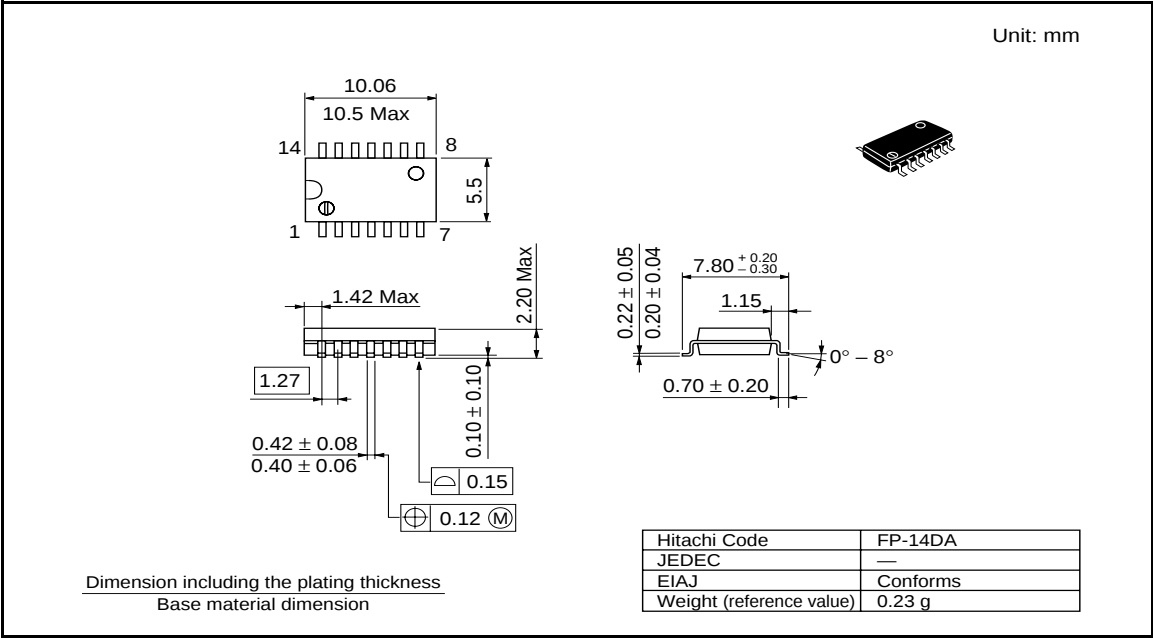
Test Circuit

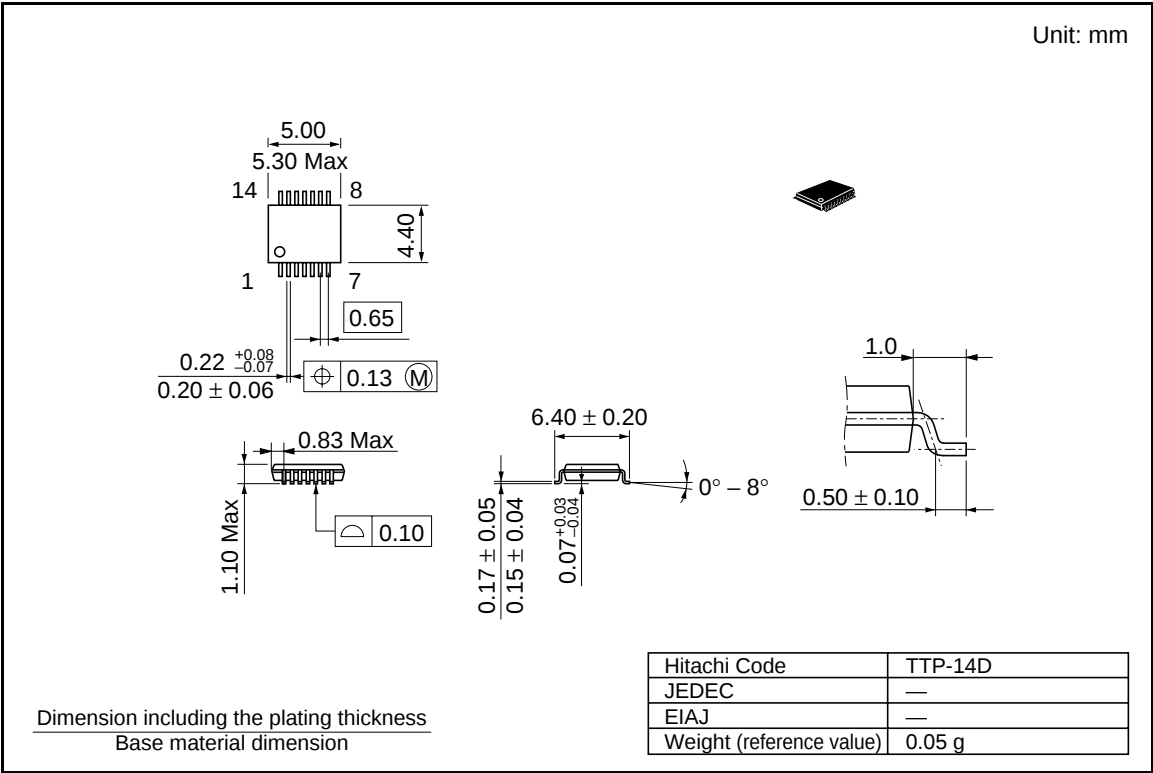
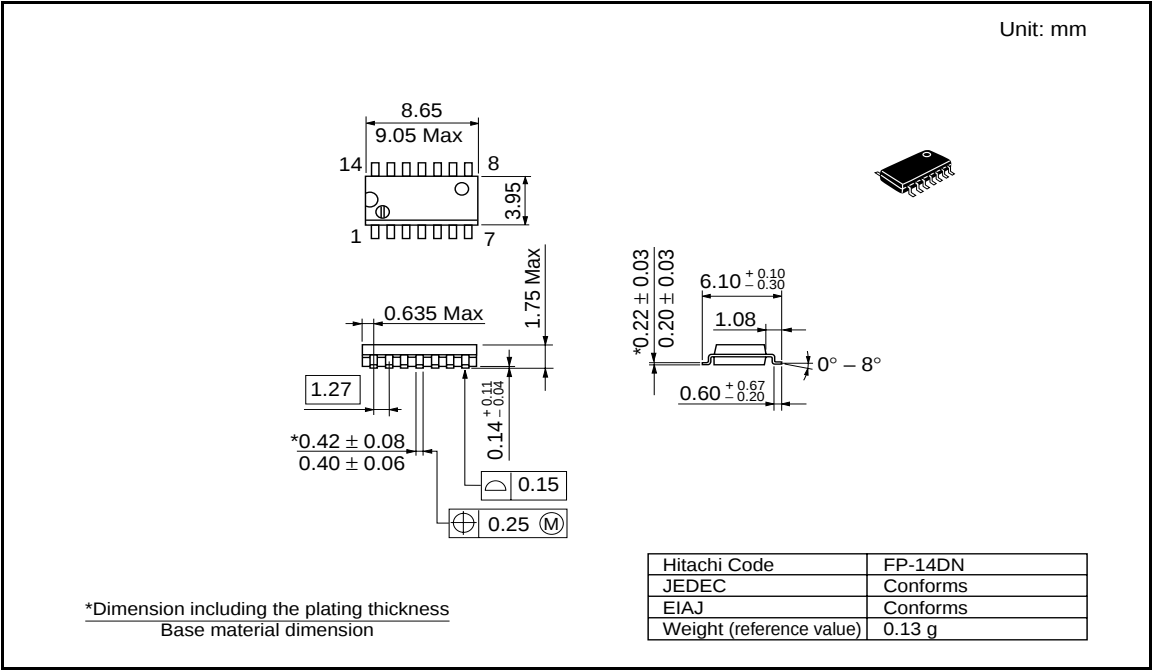


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Package Dimensions





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