Hex Buffers / Drivers with Open Drain Outputs

HITACHI

ADE-205-297B (Z) 3rd Edition August 2000

Description

The HD74LV07A has six buffers / drivers with open drain outputs in a 14-pin package. Low-voltage and high-speed operation is suitable for the battery-powered products (e.g., notebook computers), and the low-power consumption extends the battery life.

Features

- $V_{CC} = 2.0 \text{ V}$ to 5.5 V operation
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- All outputs V_0 (Max.) = 5.5 V (@ V_{CC} = 0 V)
- All outputs V_0 (Max.) = 5.5 V (@ V_{CC} = 2.0 V to 5.5 V, Output "Z" state)
- Typical V_{OI} ground bounce < 0.8 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- Output current ± 8 mA (@V_{CC} = 3.0 V to 3.6 V), ± 16 mA (@V_{CC} = 4.5 V to 5.5 V)

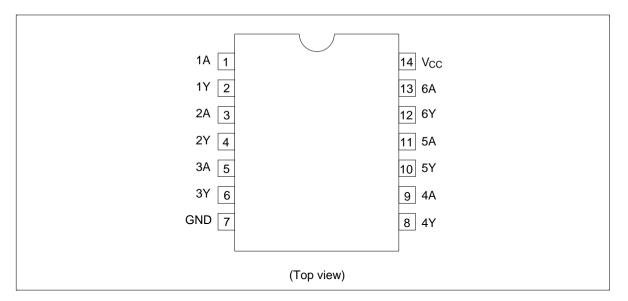
Function Table

Input A	Output Y
L	L
Н	Z

Note: H: High level
L: Low level
Z: High impedance



Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V _{cc}	-0.5 to 7.0	V	
Input voltage range*1	Vı	-0.5 to 7.0	V	
Output voltage range*1,2	Vo	-0.5 to $V_{cc} + 0.5$	V	Output: Z or L
		-0.5 to 7.0	=	V _{cc} : OFF
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{OK}	±50	mA	V ₀ < 0
Continuous output current	Io	±35	mA	$V_{o} = 0 \text{ to } V_{cc}$
Continuous current through V _{cc} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air)*3	P _T	785	mW	SOP
		500	-	TSSOP
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

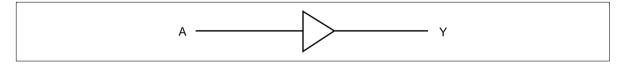
- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 7.0 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{cc}	2.0	5.5	V	
Input voltage range	V _I	0	5.5	V	
Output voltage range	V _o	0	5.5	V	
Output current	I _{OL}	-	50	μΑ	V _{cc} = 2.0 V
		_	2	mA	$V_{cc} = 2.3 \text{ to } 2.7 \text{ V}$
		_	8		$V_{cc} = 3.0 \text{ to } 3.6 \text{ V}$
		_	16		$V_{cc} = 4.5 \text{ to } 5.5 \text{ V}$
Input transition rise or fall rate	Δt / Δν	0	200	ns/V	$V_{cc} = 2.3 \text{ to } 2.7 \text{ V}$
		0	100		$V_{cc} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		$V_{cc} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	Та	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Logic Diagram



DC Electrical Characteristics

• $Ta = -40 \text{ to } 85^{\circ}C$

Item	Symbol	V _{cc} (V)*	Min	Тур	Max	Unit	Test Conditions
Input voltage	V _{IH}	2.0	1.5	_	_	V	
		2.3 to 2.7	$V_{cc} \times 0.7$	_	_		
		3.0 to 3.6	$V_{cc} \times 0.7$	_		•	
		4.5 to 5.5	$V_{cc} \times 0.7$	_	_	•	
	V _{IL}	2.0		_	0.5		
		2.3 to 2.7	_	_	$V_{cc} \times 0.3$		
		3.0 to 3.6	_	_	$V_{\rm CC} \times 0.3$	•	
		4.5 to 5.5		_	$V_{cc} \times 0.3$		
Output voltage	V _{OL}	Min to Max			0.1	V	I _{OL} = 50 μA
		2.3	_	_	0.4		I _{OL} = 2 mA
		3.0	_	_	0.44	•	I _{OL} = 8 mA
		4.5		_	0.55		I _{OL} = 16 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	V _{IN} = 5.5 V or GND
Off state output current	l _{oz}	Min to Max	_	_	2.5	μА	V _o = 5.5 V
Quiescent supply current	I _{cc}	5.5	_	_	20	μΑ	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	I _{OFF}	0	_	_	5	μА	V_1 or $V_0 = 0$ to 5.5 V
Input capacitance	C _{IN}	3.3	_	2.3	_	pF	$V_{I} = V_{CC}$ or GND

Note: For conditions shown as Min or Max use the appropriate values under recommended operating conditions.

Switching Characteristics

• $V_{CC} = 2.5 \pm 0.2 \text{ V}$

		Ta = 2	25°C		Ta = -4	40 to 85°C				
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Test Conditions	FROM (Input)	TO (Output)
Propagation	t _{PLH}	_	4.7	10.4	1.0	13.0	ns	C _L = 15 pF	Α	Υ
delay time		_	9.5	15.2	1.0	18.0	-	C _L = 50 pF		
	t _{PHL}	_	5.4	10.4	1.0	13.0	-	C _L = 15 pF		
		_	7.9	15.2	1.0	18.0	-	C _L = 50 pF	-	

• $V_{CC} = 3.3 \pm 0.3 \text{ V}$

		Ta = 2	25°C		Ta = -4	Ta = −40 to 85°C				
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Test Conditions	FROM (Input)	TO (Output)
Propagation	t _{PLH}	_	4.0	7.1	1.0	8.5	ns	C _L = 15 pF	Α	Υ
delay time		_	7.3	10.6	1.0	12.0	_	C _L = 50 pF	-	
	t _{PHL}	_	4.3	7.1	1.0	8.5	-	C _L = 15 pF		
		_	5.8	10.6	1.0	12.0	-	C _L = 50 pF		

• $\mathbf{V}_{\mathrm{CC}} = 5.0 \pm 0.5 \ \mathbf{V}$

		Ta = 2	25°C		Ta = -4	40 to 85°C				
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Test Conditions	FROM (Input)	TO (Output)
Propagation	t _{PLH}	_	3.3	5.5	1.0	6.5	ns	C _L = 15 pF	Α	Υ
delay time		_	5.6	7.5	1.0	8.5	-	C _L = 50 pF		
	t _{PHL}	_	3.4	5.5	1.0	6.5	=	C _L = 15 pF	-	
		_	4.1	7.5	1.0	8.5	-	C _L = 50 pF		

Operating Characteristics

• $C_L = 50 \text{ pF}$

Ta	=	259	C

Item	Symbol	V_{cc} (V)	Min	Тур	Max	Unit	Test Conditions
Power dissipation capacitance	$C_{\mathtt{PD}}$	3.3	_	9.6	_	pF	f = 10 MHz
		5.0	_	11.4	_		

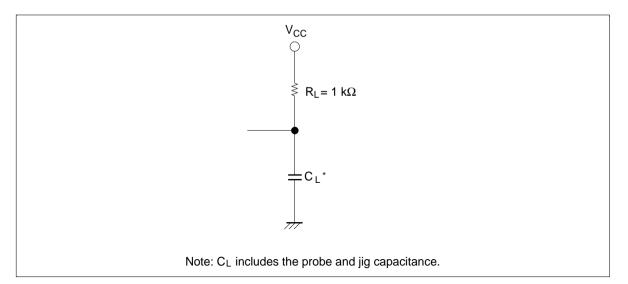
Noise Characteristics

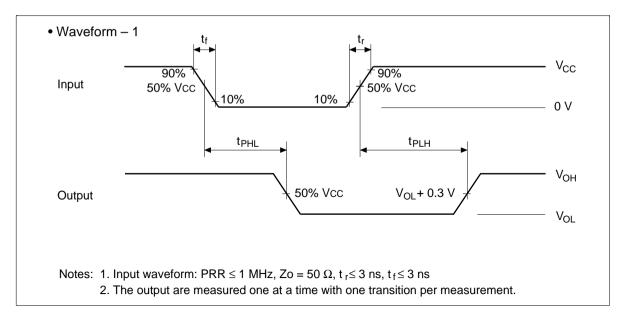
• $C_L = 50 \text{ pF}$

Ta	_	25°	\sim
Ιa	=	23	C

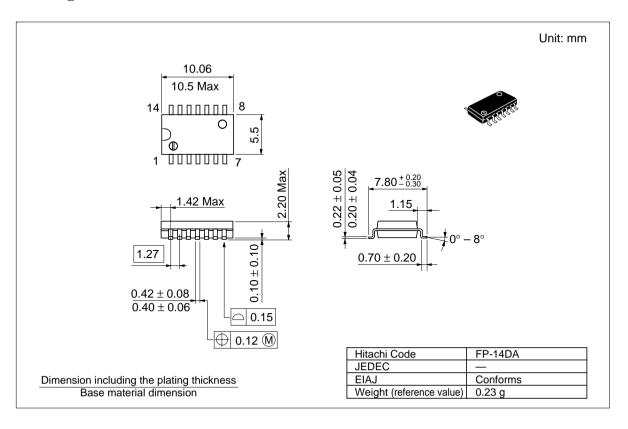
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test Conditions
Quiet output, maximum dynamic V _{OL}	$V_{OL(P)}$	3.3	_	0.3	0.8	V	
Quiet output, minimum dynamic V _{OL}	$V_{OL(V)}$	3.3	_	-0.1	-0.8		
High-level dynamic input voltage	V _{IH (D)}	3.3	2.31	<u> </u>	_	V	
Low-level dynamic input voltage	V _{IL (D)}	3.3	_	_	0.99		

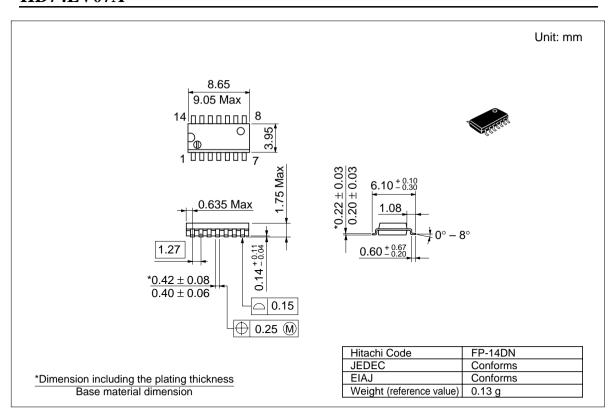
Test Circuit

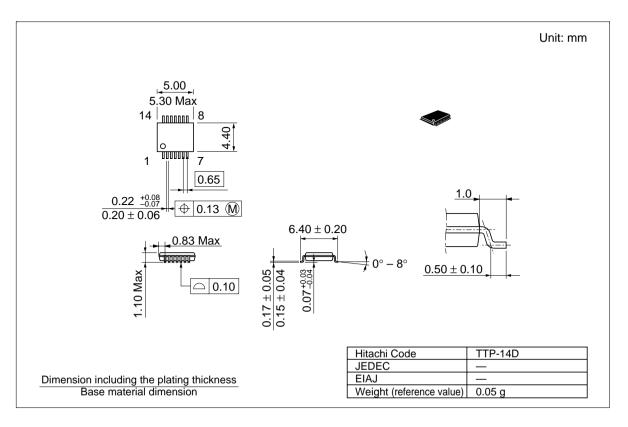




Package Dimensions







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