

HD74LV139A

Dual 2-to-4 line Decoder / Demultiplexers

HITACHI

ADE-205-262 (Z)

1st Edition

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Description

The HD74LV139A is designed to be used in high-performance memory-decoding or data-routing applications requiring very short propagation delay times. The active-low enable input can be used as a data line in demultiplexing applications.

This decoder/demultiplexer features fully buffered inputs, each of which represents only one normalized load to its driving circuit.

Low-voltage and high-speed operation is suitable for the battery-powered products (e.g., notebook computers), and the low-power consumption extends the battery life.

Features

- $V_{CC} = 2.0 \text{ V}$ to 5.5 V operation
- All inputs $V_{IH} (\text{Max.}) = 5.5 \text{ V}$ ($@V_{CC} = 0 \text{ V}$ to 5.5 V)
- All outputs $V_O (\text{Max.}) = 5.5 \text{ V}$ ($@V_{CC} = 0 \text{ V}$)
- Typical V_{OL} ground bounce $< 0.8 \text{ V}$ ($@V_{CC} = 3.3 \text{ V}$, $T_a = 25^\circ\text{C}$)
- Typical V_{OH} undershoot $> 2.3 \text{ V}$ ($@V_{CC} = 3.3 \text{ V}$, $T_a = 25^\circ\text{C}$)
- Output current $\pm 6 \text{ mA}$ ($@V_{CC} = 3.0 \text{ V}$ to 3.6 V), $\pm 12 \text{ mA}$ ($@V_{CC} = 4.5 \text{ V}$ to 5.5 V)

Function Table

Inputs						
	Select		Outputs			
G1	B	A	Y0	Y1	Y2	Y3
H	X	X	H	H	H	H
L	L	L	L	H	H	H
L	L	H	H	L	H	H
L	H	L	H	H	L	H
L	H	H	H	H	H	L

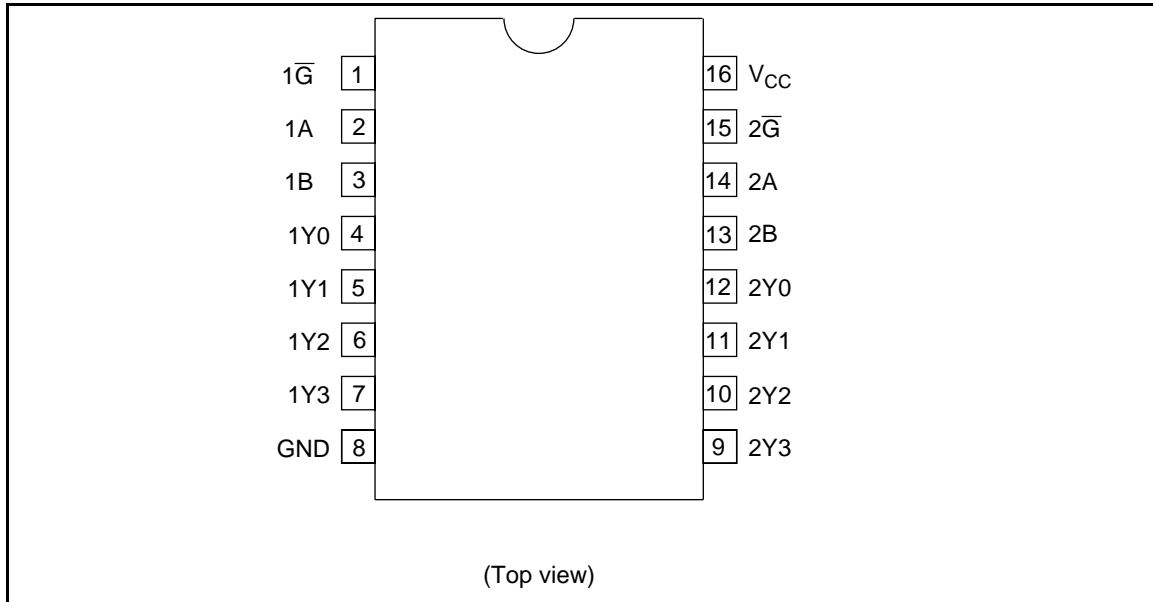
Note: H:High level

L:Low level

X:Immaterial

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Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions
Supply voltage range	V_{CC}	-0.5 to 7.0	V	
Input voltage range*1	V_I	-0.5 to 7.0	V	
Output voltage range*1, 2	V_O	-0.5 to $V_{CC} + 0.5$ -0.5 to 7.0	V	Output: H or L V_{CC} : OFF
Input clamp current	I_{IK}	-20	mA	$V_I < 0$
Output clamp current	I_{OK}	± 50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	I_O	± 25	mA	$V_O = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I_{CC} or I_{GND}	± 50	mA	
Maximum power dissipation at $T_a = 25^\circ\text{C}$ (in still air)*3	P_T	785 500	mW	SOP TSSOP
Storage temperature	T_{stg}	-65 to 150	$^\circ\text{C}$	

Notes: The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

1.The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2.This value is limited to 5.5 V maximum.

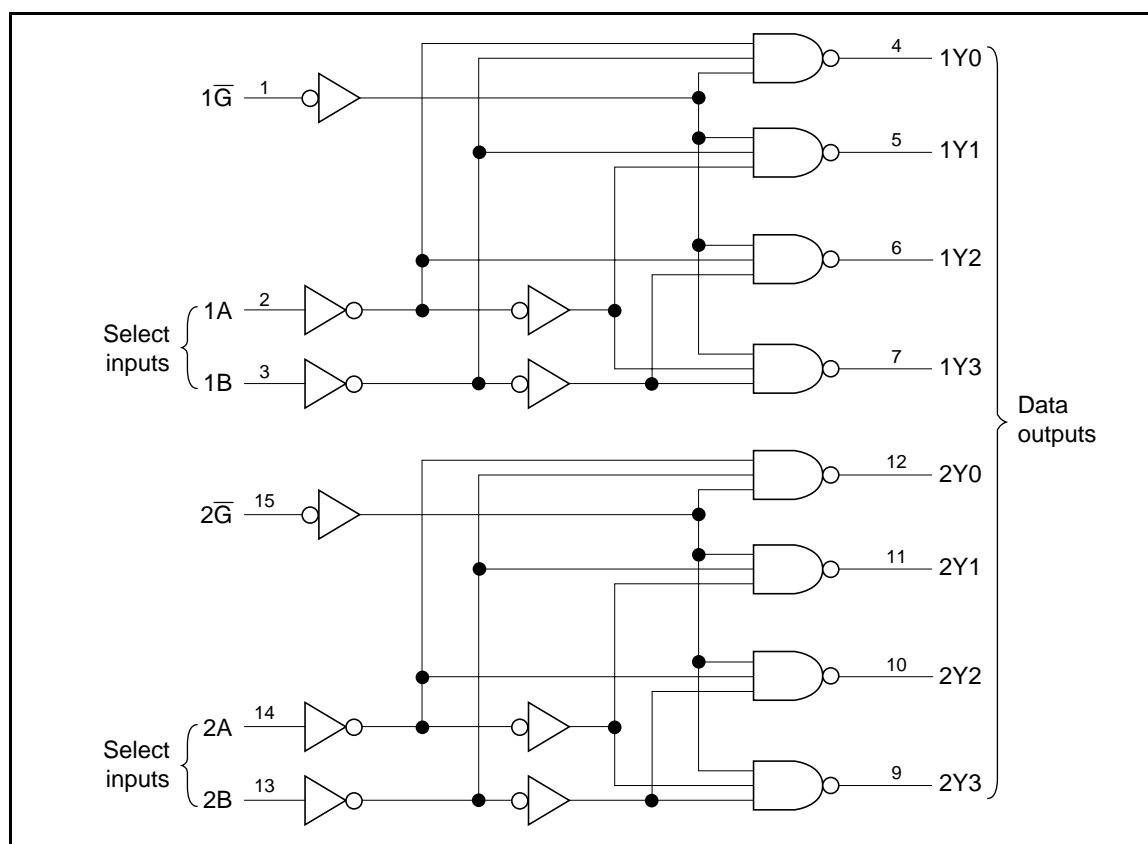
3.The maximum package power dissipation was calculated using a junction temperature of 150 $^\circ\text{C}$.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V_{CC}	2.0	5.5	V	
Input voltage range	V_I	0	5.5	V	
Output voltage range	V_O	0	V_{CC}	V	H or L
Output current	I_{OH}	—	−50	μA	$V_{CC} = 2.0\text{ V}$
		—	−2	mA	$V_{CC} = 2.3\text{ to }2.7\text{ V}$
		—	−6		$V_{CC} = 3.0\text{ to }3.6\text{ V}$
		—	−12		$V_{CC} = 4.5\text{ to }5.5\text{ V}$
	I_{OL}	—	50	μA	$V_{CC} = 2.0\text{ V}$
		—	2	mA	$V_{CC} = 2.3\text{ to }2.7\text{ V}$
		—	6		$V_{CC} = 3.0\text{ to }3.6\text{ V}$
		—	12		$V_{CC} = 4.5\text{ to }5.5\text{ V}$
Input transition rise or fall rate	$\Delta t/\Delta v$	0	200	ns/V	$V_{CC} = 2.3\text{ to }2.7\text{ V}$
		0	100		$V_{CC} = 3.0\text{ to }3.6\text{ V}$
		0	20		$V_{CC} = 4.5\text{ to }5.5\text{ V}$
Operating free-air temperature	T_a	−40	85	$^{\circ}C$	

Note: Unused or floating inputs must be held high or low.

Logic Diagram



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DC Electrical Characteristics

- $T_a = -40$ to 85°C

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Item	Symbol	V_{CC} (V)*	Min	Typ	Max	Unit	Test Conditions
Input voltage	V_{IH}	2.0	1.5	—	—	V	
		2.3 to 2.7	$V_{CC} \times 0.7$	—	—		
		3.0 to 3.6	$V_{CC} \times 0.7$	—	—		
		4.5 to 5.5	$V_{CC} \times 0.7$	—	—		
	V_{IL}	2.0	—	—	0.5		
		2.3 to 2.7	—	—	$V_{CC} \times 0.3$		
		3.0 to 3.6	—	—	$V_{CC} \times 0.3$		
		4.5 to 5.5	—	—	$V_{CC} \times 0.3$		
Output voltage	V_{OH}	Min to Max	$V_{CC} - 0.1$	—	—	V	$I_{OH} = -50 \mu\text{A}$
		2.3	2.0	—	—		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	—	—		$I_{OH} = -6 \text{ mA}$
		4.5	3.8	—	—		$I_{OH} = -12 \text{ mA}$
	V_{OL}	Min to Max	—	—	0.1		$I_{OL} = 50 \mu\text{A}$
		2.3	—	—	0.4		$I_{OL} = 2 \text{ mA}$
		3.0	—	—	0.44		$I_{OL} = 6 \text{ mA}$
		4.5	—	—	0.55		$I_{OL} = 12 \text{ mA}$
Input current	I_{IN}	0 to 5.5	—	—	± 1	μA	$V_I = 5.5 \text{ V}$ or GND
Quiescent supply current	I_{CC}	5.5	—	—	20	μA	$V_I = V_{CC}$ or GND, $I_O = 0$
Output leakage current	I_{OFF}	0	—	—	5	μA	V_I or $V_O = 0 \text{ V}$ to 5.5 V
Input capacitance	C_{IN}	3.3	—	1.9	—	pF	$V_I = V_{CC}$ or GND

- Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

- $V_{CC} = 2.5 \pm 0.2 \text{ V}$

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		$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$				
Item	Symbol	Min	Typ	Max	Min	Max	Unit	Test Conditions	FROM (Input) TO (Output)
Propagation delay time	t_{PLH}	—	7.7	17.6	1.0	21.0	ns	$C_L = 15 \text{ pF}$	A or B Y
	t_{PHL}	—	10.2	22.5	1.0	26.5		$C_L = 50 \text{ pF}$	
		—	7.4	15.8	1.0	19.0		$C_L = 15 \text{ pF}$	\overline{G}
		—	9.9	20.2	1.0	24.0		$C_L = 50 \text{ pF}$	

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- $V_{CC} = 3.3 \pm 0.3 \text{ V}$

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		$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$				
Item	Symbol	Min	Typ	Max	Min	Max	Unit	Test Conditions	FROM (Input) TO (Output)
Propagation delay time	t_{PLH}	—	5.3	11.0	1.0	13.0	ns	$C_L = 15 \text{ pF}$	A or B Y
	t_{PHL}	—	7.3	14.5	1.0	16.5		$C_L = 50 \text{ pF}$	
		—	5.1	9.2	1.0	11.0		$C_L = 15 \text{ pF}$	\overline{G}
		—	7.0	12.7	1.0	14.5		$C_L = 50 \text{ pF}$	

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- $V_{CC} = 5.0 \pm 0.5 \text{ V}$

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		$T_a = 25^\circ\text{C}$			$T_a = -40 \text{ to } 85^\circ\text{C}$				
Item	Symbol	Min	Typ	Max	Min	Max	Unit	Test Conditions	FROM (Input) TO (Output)
Propagation delay time	t_{PLH}	—	3.7	7.2	1.0	8.5	ns	$C_L = 15 \text{ pF}$	A or B Y
	t_{PHL}	—	5.2	9.2	1.0	10.5		$C_L = 50 \text{ pF}$	
		—	3.5	6.3	1.0	7.5		$C_L = 15 \text{ pF}$	\overline{G}
		—	4.9	8.3	1.0	9.5		$C_L = 50 \text{ pF}$	

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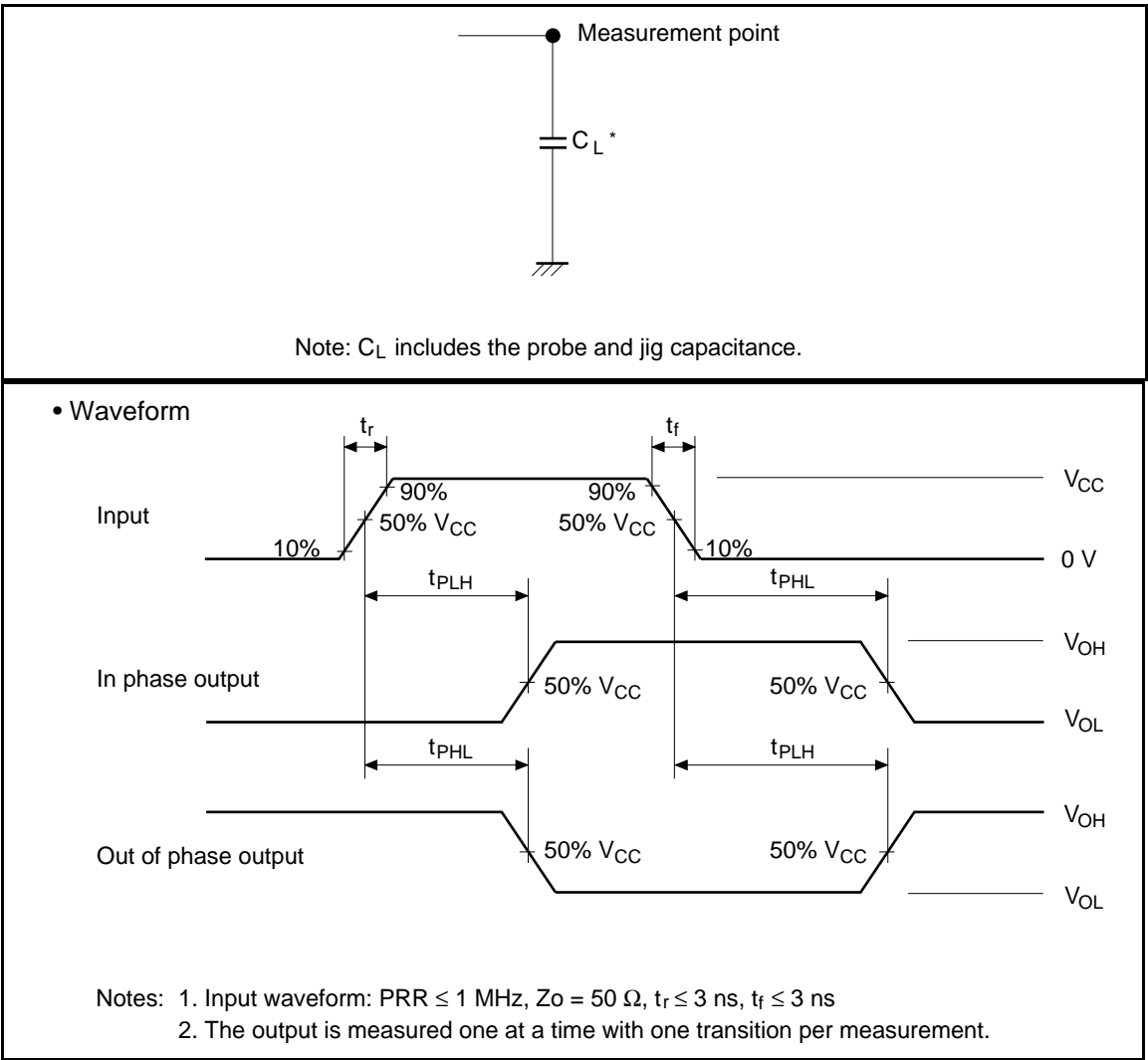
Operating Characteristics

- $C_L = 50\text{ pF}$

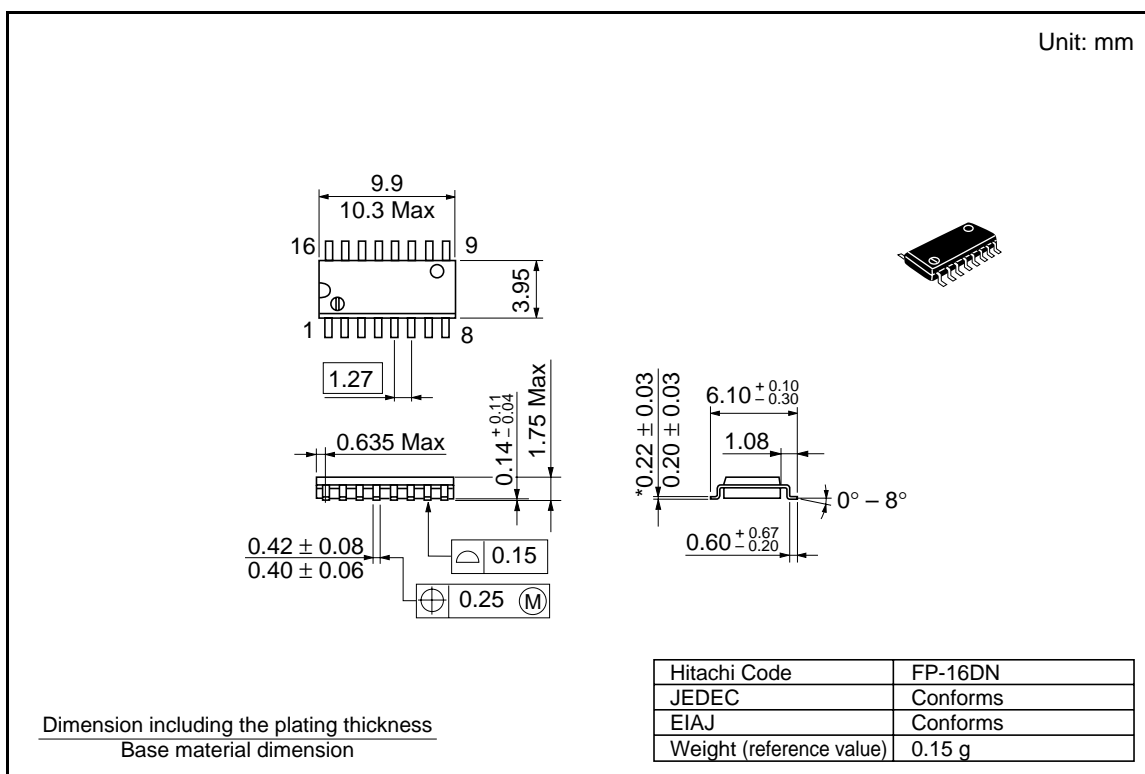
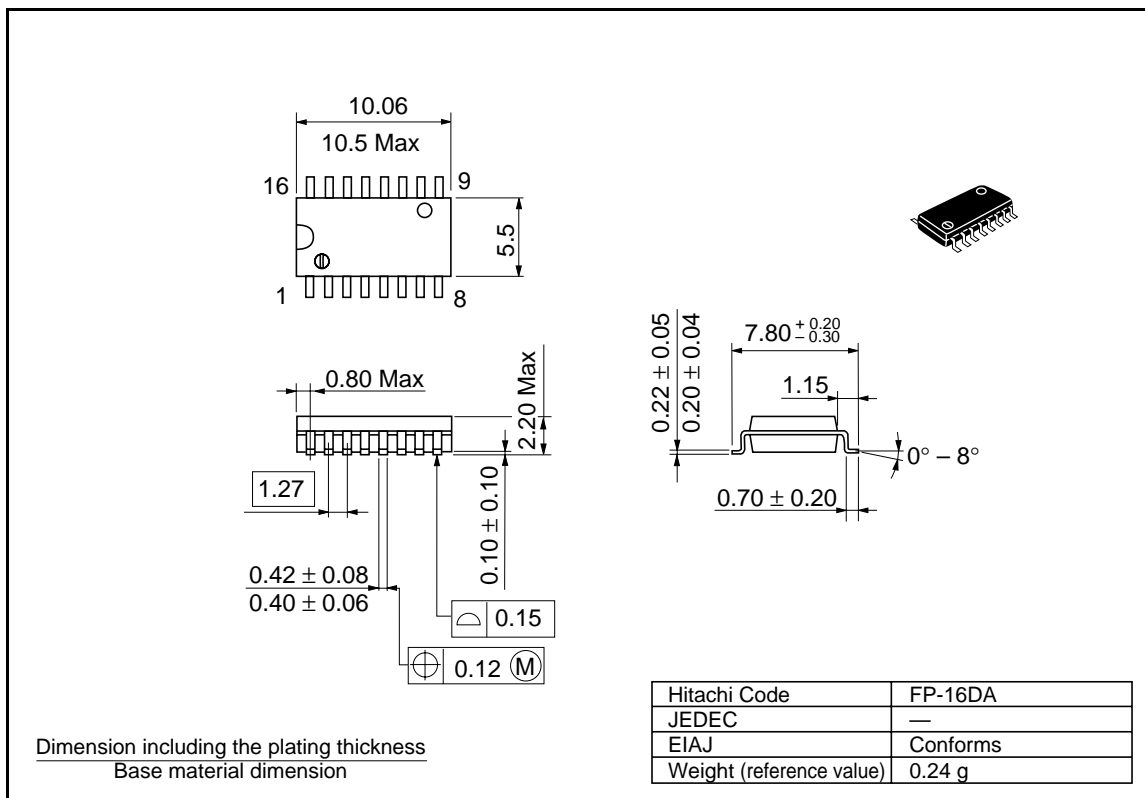
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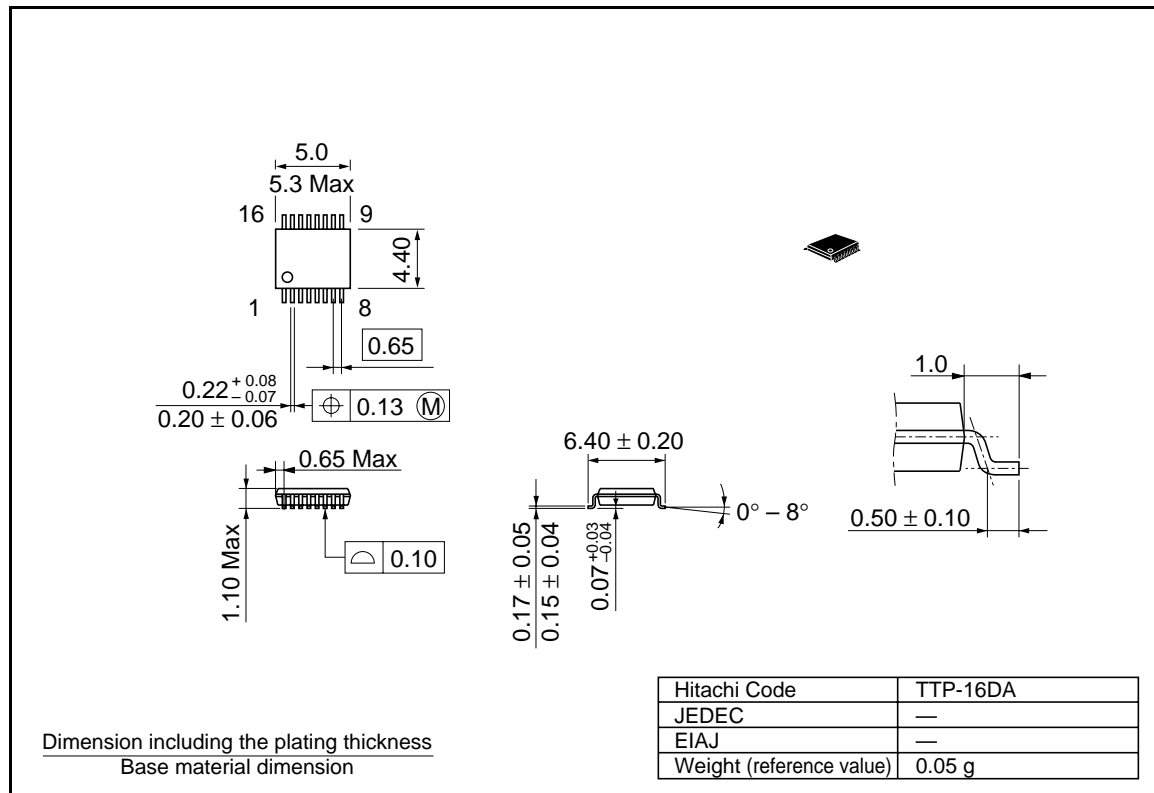
Ta = 25°C							
Item	Symbol	VCC (V)	Min	Typ	Max	Unit	Test Conditions
Power dissipation capacitance	CPD	3.3	—	17.3	—	pF	f = 10 MHz
		5.0	—	18.2	—		

Test Circuit



Package Dimensions





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