Dual Buffer

HITACHI

ADE-205-708 (Z)

Rev.0 Jan. 2003

Description

The HD74LV1GW16A has dual buffer in a 6 pin package. Low voltage and high speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

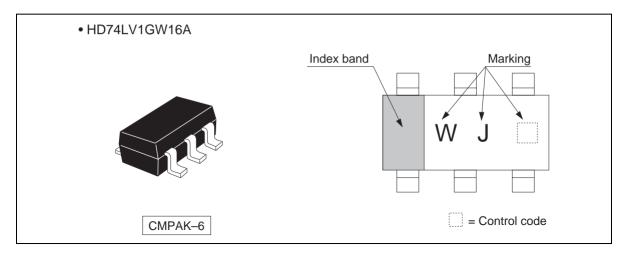
Features

- The basic gate function is lined up as hitachi uni logic series.
- Supplied on emboss taping for high speed automatic mounting.
- Supply voltage range : 1.65 to 5.5 V Operating temperature range : -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V) All outputs V_{O} (Max.) = 5.5 V (@ V_{CC} = 0 V)
- Output current ± 6 mA (@V_{CC} = 3.0 V to 3.6 V), ± 12 mA (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1GW16ACME	CMPAK-6 pin	CMPAK-6V(O)	CM	E (3,000 pcs / Reel)



Outline and Article Indication

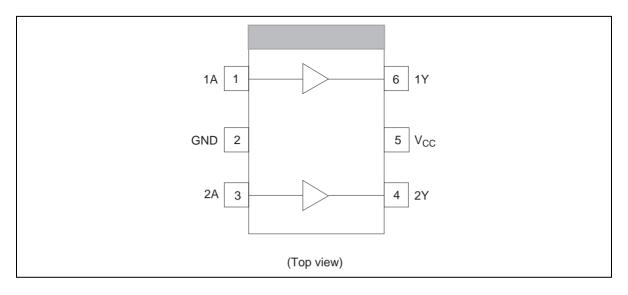


Function Table

Input A	Output Y
Н	Н
L	L

H : High level L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	Vı	-0.5 to 7.0	V	
Output voltage range *1, 2	Vo	-0.5 to V_{CC} + 0.5	V	Output : H or L
		-0.5 to 7.0		V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V _I < 0
Output clamp current	lok	±50	mA	$V_O < 0$ or $V_O > V_{CC}$
Continuous output current	Io	±25	mA	$V_{O} = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW	
Storage temperature	Tsta	-65 to 150	°C	

Notes:

- The absolute maximum ratings are values which must not individually be exceeded, and furthermore no two of which may be realized at the same time.
- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	Vcc	V	
Output current	I _{OL}	_	1	mA	$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
		_	2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
	I _{OH}	_	-1		$V_{CC} = 1.65 \text{ to } 1.95 \text{ V}$
		_	-2		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		_	-6		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		_	-12		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Input transition rise or fall rate	Δt / Δν	0	300	ns / V	V _{CC} = 1.65 to 1.95 V
		0	200		$V_{CC} = 2.3 \text{ to } 2.7 \text{ V}$
		0	100		$V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		$V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

• $Ta = -40 \text{ to } 85^{\circ}\text{C}$

Item	Symbol	V _{CC} (V) *	Min	Тур	Max	Unit	Test condition
Input voltage	V _{IH}	1.65 to 1.95	V _{CC} ×0.75	_	_	V	
		2.3 to 2.7	V _{CC} ×0.7	_	_	-	
		3.0 to 3.6	V _{CC} ×0.7	_	_	_	
		4.5 to 5.5	V _{CC} ×0.7	_	_	=	
	V _{IL}	1.65 to 1.95	_	_	V _{CC} ×0.25	-	
		2.3 to 2.7	_	_	V _{CC} ×0.3	-	
		3.0 to 3.6	_	_	V _{CC} ×0.3	-	
		4.5 to 5.5	_	_	V _{CC} ×0.3	=	
Hysteresis voltage	V _H	1.8	_	0.25	_	V	$V_T^+ - V_T^-$
		2.5	_	0.30	_	-	
		3.3	_	0.35	_	=	
		5.0	_	0.45	_	=	
Output voltage	V _{OH}	Min to Max	V _{CC} -0.1	_	_	V	$I_{OH} = -50 \ \mu A$
		1.65	1.4	_	_	=	$I_{OH} = -1 \text{ mA}$
		2.3	2.0	_	_	=	$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	_	=	$I_{OH} = -6 \text{ mA}$
		4.5	3.8	_	_	_	I _{OH} = -12 mA
	V _{OL}	Min to Max	_	_	0.1	=	I _{OL} = 50 μA
		1.65	_	_	0.3	=	I _{OL} = 1 mA
		2.3	_	_	0.4	_	I _{OL} = 2 mA
		3.0	_	_	0.44	=	I _{OL} = 6 mA
		4.5	_	_	0.55	=	I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μΑ	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I _{CC}	5.5	_	_	10	μΑ	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	I _{OFF}	0	_	_	5	μΑ	V_1 or $V_0 = 0$ to 5.5 V
Input capacitance	C _{IN}	3.3	_	3.0	_	pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

• $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	$T_a = 2$	25°C	$T_a = -40 \text{ to } 85^{\circ}\text{C}$		Unit		FROM	ТО	
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	11.6	20.0	1.0	22.0	ns	C _L = 15 pF	Α	Υ
delay time	t _{PHL}	_	18.6	30.4	1.0	33.0	_	C _L = 50 pF	_	

• $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	$T_a = 2$	25°C	$T_a = -40 \text{ to } 85^{\circ}\text{C}$		Unit		FROM	ТО	
		Min	Тур	Max	Min	Max		Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	7.0	11.7	1.0	14.0	ns	C _L = 15 pF	Α	Υ
delay time	t _{PHL}	_	10.5	15.5	1.0	18.0		C _L = 50 pF	_	

• $V_{CC} = 3.3 \pm 0.3 \text{ V}$

Item	Symbol	$T_a = 2$	25°C		$T_a = -40 \text{ to } 85^{\circ}\text{C}$		Unit		FROM	то
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	5.0	7.1	1.0	8.5	ns	C _L = 15 pF	А	Υ
delay time	t _{PHL}	_	7.5	10.6	1.0	12.0		$C_L = 50 pF$	_	

 $\bullet \quad V_{CC} = 5.0 \pm 0.5 \ V$

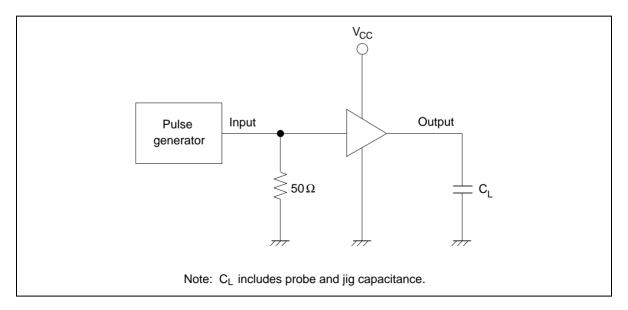
Item	Symbol	$T_a = 2$	25°C		$T_a = -4$	0 to 85°C	Unit		FROM	то
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	3.8	5.5	1.0	6.5	ns	C _L = 15 pF	Α	Υ
delay time	t _{PHL}	_	5.3	7.5	1.0	8.5	_	$C_L = 50 \text{ pF}$	_	

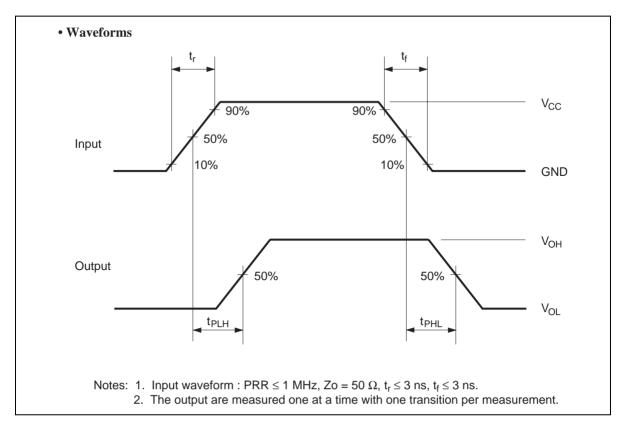
Operating Characteristics

• $C_L = 50 \text{ pF}$

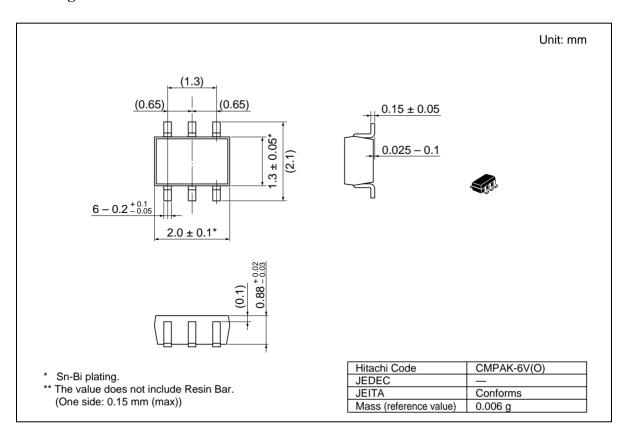
Item	Symbol	V _{CC} (V)	$T_a = 25$ °C			Unit	Test Conditions
			Min	Тур	Max		
Power dissipation	C_{PD}	3.3	_	8.5	_	pF	f = 10 MHz
capacitance		5.0	_	10.0	_		

Test Circuit





Package Dimensions



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Sales Offices

HITACHI

Semiconductor & Integrated Circuits Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: (03) 3270-2111 Fax: (03) 3270-5109

LIRI http://www.hitachisemiconductor.com/

For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive San Jose, CA 95134 Tel: <1> (408) 433-1990 Maidenhead

Hitachi Europe Ltd. Electronic Components Group Whitebrook Park Lower Cookham Road

Tel: <44> (1628) 585000 Fax: <44> (1628) 778322

Hitachi Europe GmbH Electronic Components Group Dornacher Str 3 D-85622 Feldkirchen Postfach 201, D-85619 Feldkirchen

Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00 Hitachi Asia I td Hitachi Tower 16 Collver Quay #20-00 Singapore 049318

Tel: <65>-6538-6533/6538-8577 Fax: <1>(408) 433-0223 Berkshire SL6 8YA, United Kingdom Fax: <65>-6538-6933/6538-3877

> Hitachi Asia I td (Taipei Branch Office) 4/F, No. 167, Tun Hwa North Road Hung-Kuo Building Taipei (105), Taiwan Tel: <886>-(2)-2718-3666 Fax: <886>-(2)-2718-8180 Telex: 23222 HAS-TP

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Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower

World Finance Centre Harbour City, Canton Road

Tsim Sha Tsui, Kowloon Hong Kong

URL: http://semiconductor.hitachi.com.hk

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