

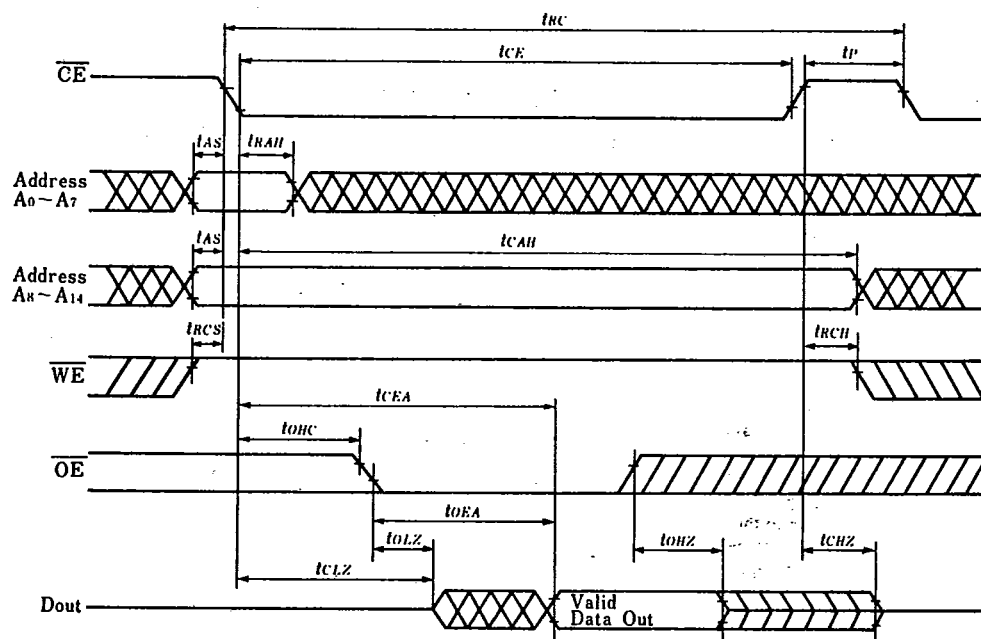
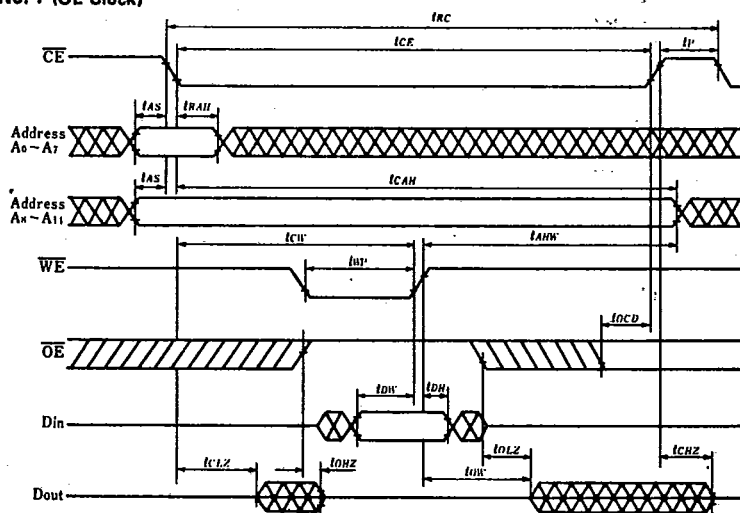
4496203 HITACHI/ LOGIC/ARRAYS/MEM

04E 12766 D

HM65256B Series

• Read Cycle No. 2 ($\overline{OE}$ controlled)

T-16-23-25

• Write Cycle No. 1 ($\overline{OE}$ Clock)

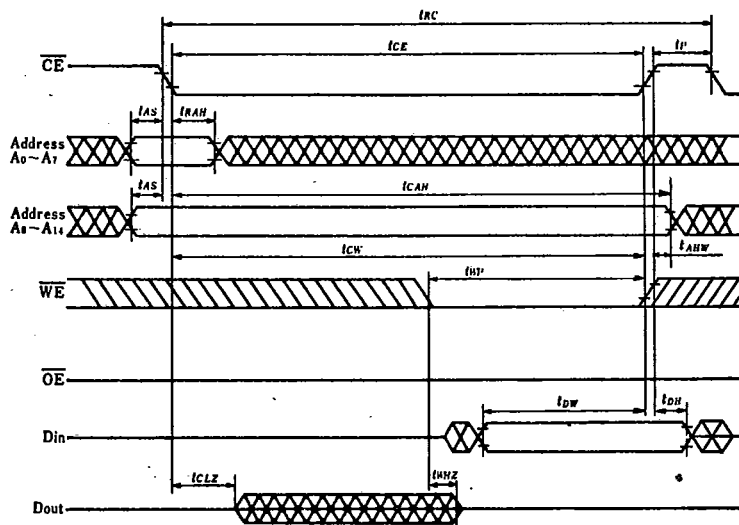
4496203 HITACHI/ LOGIC/ARRAYS/MEM

04E 12767 D

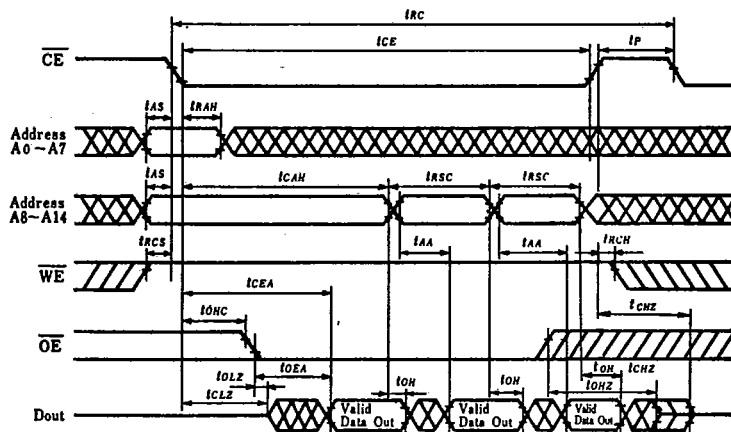
HM65256B Series

T-46-73-25

• Write Cycle No. 2 ($\overline{OE}$ low fix)



• Static Column Mode Read Cycle



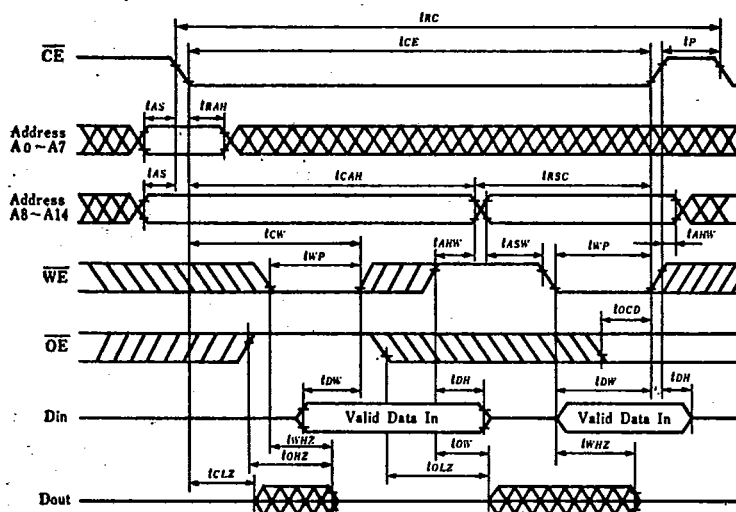
4496203 HITACHI/ LOGIC/ARRAYS/MEM

04E 12768 D

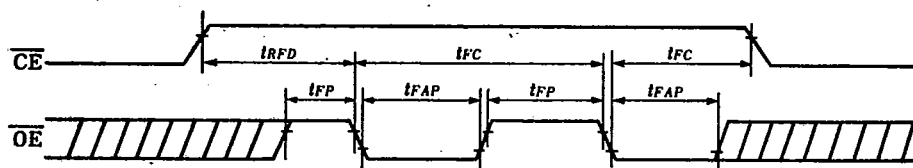
HM65256B Series

T-46-23-25

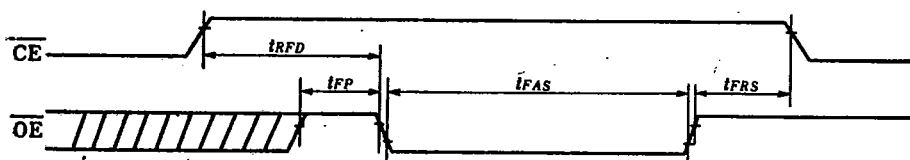
• Static Column Mode Write Cycle



• Automatic Refresh Cycle



• Self Refresh Cycle



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