



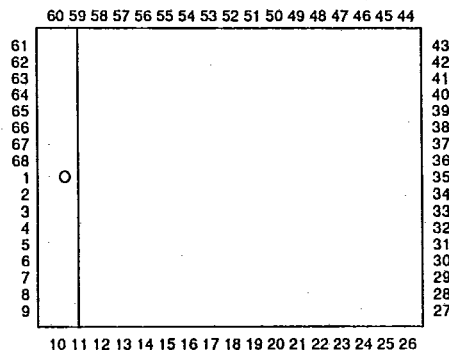
HM6666AL

MULTI I/O CONTROLLER CHIP

Features

1. Easily interfaces with most popular micro-processors.
2. Operate with IBM PC/XT/AT and compatibles.
3. Provides two Asynchronous Communication Elements.
 - a. Full double buffering eliminates need for precise synchronization.
 - b. Programmable Baud-Rate Generator from Baud 50 to 9600 Baud.
 - c. Line status reporting capabilities.
 - d. Full programmable serial-interface characteristics:
 - #. 5, 6, 7, or 8 bit characters
 - #. Even, Odd, or No-parity bit generation and detection
 - #. 1, 1½, or 2 stop bit generation
 - e. Modem control functions (CTS, RTS, DSR, DTR, RI and DCD)
 - f. Internal Diagnostic Capabilities.
 - g. Full priority interrupt system controls transmitter, receiver, error, line status and data set interrupts.
 - h. Complete status reporting capabilities.
4. Provides two parallel ports to attach printers.
 - a. High Driving Capabilities
 - b. Internal diagnostic capabilities
 - c. In addition to attach printer (Centronix Interface), it can be transmission with other parallel devices.
5. Single 5 volt power.
6. High performance low-power CMOS Multi-I/O controller chip.

Pin Assignment



P.C BOARD VIEW-AS VIEWED FROM THE COMPONENT SIDE OF THE P.C. BOARD.

Pin Number/Name

1	V _{DD}	10	CTS1	18	V _{SS}	27	DSR2	35	V _{DD}	44	SLIN1	52	V _{SS}	61	DLE2
2	I _{OR}	11	DTR1	19	A8	28	DCD2	36	RESET	45	INIT1	53	D3	62	IRQ5
3	I _{OW}	12	RTS1	20	A7	29	RI2	37	ERR1	46	AFX1	54	D2	63	BUSY2
4	A2	13	IRQ4	21	SIN2	30	AEN	38	SLCT1	47	STRB1	55	D1	64	ACK2
5	A1	14	SOUT2	22	SOUT2	31	A3	39	PE1	48	D7	56	D0	65	PE2
6	A0	15	SIN1	23	IRQ3	32	A5	40	ACK1	49	D6	57	STRB2	66	SLCT2
7	RI1	16	XTAL1	24	RTS2	33	A6	41	BUSY1	50	D5	58	AFX2	67	ERR2
8	DCD1	17	XTAL2	25	DTR2	34	A9	42	IRQ7	51	D4	59	INIT2	68	DIR
9	DSR1			26	CTS2			43	DLE1			60	SLIN2		