



HM82C215

ADDRESS/DATA BUFFER

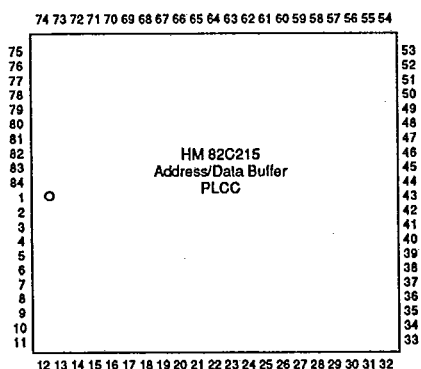
Features

- Bus conversion logic for 16 bit to 8 bit transfers
- Parity generation/detection logic

Functions

- Address buffers and latches
- Data buffers and latches.
- Bus conversion logic
- Parity generation/detection logic

Pin Assignment



Pin Number/Name

1	V _{SS}	12	AC1	22	V _{SS}	43	V _{SS}	64	V _{SS}	75	V _{SS}
2	D15	13	AC0	23	D8	44	XA16	65	MD9	76	DLE
3	D7	14	ACEN	24	D0	45	XA15	66	MD2	77	MD0
4	D14	15	IALE	25	A1	46	XA14	67	MD10	78	MD1
5	D6	16	D11	26	A2	47	XA13	68	MD3	79	MD6
6	D13	17	D3	27	A3	47	XA12	69	MD11	80	MD14
7	D5	18	D10	28	A4	49	XA11	70	MD4	81	MD7
8	D12	19	D2	29	A5	50	XA10	71	MD12	82	MD15
9	D4	20	D9	30	A6	51	XA9	72	MD5	83	PARERR
10	A0	21	D1	31	A7	52	XA8	73	MD13	84	V _{DD}
11	BHE			32	V _{DD}	53	HLDA1	74	DRD		