

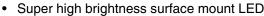
20783

Vishay Semiconductors

Little Star[®] 1 Watt Power SMD LED

Watt Power SMD L

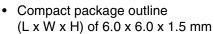


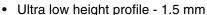






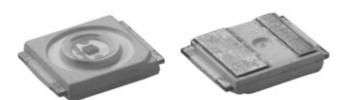








- Low thermal resistance; R_{th,IP} = 20 K/W
- Qualified according to JEDEC moisture sensitivity level 2a
- Compatible with IR reflow solder processes according to CECC 00802 and J-STD-020C
- Little Star[®] are class 1M LED products. Do not view directly with optical instrument
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC
- Automotive qualified AEC-Q101
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Optical efficiency typical up to 52 lm/W



DESCRIPTION

The VLMK/R/Y71.. is one of the most robust and light efficient LEDs in the market. With its extremely high level of brightness and the ultra low high profile, which is only 1.5 mm are highly suitable for both conventional lighting and specialized application such as automotive signal lights, traffic lights, channel lights, tube lights and garden lights among others.

PRODUCT GROUP AND PACKAGE DATA

Product group: LED
Package: SMD Little Star
Product series: power
Angle of half intensity: ± 60°

APPLICATIONS

- Automotive: exterior applications, e.g.: center high mounted stop light (CHMSL), rear combination lights (RCLs), signal lighting, etc.
- Communication: indicator and backlight in mobile phone
- Industry: white goods (e.g.: oven, microwave, etc.)
- Lighting: garden light, architecture lighting, general lighting, etc.

PARTS TABLE			
PART	COLOR, LUMINOUS INTENSITY (at I _F = 400 mA)	LUMINOUS FLUX CORRELATION BETWEEN LUM. FLUX/LUM. INTENSITY (at I _F = 400 mA)	TECHNOLOGY
VLMK71ABAD-GS08	Amber, I _V = (9000 to 18000) mcd	26100 to 52000 mlm	AllnGaP
VLMR71AAAC-GS08	Red, I _V = (7150 to 14000) mcd	20700 to 39000 mlm	AllnGaP
VLMY71AAAC-GS08	Yellow, I _V = (7150 to 14000) mcd	20700 to 39000 mlm	AllnGaP

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ABSOLUTE MAXIMUM RATINGS 1) VLMK71/VLMR71/VLMY71							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Forward current		I _F	400	mA			
Power dissipation		P _{tot}	1120	mW			
Junction temperature		T _j	+ 120	°C			
Surge current t < 10 µs, d = 0.1		I _{FM}	500	mA			
Operating temperature range		T _{amb}	- 40 to + 100	°C			
Storage temperature range		T _{stg}	- 40 to + 100	°C			
Thermal resistance junction/pin		R _{thJP}	20	K/W			

Note:

Not designed for reverse operation

 $^{^{1)}}$ T_{amb} = 25 °C, unless otherwise specified

OPTICAL AND ELECTRICAL CHARACTERISTICS 1) VLMK71, AMBER								
PARAMETER	TEST CONDITION	PART	INCLUDED TYPES	SYMBOL	MIN.	TYP.	MAX.	UNIT
			VLMK71AB	I _V	9000		11250	mcd
Luminous intensity	$I_F = 400 \text{ mA}$	VLMK71ABAD	VLMK71AC	I _V	11250		14000	mcd
			VLMK71AD	I _V	14000		18000	mcd
Dominant wavelength	I _F = 400 mA			λ_{d}	610		620	nm
Spectral bandwidth at 50 % I _{rel max}	I _F = 400 mA			Δλ		18		nm
Angle of half intensity	I _F = 400 mA			φ		60		deg
Forward voltage ²⁾	I _F = 400 mA			V_{F}	2.2		2.8	٧
Optical efficiency	I _F = 400 mA			ηopt		40	52	Im/W

Note:

¹⁾ T_{amb} = 25 °C, unless otherwise specified ²⁾ Forward voltages are tested at a current pulse duration of 1 ms and a tolerance of ± 0.05 V

OPTICAL AND ELECTRICAL CHARACTERISTICS 1) VLMR71, RED								
PARAMETER	TEST CONDITION	PART	INCLUDED TYPES	SYMBOL	MIN.	TYP.	MAX.	UNIT
			VLMR71AA	I _V	7150		9000	mcd
Luminous intensity	$I_F = 400 \text{ mA}$	VLMR71AAAC	VLMR71AB	I _V	9000		11250	mcd
			VLMR71AC	I _V	11250		14000	mcd
Dominant wavelength	I _F = 400 mA			λ_{d}	620		630	nm
Spectral bandwidth at 50 % I _{rel max}	I _F = 400 mA			Δλ		18		nm
Angle of half intensity	I _F = 400 mA			φ		60		deg
Forward voltage ²⁾	I _F = 400 mA			V_{F}	2.2		2.8	V
Optical efficiency	I _F = 400 mA			ηopt		30	39	lm/W

Note:

 $^{^{(1)}}$ T_{amb} = 25 °C, unless otherwise specified $^{(2)}$ Forward voltages are tested at a current pulse duration of 1 ms and a tolerance of \pm 0.05 V



OPTICAL AND ELECTRICAL CHARACTERISTICS 1) VLMY71, YELLOW								
PARAMETER	TEST CONDITION	PART	INCLUDED TYPES	SYMBOL	MIN.	TYP.	MAX.	UNIT
			VLMY71AA	I _V	7150		9000	mcd
Luminous intensity	$I_F = 400 \text{ mA}$	VLMY71AAAC	VLMY71AB	Ι _V	9000		11250	mcd
			VLMY71AC	l _V	11250		14000	mcd
Dominant wavelength	I _F = 400 mA			λ_{d}	585		597	nm
Spectral bandwidth at 50 % I _{rel max}	I _F = 400 mA			Δλ		15		nm
Angle of half intensity	I _F = 400 mA			φ		± 60		deg
Forward voltage 2)	I _F = 400 mA			V _F	2.2		2.8	V
Optical efficiency	I _F = 400 mA			ηopt		30	39	Im/W

Note:

²⁾ Forward voltages are tested at a current pulse duration of 1 ms and a tolerance of \pm 0.05 V

LUMINOUS INTENSITY/FLUX CLASSIFICATION RED/AMBER/YELLOW						
GROUP	LUMINOUS INT	LUMINOUS INTENSITY I _V (MCD)		LUX Ф _V (MLM) FION TABLE		
STANDARD	MIN.	MAX.	MIN.	MAX.		
AA	7150	9000	20700	26100		
AB	9000	11250	26100	33000		
AC	11250	14000	33000	39000		
AD	14000	18000	39000	52000		
AE	18000	22400	52000	71000		
AF	22400	28500	71000	97000		

Note:

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of \pm 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped in any one reel. In order to ensure availability, single wavelength groups will not be orderable.

COLOR CLASSIFICATION						
	DOM. WAVE	LENGTH (NM)	DOM. WAVELENGTH (NM)			
GROUP	YEL	YELLOW		BER		
	MIN.	MAX.	MIN.	MAX.		
Α	585	588	610	616		
В	588	591	616	620		
С	591	594				
D	594	597				

Note

Wavelengths are tested at a current pulse duration of 25 ms and an accuracy of \pm 1 nm.

FORWARD VOLTAGE CLASSIFICATION						
GROUP	FORWARD VOLTAGE (V)					
GROUP	MIN.	MAX.				
02	2.2	2.5				
03	2.5	2.8				

Note:

Forward voltages are tested at a current pulse duration of 25 ms and a tolerance of \pm 0.05 V.

In order to ensure availability, a single forward voltage group can not be ordered.

 $^{^{1)}}$ T_{amb} = 25 °C, unless otherwise specified

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

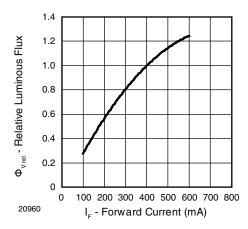


Figure 1. Relative Luminous Flux vs. Forward Current

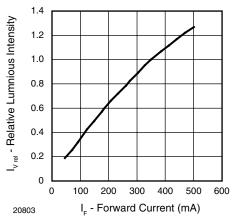


Figure 2. Relative Luminous Intensity vs. Forward Current

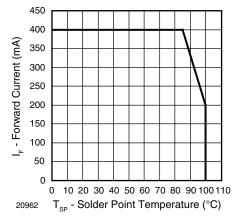


Figure 3. Forward Current vs. Solder Point Temperature

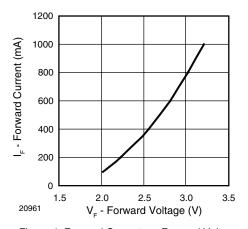


Figure 4. Forward Current vs. Forward Voltage

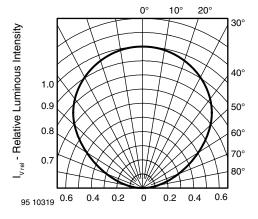
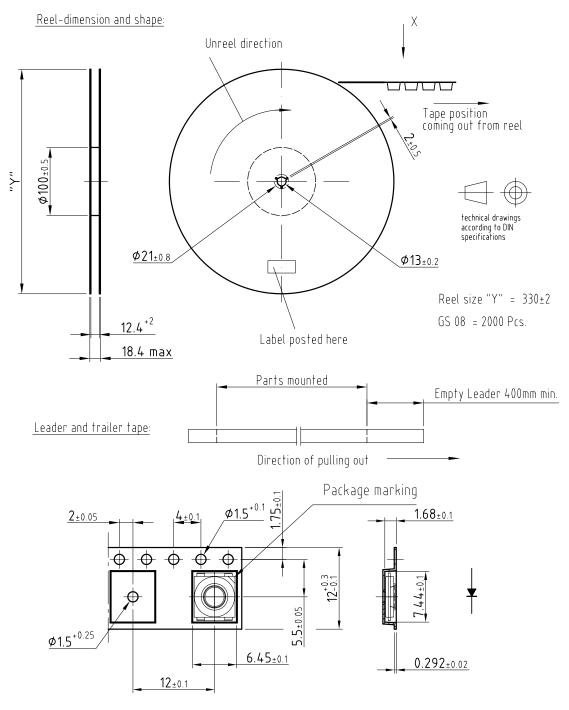


Figure 5. Relative Luminous Intensity vs. Angular Displacement

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TAPING DIMENSIONS in millimeters



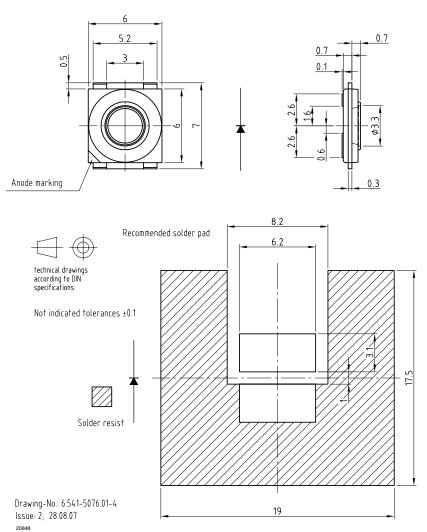
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Issue: 3; 22.01.08

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PACKAGE DIMENSIONS/SOLDERING PADS DIMENSIONS in millimeters



SOLDERING PROFILE

IR Reflow Soldering Profile for Lead (Pb)-free Soldering Preconditioning acc. to JEDEC Level 2a

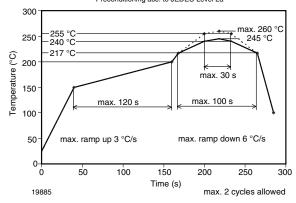
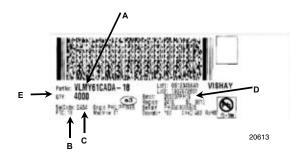


Figure 6. Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020C)



BAR CODE PRODUCT LABEL EXAMPLE:



- A) Type of component
- B) Manufacturing plant
- C) SEL selection code (bin):

e.g.: DA = code for luminous intensity group

5 = code for color group

4 = code for forward voltage

D) Batch:

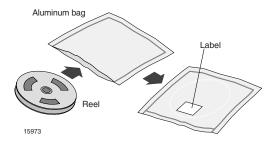
200707 = year 2007, week 07

PH19 = plant code

E) Total quantity

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

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RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

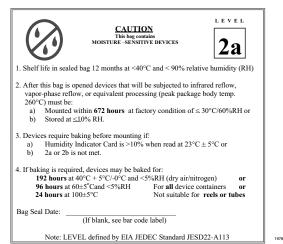
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at $40 \,^{\circ}\text{C} + 5 \,^{\circ}\text{C/-} \, 0 \,^{\circ}\text{C}$ and $< 5 \,^{\circ}\text{KH}$ (dry air/nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

VLMK/R/Y71..

Vishay Semiconductors



OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

The IEC/EN standards require that the desired classification Accessible Emission Limit shall not be exceeded in "Normal" and "Single Fault Conditions". This product is in Compliance with the requirement in CEN/IEC/EN60825-1 to ensure that required classifications are not exceeded in single fault conditions.

> We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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