Crystal Clock Oscillators Low Power Real-Time HS-326/336 Series

Description

The HS-326/336 Series of quartz tuning fork clock oscillators offers a 32.768 kHz output frequency that is CMOS compatible. The HS-326 provides a ± 10 ppm calibration tolerance at 25° C with ± 45 ppm overall tolerance at 0° to 45° C; the HS-336 offers ± 25 ppm calibration tolerance with ± 60 ppm overall tolerance at 0° to 45° C. Supply current is typically 125 μ A at V_{DD} = 5.0 V, decreasing rapidly to 25 μ A at V_{DD} = 3.0 V.

All units are resistance welded in an all metal package, offering RFI shielding, and are designed to survive standard wave soldering operations without damage. Insulated standoffs to enhance board cleaning are standard.

Suggested Applications

HS-326/336 Series quartz tuning fork clock oscillators are designed to provide low power consumption for battery backup and precise frequency control that is unavailable with conventional 32.768 kHz tuning forks.

Features

Real time clock output frequency
User specified calibration and overall tolerances
Low power consumption
Case at electrical ground
All metal, resistance weld, hermetically sealed package
High shock resistance, to 3,000 G

^{1.} Measured between $V_{DD} = 10\%$ to 90%, $C_L = 15$ pf.

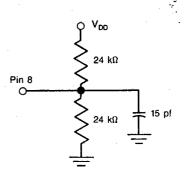
Specifications

PARAMETER	CONDITIONS		MINIMUM	MAXIMUM	
General Characteristics					
Supply voltage (V _{DD})	Supply Breakdown	÷	3.0 V	5.7 V 120 V	
Supply current (I _{DD})	V _{DD} @ 3.0 V 4.0 V 4.5 V 5.25 V 5.7 V			25 μΑ Typ. 75 μΑ Typ. 100 μΑ Typ. 150 μΑ Typ. 200 μΑ Typ.	
Operating temperature (T _A) Storage temperature (T _S) Lead temperature (T _L)	— — Soldering, 10 s		0° C -35° C -	70° C +85° C 300° C	
Output Characteristics	C.				
Frequency Tolerance	Calibration 25° C				
•	HS-326			±10 ppm	
	HS-336		. 	±25 ppm	
	Overall 0° to 45° C				
	HS-326			±45 ppm	
	HS-336		_	±60 ppm	
Symmetry	0.5 V _{DD}		40/60%	60/40%	
Logic 0 (V _{OL})	Driving equiv. load			0.2 V	
Logic 1 (V _{OH})	Driving equiv. load		V_{DD} $-0.2 V$	_ · ·	
Logic 0 (I _{OL} sink)	Driving equiv. load		•	600 μΑ	
Logic 1 (I _{OH} source)	Driving equiv. load		_	600 μΑ	
Rise & fall time (t _{.r} , t _f) ¹	$V_{DD} = 3.0 \text{ V}$			750 ns	
	$V_{DD} = 5.0 \text{ V}$		· <u> </u>	175 ns	* * * * * * * * * * * * * * * * * * * *

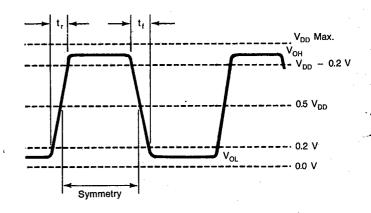


0.815 Max. (20.70)0.515 Max. (13.08) 0.439 Max. (11.15) 0.220 Max. (5.59) 0.250 ± 0.02 (6.35 ± 0.050) INSULATED STANDOFFS 0.020 ± 0.010 (0.51 ± 0.254) 0.018 ± 0.002 (0.45 ± 0.5) 0.600 ± 0.005 (15.24 ± 0.13) PIN #7 CONNECTED SQUARE CORNER PIN #1 TO CASE 0.300 ± 0.005 (7.62 ± 0.13) 0.180 ± 0.008 (4.58 ± 0.20) PIN #14 PIN #8 0.480 ± 0.008 (12.20 ± 0.20) Pin Connection NC 7 gnd and case 8 Output 14 V_{DD}

Equivalent Load



Output Waveform



HS-326/336 Series

Dimensions are for reference only.

This information is believed to be reliable at the time of printing; no responsibility is assumed for inaccuracies. NEL reserves the right to make changes at any time.

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