Variable Capacitance Diode for VCO



ADE-208-1563 (Z)

Rev.0 Nov. 2002

Features

- High capacitance ratio. (n = 2.20 min)
- Low series resistance. (rs = $0.6 \Omega \text{ max}$)
- Extremely small Flat Package (EFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVL355B	В	EFP

Pin Arrangement

Cathode mark	\$
2	1. Cathode 2. Anode



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit	
Reverse voltage	V _R	15	V	
Junction temperature	Tj	125	٥C	
Storage temperature	Tstg	-55 to +125	°C	

Electrical Characteristics

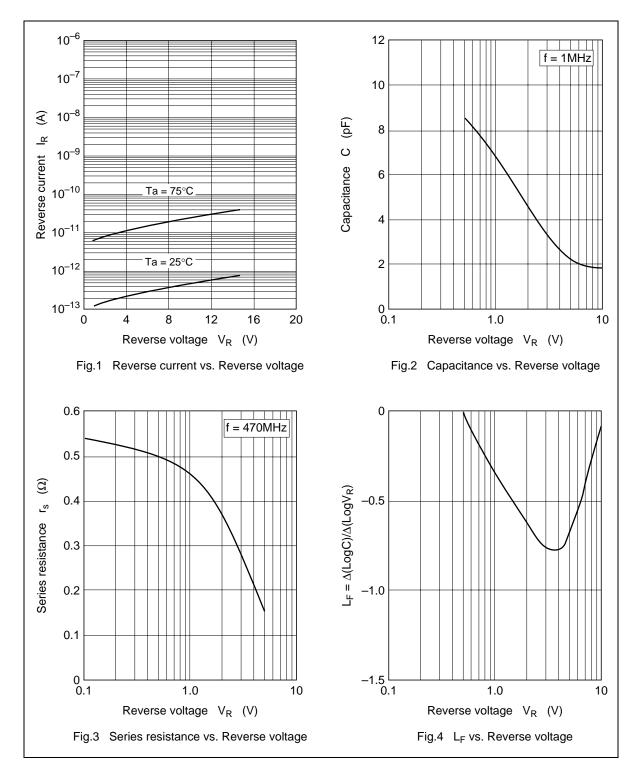
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}			10	nA	$V_{R} = 15 V$
	I _{R2}			100		V _R = 15 V, Ta = 60°C
Capacitance	C ₁	6.40		7.20	pF	V _R = 1 V, f = 1 MHz
	C ₄	2.55		2.95		$V_{R} = 4 \text{ V}, \text{ f} = 1 \text{ MHz}$
Capacitance ratio	n	2.20				C ₁ / C ₄
Series resistance	r _s			0.6	Ω	V _R = 1 V, f = 470 MHz

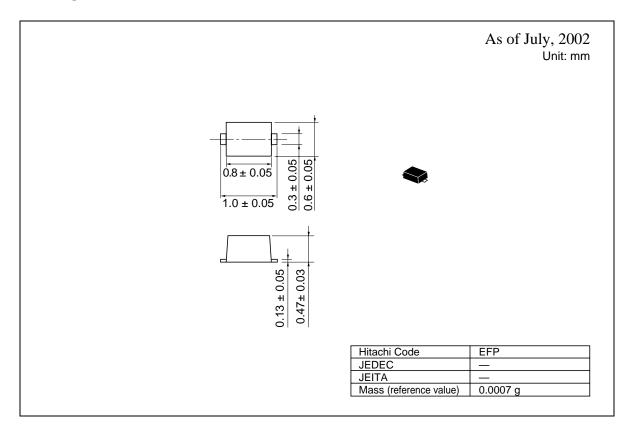
Notes: 1. Please do not use the soldering iron due to avoid high stress to the EFP package.

2. The material of lead is exposed for cutting plane. Therefore, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristic



Package Dimensions





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