

ICD20291

PC Notebook/Subnotebook/Palmtop Clock Generator

Second-Generation Single-Chip Oscillator for Portable 3.3V & 5V Personal Computers

- 8 Independent Clock Outputs
Handle all Clocking for Portable Personal Computers
- Upward Compatible with Industry Standard ICD2028; Plug Compatible with ICD2029
- Multiple Power-Down Modes with Suspend & Shutdown Capabilities
- 4 User-Configurable Outputs
- CPU Clock Frequency Range: 196 KHz – 80 MHz (100 MHz @ 5V)
- Low Skew CPU Clock, CPU Clock $\div 2$ and Buffered CPU Clock Options
- Full Intel Clock Specification Compliance
- Supports Ethernet & SCSI Timing Specifications
- PLL Oscillator Input Derived from Single 14.31818 MHz or 20 MHz Crystal (Other Frequencies Available on Request)
- Battery Input Runs 32.768 KHz Clock During Power-Down
- 3-State Oscillator Control Disables Outputs for Testing
- 3.3V & 5V Operation
- Low-Power, High-Speed CMOS Technology
- Available in 20-Pin SOIC Package Configuration

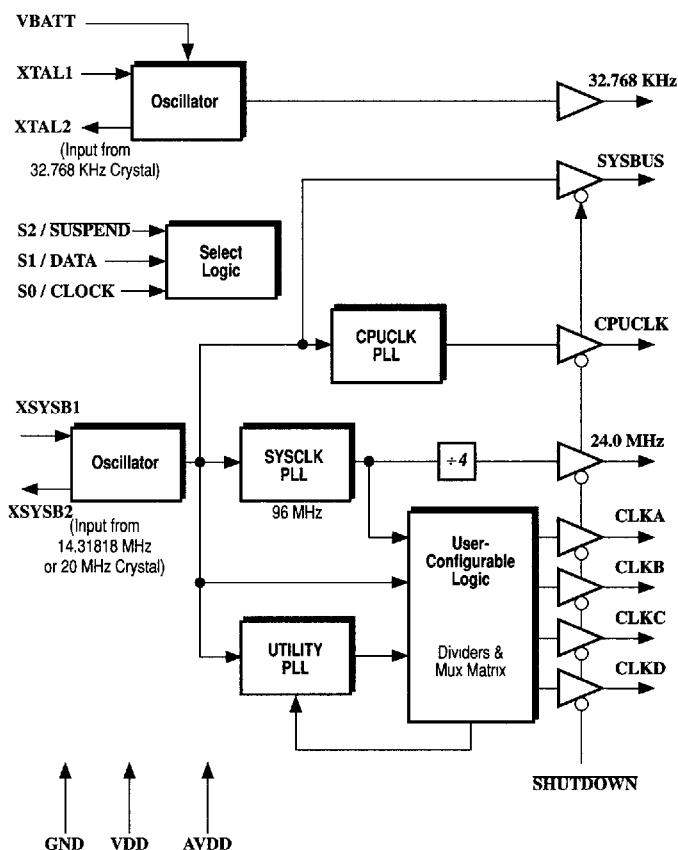
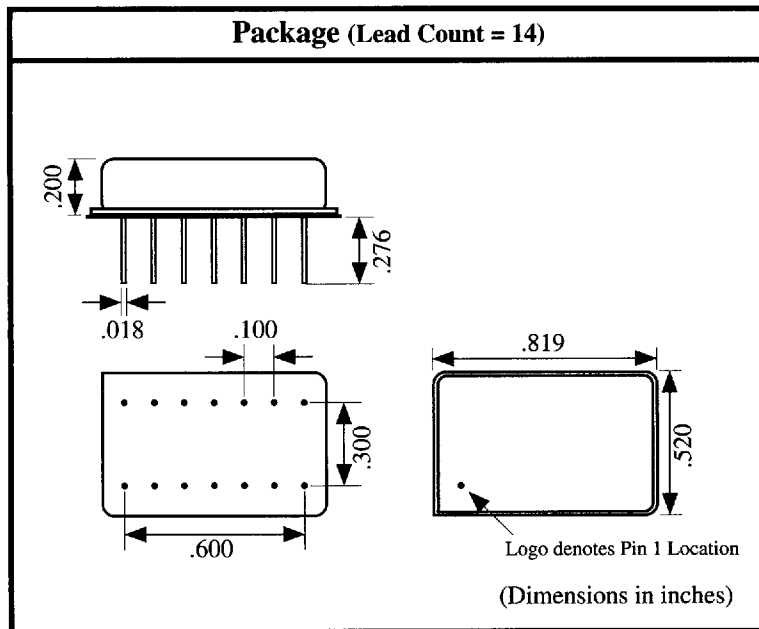


Fig. 2: ICD20291 Block Diagram

14-Pin Packages

Table 1: 14-Pin Metal Can Outline



16-Pin Packages

Table 2: 16-Pin SOIC Outline

Symbol	MIN	MAX	Package (Lead Count = 16)
A	.099	.104	
A1	.004	.009	
B	.014	.019	
C	.010	REF	
D	.405	.410	
E	.294	.299	
e	.050	TYP	
H	.402	.419	
h	.025	x 45°	
L	.030	.040	
∞	0°	8°	
K	.088	.098	
M	.020	.030	
N	.335	.351	

(Dimensions in inches)

20-Pin Packages

Table 3: 20-Pin SOIC Outline

Symbol	MIN	MAX	Package (Lead Count = 20)
A	.099	.104	
A1	.004	.009	
B	.014	.019	
C	.010	REF	
D	.505	.512	
E	.294	.299	
e	.050	TYP	
H	.402	.419	
h	.025	x 45°	
L	.030	.040	
∞	0°	8°	
K	.088	.098	
M	.020	.030	
N	.335	.351	

(Dimensions in inches)