

May 1992

Automatic Battery Back-Up Switch

Features

- Automatically Connects Output to the Greater of Either Input Supply Voltage
- If Main Power to External Equipment is Lost, Circuit Will Automatically Connect Battery Backup
- Reconnects Main Power When Restored
- Logic Indicator Signaling Status of Main Power
- Low Impedance Connection Switches
- Low Internal Power Consumption
- Wide Supply Range: 2.5V to 15V
- Low Leakage Between Inputs
- External Transistors May Be Added If Very Large Currents Need to Be Switched

Applications

- On Board Battery Backup for Real-Time Clocks, Timers, or Volatile RAMs
- Over/Under Voltage Detector
- Peak Voltage Detector
- Other Uses:
 - Portable Instruments, Portable Telephones, Line Operated Equipment

Description

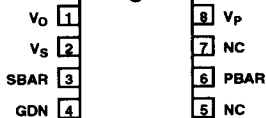
The Harris ICL7673 is a monolithic CMOS battery backup circuit that offers unique performance advantages over conventional means of switching to a backup supply. The ICL7673 is intended as a low-cost solution for the switching of systems between two power supplies; main and battery backup. The main application is keep-alive-battery power switching for use in volatile CMOS RAM memory systems and real time clocks. In many applications this circuit will represent a low insertion voltage loss between the supplies and load. This circuit features low current consumption, wide operating voltage range, and exceptionally low leakage between inputs. Logic outputs are provided that can be used to indicate which supply is connected and can also be used to increase the power switching capability of the circuit by driving external PNP transistors.

Ordering Information

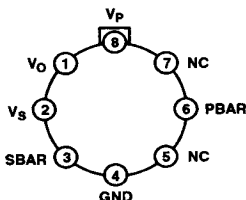
PART NUMBER	TEMPERATURE	PACKAGE
ICL7673CPA	0°C to +70°C	8 Lead Mini-DIP
ICL7673CBA	0°C to +70°C	8 Lead SOIC
ICL7673ITV	-25°C to +85°C	9 Lead TO-99

Pinouts

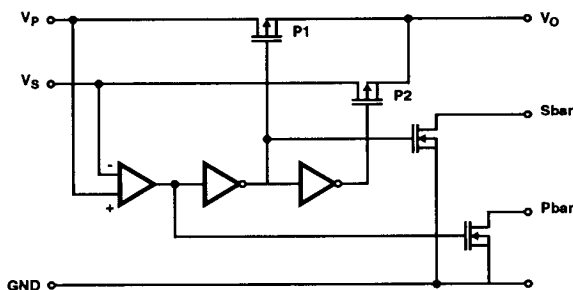
8 LEAD SOIC AND MINI-DIP
TOP VIEW



8 LEAD TO-99
TOP VIEW



Functional Block Diagram



VP > VS, P1 SWITCH ON AND Pbar SWITCH ON
 VS > VP, P2 SWITCH ON AND Sbar SWITCH ON

CAUTION: These devices are sensitive to electrostatic discharge. Users should follow proper I.C. Handling Procedures.
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File Number **3183**

7-118

Specifications ICL7673

Absolute Maximum Ratings

Input Supply (V_P or V_S) Voltage	GND - 0.3V to +18V	Continuous Current	
Output Voltages P_{bar} and S_{bar}	GND - 0.3V to +18V	Input V_P (at $V_P = 5V$) See Note	38mA
Peak Current		Input V_S (at $V_S = 3V$)	30mA
Input V_P (at $V_P = 5V$) See Note	38mA	P_{bar} or S_{bar}	50mA
Input V_S (at $V_S = 3V$)	30mA	Package Dissipation	300mW
P_{bar} or S_{bar}	150mA	Derate	6.1mW/°C

NOTE: Derate above +25°C by 0.38mA/°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Operating Conditions

Operating Temperature Range:		Storage Temperature	-65°C to +150°C
ICL7673C	0°C to +70°C	Lead Temperature (Soldering, 10s)	300°C
ICL7673L	-25°C to +85°C		

Electrical Specifications ($T_A = +25^\circ\text{C}$ Unless Otherwise Specified)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
Input Voltage	V_P	$V_S = 0V, I_{LOAD} = 0mA$	2.5	-	15	V
	V_S	$V_P = 0V, I_{LOAD} = 0mA$	2.5	-	15	V
Quiescent Supply Current	I_q	$V_P = 0V, V_S = 3V, I_{LOAD} = 0mA$	-	1.5	5	μA
Switch Resistance P1 (Note 1)	$R_{ds(on)P1}$	$V_P = 5V, V_S = 3V, I_{LOAD} = 15mA$	-	8	15	Ω
		At $T_A = 85^\circ\text{C}$	-	16	-	Ω
		$V_P = 9V, V_S = 3V, I_{LOAD} = 15mA$	-	6	-	Ω
		$V_P = 12V, V_S = 3V, I_{LOAD} = 15mA$	-	5	-	Ω
Temperature Coefficient of Switch Resistance P1	$T_{C(P1)}$	$V_P = 5V, V_S = 3V, I_{LOAD} = 15mA$	-	0.5	-	%/°C
Switch Resistance P2 (Note 1)	$R_{ds(on)P2}$	$V_P = 0V, V_S = 3V, I_{LOAD} = 1mA$	-	40	100	Ω
		At $T_A = 85^\circ\text{C}$	-	60	-	Ω
		$V_P = 0V, V_S = 5V, I_{LOAD} = 1mA$	-	26	-	Ω
		$V_P = 0V, V_S = 9V, I_{LOAD} = 1mA$	-	16	-	Ω
Temperature Coefficient of Switch Resistance P2	$T_{C(P2)}$	$V_P = 0V, V_S = 3V, I_{LOAD} = 1mA$	-	0.7	-	%/°C
Leakage Current (V_P to V_S)	$I_{L(P_S)}$	$V_P = 5V, V_S = 3V, I_{LOAD} = 10mA$	-	0.01	20	nA
		At $T_A = 85^\circ\text{C}$	-	35	-	nA
Leakage Current (V_P to V_S)	$I_{L(S_P)}$	$V_P = 0V, V_S = 3V, I_{LOAD} = 10mA$	-	0.01	50	nA
		at $T_A = 85^\circ\text{C}$	-	120	-	nA
Open Drain Output Saturation Voltages	V_{OPbar}	$V_P = 5V, V_S = 3V, I_{SINK} = 3.2mA, I_{LOAD} = 0mA$	-	85	400	mV
		At $T_A = 85^\circ\text{C}$	-	120	-	mV
		$V_P = 9V, V_S = 3V, I_{SINK} = 3.2mA, I_{LOAD} = 0mA$	-	50	-	mV
		$V_P = 12V, V_S = 3V, I_{SINK} = 3.2mA, I_{LOAD} = 0mA$	-	40	-	mV

Specifications ICL7673

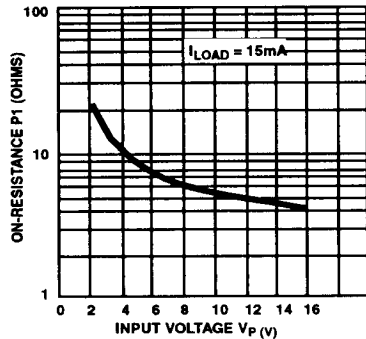
Electrical Specifications ($T_A = +25^\circ\text{C}$ Unless Otherwise Specified) (Continued)

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
Open Drain Output Saturation Voltages	V_{OSbar}	$V_P = 0\text{V}, V_S = 3\text{V}, I_{SINK} = 3.2\text{mA}, I_{LOAD} = 0\text{mA}$	-	150	400	mV
		at $T_A = 85^\circ\text{C}$	-	210	-	mV
		$V_P = 0\text{V}, V_S = 5\text{V}, I_{SINK} = 3.2\text{mA}, I_{LOAD} = 0\text{mA}$	-	85	-	mV
		$V_P = 0\text{V}, V_S = 9\text{V}, I_{SINK} = 3.2\text{mA}, I_{LOAD} = 0\text{mA}$	-	50	-	mV
Output Leakage Currents of P_{bar} and S_{bar}	I_{LPbar}	$V_P = 0\text{V}, V_S = 15\text{V}, I_{LOAD} = 0\text{mA}$	-	50	500	nA
		at $T_A = 85^\circ\text{C}$	-	900	-	nA
	I_{LSbar}	$V_P = 15\text{V}, V_S = 0\text{V}, I_{LOAD} = 0\text{mA}$	-	50	500	nA
		at $T_A = 85^\circ\text{C}$	-	900	-	nA
Switchover Uncertainty for Complete Switching of Inputs and Open Drain Outputs	$V_P - V_S$	$V_S = 3\text{V}, I_{SINK} = 3.2\text{mA}, I_{LOAD} = 15\text{mA}$	-	± 10	± 50	mV

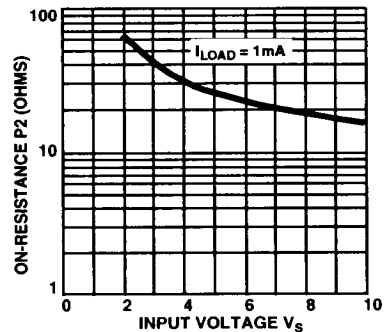
NOTE:

1. The Minimum input to output voltage can be determined by multiplying the load current by the switch resistance.

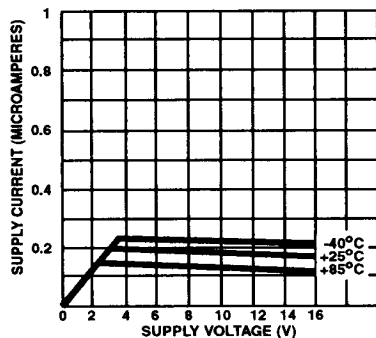
Typical Performance Characteristics



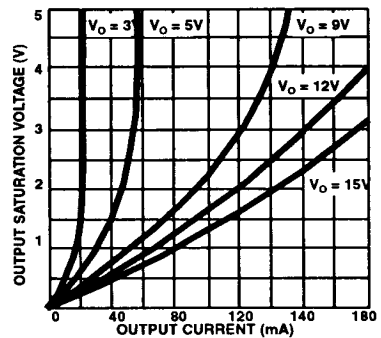
ON-RESISTANCE SWITCH P1 AS A FUNCTION OF INPUT VOLTAGE V_P



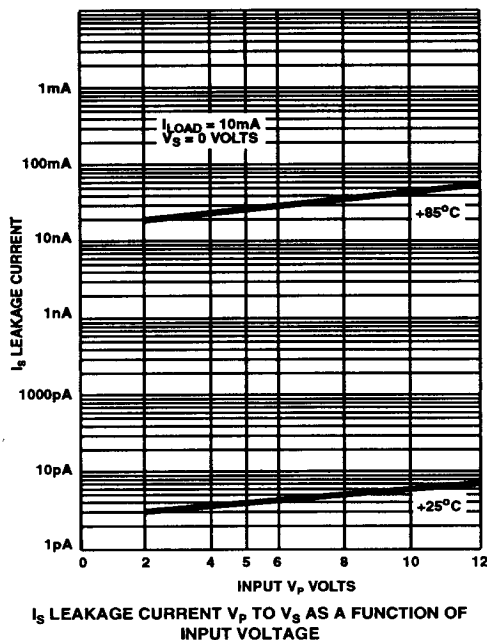
ON-RESISTANCE SWITCH P2 AS A FUNCTION OF INPUT VOLTAGE V_S



SUPPLY CURRENT AS A FUNCTION OF SUPPLY VOLTAGE



P_{bar} OR S_{bar} SATURATION VOLTAGE AS A FUNCTION OF OUTPUT CURRENT



Detailed Description

As shown in the functional diagram (Figure 1), the ICL7673 includes a comparator which senses the input voltages V_P and V_S . The output of the comparator drives the first inverter and the open-drain N-channel transistor P_{bar} . The first inverter drives a large P-channel switch, P_1 , a second inverter, and another open-drain N-channel transistor, S_{bar} . The second inverter drives another large P-channel switch P_2 . The ICL7673, connected to a main and a backup power supply, will connect the supply of greater potential to its output. The circuit provides break-before-make switch action as it switches from main to backup power in the event of a main power supply failure. For proper operation, inputs V_P and V_S must not be allowed to float, and, the difference in the two supplies must be greater than 50 millivolts. The leakage current through the reverse biased parasitic diode of switch P_2 is very low.

Output Voltage

The output operating voltage range is 2.5 to 15 volts. The insertion loss between either input and the output is a function of load current, input voltage, and temperature. This is due to the P-channels being operated in their triode region, and, the ON-resistance of the switches is a function of output voltage V_O . The ON-resistance of the P-channels have positive temperature coefficients, and therefore as temperature increases the insertion loss also increases. At low load currents the output voltage is nearly equal to the greater of the two inputs. The maximum voltage drop across switch P_1 or

P_2 is 0.5 volts, since above this voltage the body-drain parasitic diode will become forward biased. Complete switching of the inputs and open-drain outputs typically occurs in 50 microseconds.

Input Voltage

The input operating voltage range for V_P or V_S is 2.5 to 15 volts. The input supply voltage (V_P or V_S) slew rate should be limited to 2 volts per microsecond to avoid potential harm to the circuit. In line-operated systems, the rate-of-rise (or fall) of the supply is a function of power supply design. For battery applications it may be necessary to use a capacitor between the input and ground pins to limit the rate-of-rise of the supply voltage. A low-impedance capacitor such as a 0.047 μ F disc ceramic can be used to reduce the rate-of-rise.

Status Indicator Outputs

The N-channel open drain output transistors can be used to indicate which supply is connected, or can be used to drive external PNP transistors to increase the power switching capability of the circuit. When using external PNP power transistors, the output current is limited by the beta and thermal characteristics of the power transistors. The application section details the use of external PNP transistors.

Applications

A typical discrete battery backup circuit is illustrated in Figure 3. This approach requires several components, substantial printed circuit board space, and high labor cost. It also consumes a fairly high quiescent current. The ICL7673 battery backup circuit, illustrated in Figure 4, will often replace such discrete designs and offer much better performance, higher reliability, and lower system manufacturing cost. A trickle charge system could be implemented with an additional resistor and diode as shown in Figure 5. A complete low power AC to regulated DC system can be implemented using the ICL7673 and ICL7663S micropower voltage regulator as shown in Figure 6.

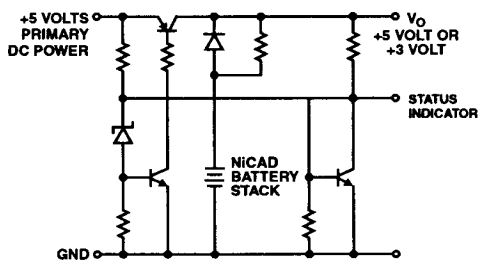


FIGURE 1. DISCRETE BATTERY BACKUP CIRCUIT

Applications for the ICL7673 include volatile semiconductor memory storage systems, real-time clocks, timers, alarm systems, and over/under the voltage detectors. Other systems requiring DC power when the master AC line supply fails can also use the ICL7673.

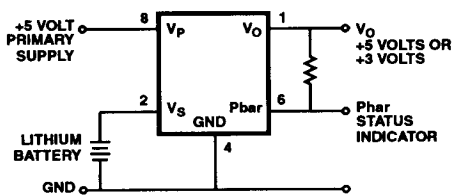


FIGURE 2. ICL7673 BATTERY BACKUP CIRCUIT

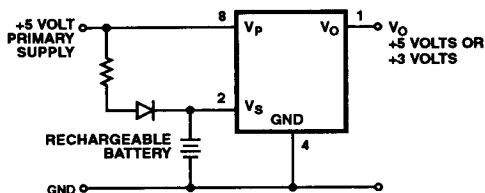


FIGURE 3. APPLICATION REQUIRING RECHARGEABLE BATTERY BACKUP

A typical application, as illustrated in Figure 7, would be a microprocessor system requiring a 5 volt supply. In the event of primary supply failure, the system is powered down, and a 3 volt battery is employed to maintain clock or volatile memory data. The main and backup supplies are connected to V_P and V_S , with the circuit output V_O supplying power to the clock or volatile memory. The ICL7673 will sense the main supply, when energized, to be of greater potential than V_S and connect, via its internal MOS switches, V_P to output V_O . The backup input, V_S will be disconnected internally. In the event of main supply failure, the circuit will sense that the backup supply is now the greater potential, disconnect V_P from V_O , and connect V_S .

Figure 8 illustrates the use of external PNP power transistors to increase the power switching capability of the circuit. In this application the output current is limited by the beta and thermal characteristics of the power transistors.

If hysteresis is desired for a particular low power application, positive feedback can be applied between the input V_P and open drain output S_{bar} through a resistor as illustrated in Figure 9. For high power applications hysteresis can be applied as shown in Figure 10.

The ICL7673 can also be used as a clipping circuit as illustrated in Figure 11. With high impedance loads the circuit output will be nearly equal to the greater of the two input signals.

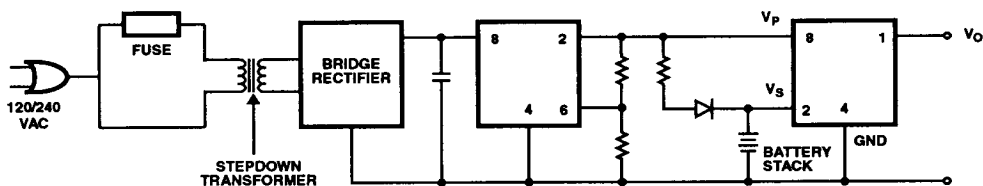


FIGURE 4. POWER SUPPLY FOR LOW POWER PORTABLE AC TO DC SYSTEMS

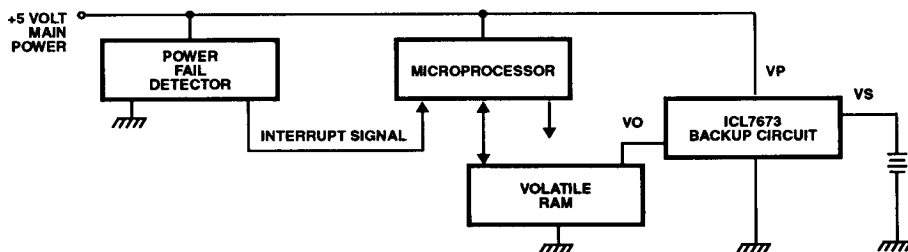


FIGURE 5. TYPICAL MICROPROCESSOR MEMORY APPLICATION

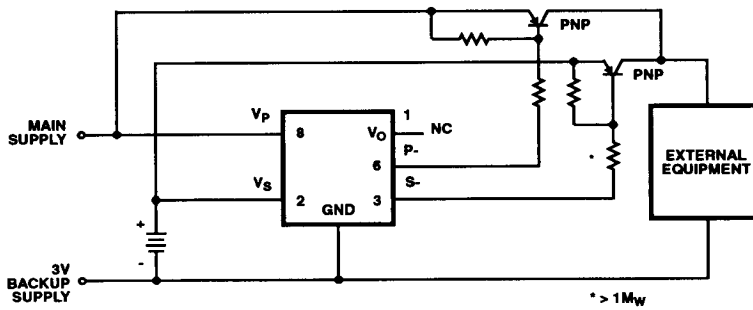


FIGURE 6. HIGH CURRENT BATTERY BACKUP SYSTEM

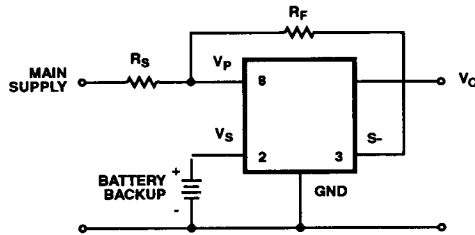


FIGURE 7. LOW CURRENT BATTERY BACKUP SYSTEM WITH HYSTERESIS

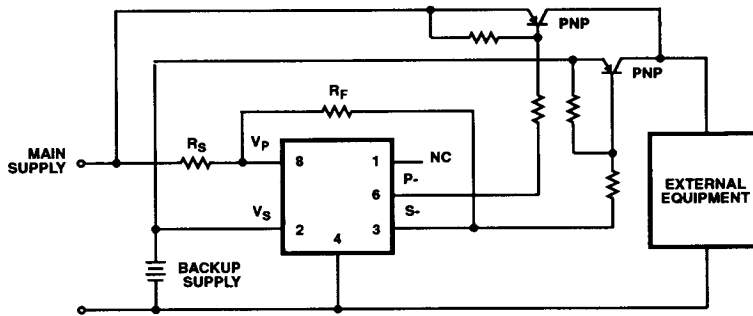


FIGURE 8. HIGH CURRENT BACKUP SYSTEM WITH HYSTERESIS

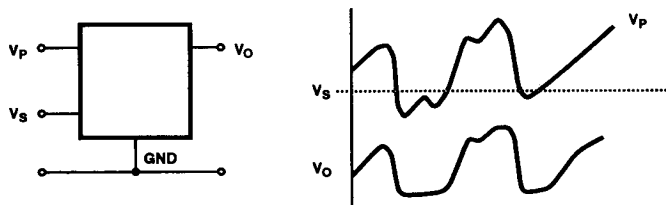


FIGURE 9. CLIPPING CIRCUITS