



IDT71B256

- 32K x 8 BiCMOS Static RAM
- High-speed address /chip select time
 - Military: 20ns
 - Commercial: 12/15/20ns
- One Chip Select plus one Output Enable pin
- Single 5V ($\pm 10\%$) power supply
- Input and output directly TTL-compatible
- Available in 28-pin sidebraze ceramic, 300 mil DIP; 300 mil plastic DIP and 28-pin, 300 mil plastic SOJ packages

The IDT71B256 is a 262,144-bit high-speed static RAM organized as 32Kx8. It is fabricated using IDT's high-performance high-reliability BiCEMOS technology. This state-of-the-art technology, combined with innovative circuit design techniques, provides a cost-effective solution for high-speed memory needs.

The IDT71B256 is packaged in a 28-pin, 300-mil side-braze 28-pin, 300 mil plastic DIP and 28-pin, 300-mil SOJ packages.

Block diagram of the 2962 144-bit memory device. The diagram shows a 16-bit address bus (A0-A15) connected to a 16-to-1 decoder, which then connects to the 144-bit memory array. A 4-bit I/O bus (I/O0-I/O3) is connected to the memory array and the I/O control logic. The I/O control logic is also connected to the memory array. The device is labeled '2962 144-BIT MEMORY'.

2962 drw 01

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MILITARY AND COMMERCIAL TEMPERATURE RANGES

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