

MAXIM

Dual/Quad RF/Video Switches

IH5341/IH5352

General Description

The IH5341 and the IH5352 are dual and quad, single pole single throw (SPST) switches designed specifically for switching RF and video signals. Maxim's IH5341 and IH5352 incorporate an enhanced series-shunt-series structure, providing 70dB of OFF isolation and cross coupling rejection (an additional 10dB compared with other manufacturers' products).

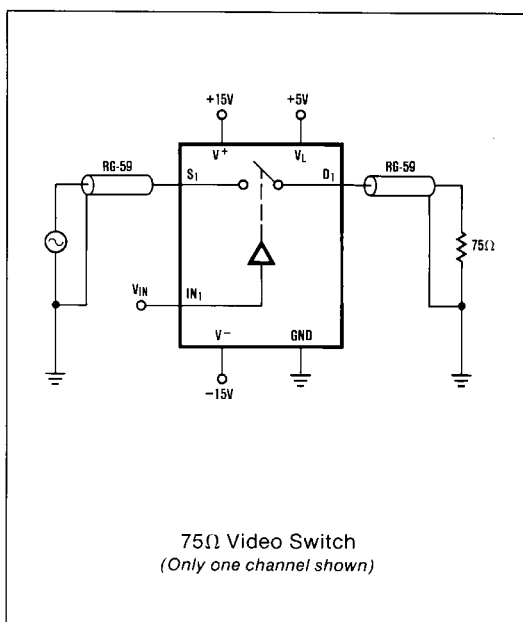
Both devices can be operated with supplies ranging from $\pm 5V$ to $\pm 15V$. The switches typically have a $t_{ON} = 160ns$ and a $t_{OFF} = 70ns$, assuring break-before-make switching. The channel thru resistance of 50 Ω provides excellent matching to video impedances. In the D.C. state, with switches being either on or off, power supply quiescent currents are typically 100nA. This limits the quiescent current drain to 3 μ watts—ideal for portable equipment.

Applications

These devices are used in applications requiring the routing, blocking or switching of video or RF signals such as:

- Commercial TV Cameras
- Video Special Effects
- Low Power RF Switching
- Radar Switching
- Mil and Space Communications

Typical Operating Circuit



Features

- ♦ "OFF" Isolation $\geq 70dB$ @ 10MHz
- ♦ Cross Coupling Isolation $\geq 70dB$ @ 10MHz
- ♦ $rDS(ON) < 75\Omega$, $< 3dB$ Loss from DC to 100MHz
- ♦ $\pm 5V$ to $\pm 15V$ Operating Supply Range
- ♦ Supply Currents $< 1\mu A$
- ♦ Fast, Break-Before-Make Switching (70ns/160ns typ.)
- ♦ Monolithic, Low Power CMOS Design

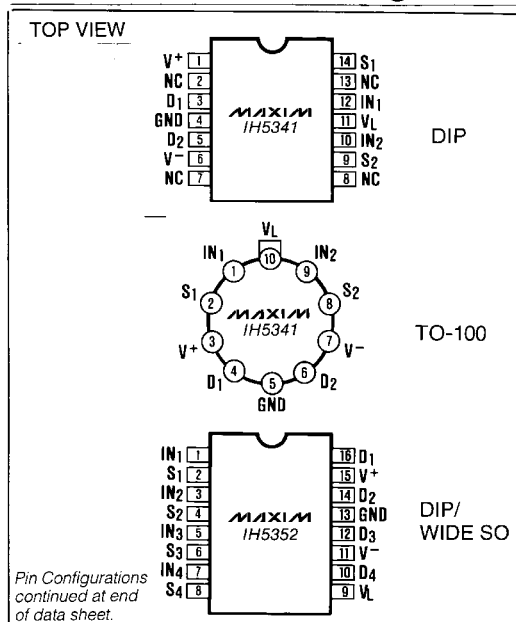
Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
IH5341CPD	0°C to +70°C	14 Plastic DIP
IH5341CWE	0°C to +70°C	16 Wide SO
IH5341C/D	0°C to +70°C	Dice*
IH5341EPD	-40°C to +85°C	14 Plastic DIP
IH5341EWE	-40°C to +85°C	16 Wide SO
IH5341MJD	-55°C to +125°C	14 CERDIP**
IH5341MTW	-55°C to +125°C	10 TO-100
IH5352CPE	0°C to +70°C	16 Plastic DIP
IH5352CWE	0°C to +70°C	16 Wide SO
IH5352C/D	0°C to +70°C	Dice*
IH5352EPE	-40°C to +85°C	16 Plastic DIP
IH5352EWE	-40°C to +85°C	16 Wide SO
IH5352MJE	-55°C to +125°C	16 CERDIP

* Contact factory for dice specifications.

** Contact factory for availability and processing to MIL-STD-883.

Pin Configurations



Dual/Quad RF/Video Switches

ABSOLUTE MAXIMUM RATINGS

Supply Voltages V^+ and V^-	$\pm 17V$
Current in Terminal	50mA
Analog Input Voltage	V^+
Operational Temperature Range	$\pm 30V$
(M Version)	$-55^\circ C$ to $+125^\circ C$
(E Version)	$-40^\circ C$ to $+85^\circ C$
(C Version)	$0^\circ C$ to $+70^\circ C$

Continuous Power Dissipation ($T_A = +70^\circ C$)	
14-Pin Plastic DIP (derate 10.00mW/ $^\circ C$ above $+70^\circ C$)	800mW
16-Pin Plastic DIP (derate 10.53mW/ $^\circ C$ above $+70^\circ C$)	842mW
16-Pin Wide SO (derate 9.52mW/ $^\circ C$ above $+70^\circ C$)	762mW
10-Pin TO-100 (derate 6.67mW/ $^\circ C$ above $+70^\circ C$)	533mW
Storage Temperature Range	$-65^\circ C$ to $+150^\circ C$
Logic Control Voltage	V^+ to V^-
Voltage on VL Pin	V^+ to V^-
Lead Temperature (Soldering, 10sec)	$+300^\circ C$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

($V^+ = +15V$, $V_L = +5V$, $V^- = -15V$, $T_A = 25^\circ C$ unless otherwise specified)

PARAMETER	SYMBOL	CONDITIONS	TYP (Note 1)	M GRADE DEVICE			I/C GRADE DEVICE			UNITS
				$-55^\circ C$	$+25^\circ C$	$+125^\circ C$	$-40/-20/0^\circ C$	$+25^\circ C$	$+85/+70^\circ C$	
Supply Voltage Ranges	V^+	(Note 3)	4.5 > 16 4.5 > V^+ -4 > -16							V
Positive Supply	V_L				5 to 15			5 to 15		
Logic Supply	V^-				5 to V^+ -5 to -15			5 to V^+ -5 to -15		
Negative Supply										
Switch "ON" Resistance (Note 4)	$r_{ds(ON)}$	$V_D = -5V$ to $+5V$ $I_S = 10$ mA, $V_{IN} = 2.4V$ $V_D = -10V$ to $+10V$		75	75	100	75	75	100	Ω
Switch "ON" Resistance	$r_{ds(ON)}$	$V^- = V_L = 5V$, $V_{IN} = 3V$ $V^- = -5V$, $V_D = \pm 3V$		250	250	350	300	300	350	
On Resistance Match		$I_S = 10$ mA, $V_D = \pm 5V$	5							
Switch "OFF" Leakage (Notes 2 and 4)	$I_{D(OFF)}$ or $I_{S(OFF)}$	$V_{S/D} = +5V$ to $-5V$ $V_{IN} = 0.8V$ $V_{S/D} = +14V$ to $-14V$			± 1	50		± 2	100	nA
Switch "ON" Leakage	$I_{D(ON)}$ + $I_{S(ON)}$	$V_D = +5V$ or $-5V$ $V_{IN} = 2.4V$ $V_D = +14V$ to $-14V$			± 1	100		± 2	100	
Input Logic Current	I_{IN}	$V_{IN} > 2.4V$ or < 0	0.001	1	1	10	1	1	10	
Positive Supply Quiescent Current	I^+	$V_{IN} = 0V$ or $+5V$ (Note 5)	0.01	1	1	10	1	1	10	μA
Negative Supply Quiescent Current	I^-	$V_{IN} = 0V$ or $+5V$ (Note 5)	0.01	1	1	10	1	1	10	
Logic Supply Quiescent Current	I_L	$V_{IN} = 0V$ or $+5V$ (Note 5)	0.01	1	1	10	1	1	10	

AC ELECTRICAL CHARACTERISTICS

$V^+ = +15V$, $V_L = +5V$, $V^- = 0V$, $T_A = +25^\circ C$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Switch "ON" Time	t_{ON}	See Figure 1		160	300	ns
Switch "OFF" Time	t_{OFF}	See Figure 1		70	150	
"OFF" Isolation Rejection Ratio	OIRR	See Figure 2 (Note 6)	70	80		dB
Cross Coupling Rejection Ratio	CCRR	Figure 3 IH5341 IH5352	70 66	80 72		
Frequency where $r_{DS(ON)} = 0.7 \times DC$		(Note 6)	100			MHz

Note 1: Typical values are not tested in production. They are given as a design aid only.

Note 2: Positive and negative voltages applied to opposite sides of switch, in both directions successively.

Note 3: These are the operating voltages at which the other parameters are tested, and are not directly tested.

Note 4: The logic inputs are either greater than or equal to 2.4V or less than or equal to 0.8V, as required, for this test.

Note 5: Maximum values shown are for the dual (IH5341). They are doubled for the quad (IH5352).

Note 6: All AC parameters are sample tested only. Test circuits should be built on copper clad ground plane board, with correctly terminated coax leads, etc.

Dual/Quad RF/Video Switches

Test Circuits

IH5341/IH5352

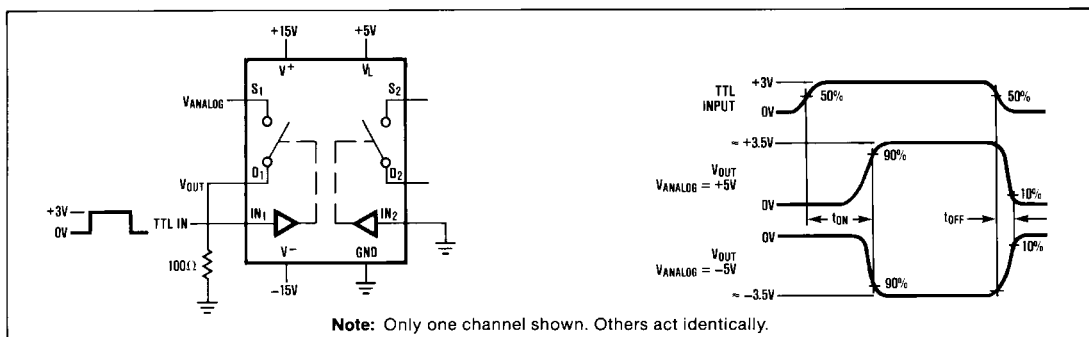


Figure 1. Switching Time Test Circuit and Waveforms

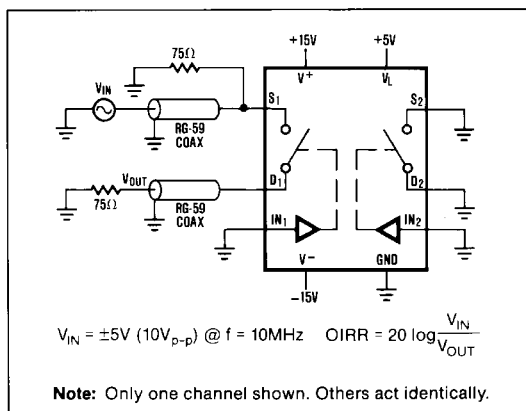


Figure 2. OFF Isolation Test Circuit

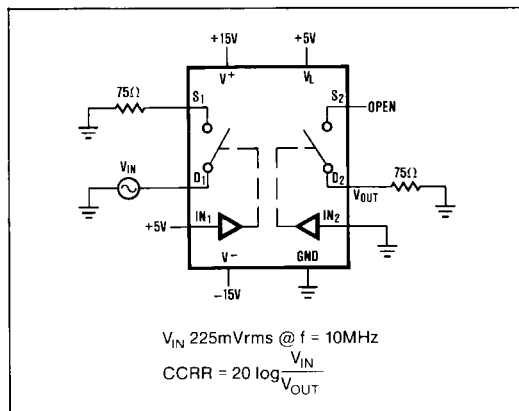
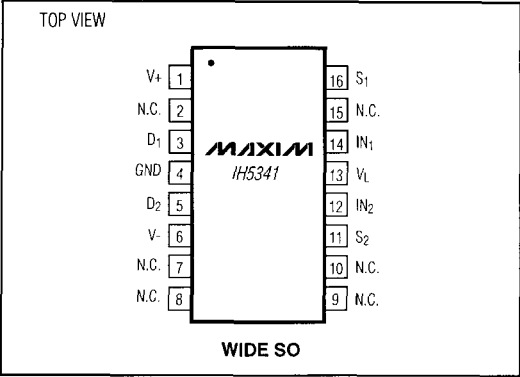


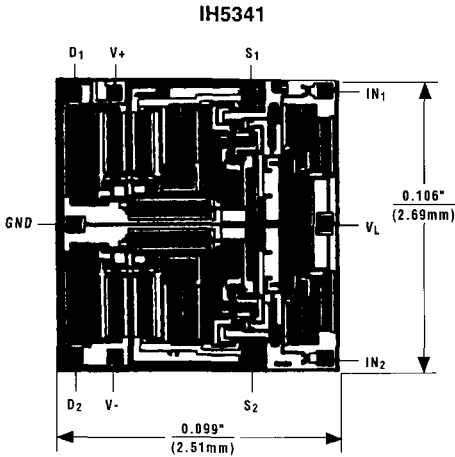
Figure 3. Cross-Coupling Rejection Test Circuit

Dual/Quad RF/Video Switches

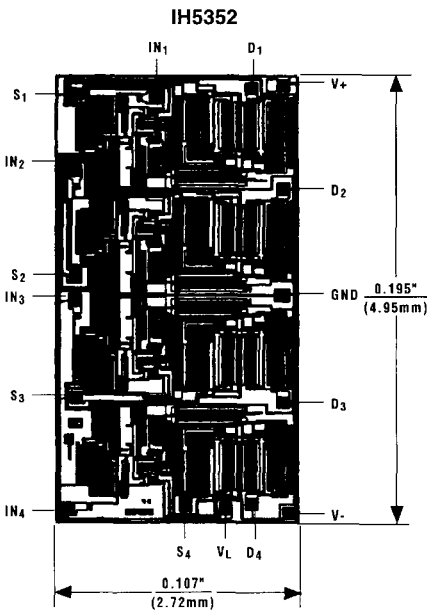
Pin Configurations (continued)



Chip Topographies



TRANSISTOR COUNT: 72
SUBSTRATED CONNECTED TO V+



TRANSISTOR COUNT: 144
SUBSTRATED CONNECTED TO V+

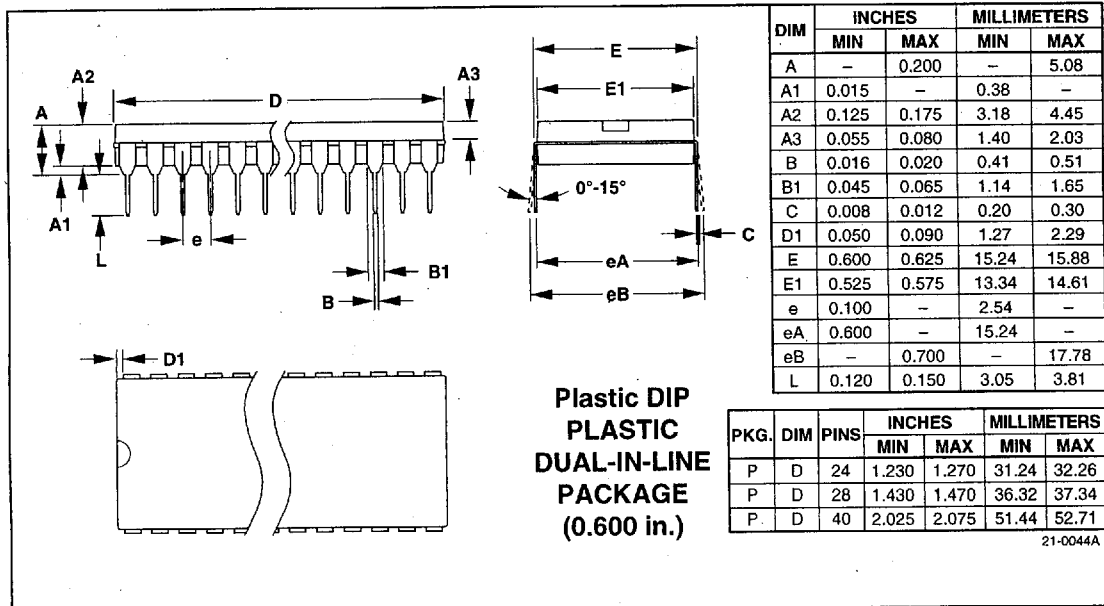
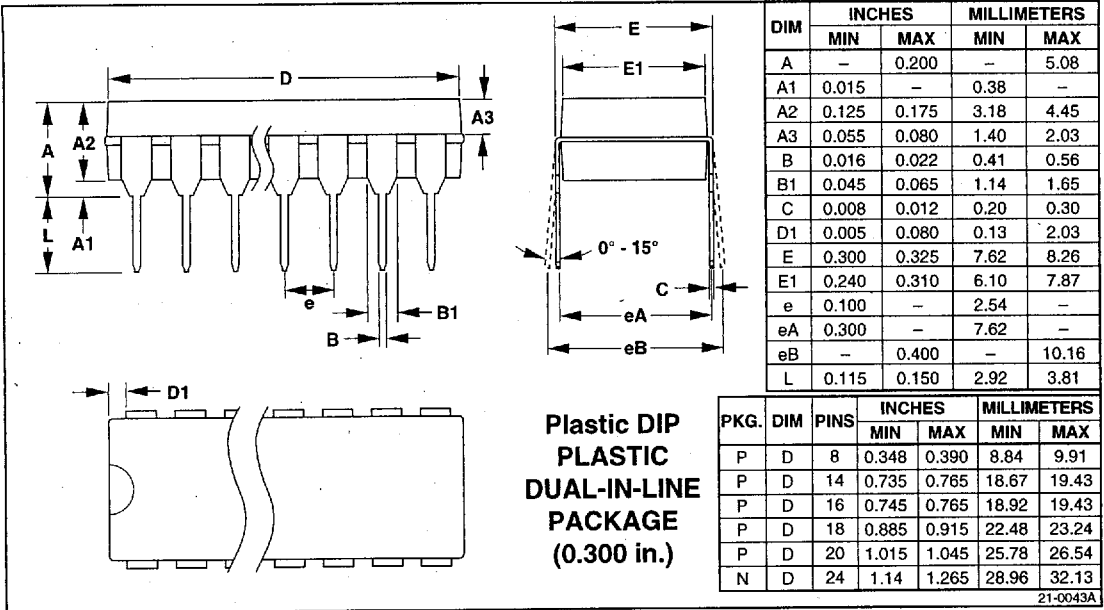
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4 **Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 (408) 737-7600**

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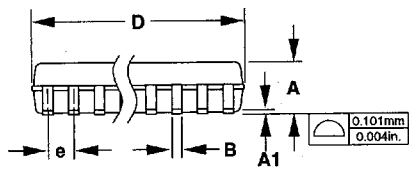
Package Information

This section contains physical dimensions for all packages currently supplied by Maxim.

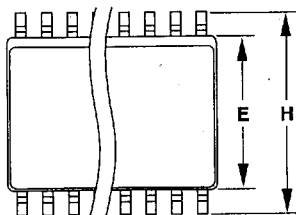


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Package Information



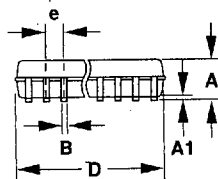
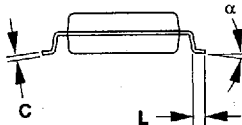
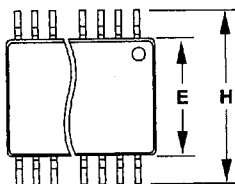
DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.093	0.104	2.35	2.65
A1	0.004	0.012	0.10	0.30
B	0.014	0.019	0.35	0.49
C	0.009	0.013	0.23	0.32
E	0.291	0.299	7.40	7.60
e	0.050		1.27	
H	0.394	0.419	10.00	10.65
L	0.016	0.050	0.40	1.27



**Wide SO
SMALL-OUTLINE
PACKAGE
(0.300 in.)**

DIM	PINS	INCHES		MILLIMETERS	
		MIN	MAX	MIN	MAX
D	16	0.398	0.413	10.10	10.50
D	18	0.447	0.463	11.35	11.75
D	20	0.496	0.512	12.60	13.00
D	24	0.598	0.614	15.20	15.60
D	28	0.697	0.713	17.70	18.10

21-0042A



**SSOP
SHRINK
SMALL-OUTLINE
PACKAGE**

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.068	0.078	1.73	1.99
A1	0.002	0.008	0.05	0.21
B	0.010	0.015	0.25	0.38
C	0.004	0.008	0.09	0.20
D	SEE VARIATIONS			
E	0.205	0.209	5.20	5.38
e	0.0256 BSC		0.65 BSC	
H	0.301	0.311	7.65	7.90
L	0.025	0.037	0.63	0.95
α	0°	8°	0°	8°

DIM	PINS	INCHES		MILLIMETERS	
		MIN	MAX	MIN	MAX
D	14	0.239	0.249	6.07	6.33
D	16	0.239	0.249	6.07	6.33
D	20	0.278	0.289	7.07	7.33
D	24	0.317	0.328	8.07	8.33
D	28	0.397	0.407	10.07	10.33

21-0056A

