

#### POWER MANAGEMENT

# Low Power, 5V/3.0V µP Reset

## - Active LOW, Open-Drain Output

#### - 350ms Reset Period

The IMP1233M supply voltage monitor is an improved, low-power replacement for the Dallas Semiconductor DS1233M. Maximum supply current over temperature is a low  $20\mu\text{A}$ , representing 60 percent lower power as compared to the DS1233M.

The IMP1233M issues an active LOW reset signal whenever the monitored supply is out-of-tolerance. A precision reference and comparator circuit monitor power supply ( $V_{\rm CC}$ ) level. Tolerance level options are 5- and 10-percent for a 5V power supply. The tolerance is 15-percent for the 3.3V, IMP1233M. When an out-of-tolerance condition is detected, an internal power-fail signal is generated which forces an active LOW reset signal. After  $V_{\rm CC}$  returns to an in-tolerance condition, the reset signal remains active for 350ms to allow the power supply and system microprocessor to stabilize.

The IMP1233M is designed with a open-drain output stage and operates over the extended industrial temperature range. Devices are available in the compact surface mount SO-8 package and a 3-lead TO-92 package.

Other low power products in this family include the IMP1810/11/12/15/ 16/17 and IMP1233D.

## Family Selection Guide

Part	RESET Voltage (V)	RESET Time (ms)	Output Stage	RESET Polarity
IMP1810	4.620, 4.370, 4.120	150	Push-Pull	LOW
IMP1811	4.620, 4.350, 4.130	150	Open-Drain	LOW
IMP1812	4.620, 4.350, 4.130	150	Push-Pull	HIGH
IMP1815	3.060, 2.880, 2.550	150	Push-Pull	LOW
IMP1816	3.060, 2.880, 2.550	150	Open-Drain	LOW
IMP1817	3.060, 2.880, 2.550	150	Push-Pull	HIGH
IMP1233D	4.625, 4.375, 4.125	350	Open-Drain	LOW
IMP1233M	4.625, 4.375, 2.720	350	Open-Drain	LOW

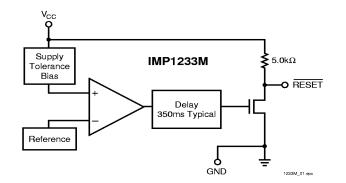
### Key Feelures

- Improved Dallas DS1233M replacement
   60% lower maximum supply current
- ◆ Low Supply Current
  - 20µA maximum (5.5V)
  - 15µA maximum (3.6V)
- ◆ Automatically restarts a microprocessor after power failure
- ◆ 350ms reset delay after V<sub>CC</sub> returns to an in-tolerance condition
- ♦ Active LOW power-up reset, 5kΩ internal pull-up
- Precision temperature-compensated voltage reference and comparator
- Eliminates external components
- Pin function compatible with the Motorola MC33064, MC34064, MC33164 and MC34164
- Motorola 68xxx and HC16 compatible
- ◆ Low-cost TO-92 package
- ◆ Compact surface mount SO-8 package
- ◆ Operating temperature -40°C to +85°C

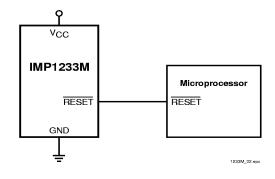
## Applications

- ◆ Set-top boxes
- ◆ Cellular phones
- PDAs
- Energy management systems
- ◆ Embedded control systems
- Printers
- Single board computers

## Block Diagram



# Typical Application



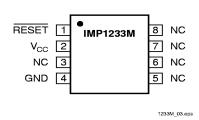
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# Pin Configuration

#### **SO-8**



# TO-92\* IMP1233M-X 1 2 3 1 2 3

# Pin Descriptions

Pin Numl	oers	Name	Function	
SO-8	TO-92	Name	runction	
1	1	RESET	Active LOW reset output	
2	2	$V_{CC}$	Power supply input	
3, 5, 6, 7 and 8	_	NC	No connection	
4	3	GND	Ground	

# **Ordering Information**

Device Summary								
Part Number	RESET Output Voltage (V)	RESET Tolerance (%)	RESET Time (ms)	Output Stage		TO-92*	SO-8	RESET
				Open-Drain**	Push-Pull	Package	Package	Polarity
IMP1233M-55	4.625	5	350	X		Х		LOW
IMP1233M-5	4.375	10	350	X		Х		LOW
IMP1233M-3	2.720	15	350	X		Х		LOW
IMP1233MS-55	4.625	5	350	X			×	LOW
IMP1233MS-5	4.375	10	350	X			×	LOW
IMP1233MS-3	2.720	15	350	X			Х	LOW

<sup>\*</sup> Add /S to Part Number for straight (unformed) leads. (ie. IMP18xx-x/S)

1233M03

<sup>\*\*</sup> Internal  $5k\Omega$  resistor pull up.



## **Absolute Maximum Ratings**

Voltages measured with respect to ground.

These are stress ratings only and functional operation is not implied.

## **Electrical Characteristics**

Unless otherwise noted,  $V_{CC}$  = 1.2V to 5.5V and specifications are over the operating temperature range of  $-40^{\circ}$ C to +85°C. All voltages are referenced to ground.

Parameter	Symbol	Conditions	Min	Тур	Max	Units
Supply Voltage	V <sub>CC</sub>		1.2		5.5	V
Output Voltage	V <sub>OH</sub>	I <sub>OUT</sub> < 500μA	V <sub>CC</sub> - 0.5V	V <sub>CC</sub> - 0.1V		V
Output Current	I <sub>OL</sub>	Output = 0.4V, $V_{CC} \ge 2.7V$	+8			mA
Operating Current	Icc	$V_{CC}$ < 5.5V, $\overline{RESET}$ output open		8	20	μΑ
Operating Current	I <sub>CC</sub>	$V_{CC} \le 3.6V$ , $\overline{RESET}$ output open		6	15	μА
V <sub>CC</sub> Trip Point (IMP1233M-5)	V <sub>CCTP</sub>		4.25	4.375	4.49	V
V <sub>CC</sub> Trip Point (IMP1233M-55)	V <sub>CCTP</sub>		4.5	4.625	4.75	V
V <sub>CC</sub> Trip Point (IMP1233M-3)	V <sub>CCTP</sub>		2.64	2.72	2.8	V
Voltage High Trip Level IMP1233M-5 IMP1233M-55	V <sub>HTL</sub>				4.75	V
Voltage Low Trip Level IMP1233M-5 IMP1233M-55	V <sub>LTL</sub>				4.00	V
Voltage High Trip Level IMP1233M-3	V <sub>HTL</sub>				3.14	V
Voltage Low Trip Level IMP1233M-3	V <sub>LTL</sub>				2.48	V
Internal Pull-Up Resistor	R <sub>P</sub>		3.5	5.0	7.5	kΩ
Output Capacitance	C <sub>OUT</sub>				10	pF
V <sub>CC</sub> Detect to RESET Low	t <sub>RPD</sub>			2	10	μs
V <sub>CC</sub> Slew Rate	t <sub>F</sub>		300			μs
(V <sub>HTL</sub> - V <sub>LTL</sub> )						
V <sub>CC</sub> Slew Rate	t <sub>R</sub>		0			ns
(V <sub>LTL</sub> - V <sub>HTL</sub> )						
V <sub>CC</sub> Detect to RESET High	t <sub>RPU</sub>	$t_R = 5\mu s$	200	350	500	ms

Notes: 1.  $A 1k\Omega$  external resistor maybe required in some applications for proper operation of the microprocessor reset control circuit.



## **Application Information**

#### Operation - Power Monitor

The IMP1233M detects out-of-tolerance power supply conditions. It resets a processor during power-up and issues a reset to the system processor when the monitored power supply voltage is below the reset threshold (power-down). When an out-of-tolerance  $V_{CC}$  voltage is detected, the  $\overline{RESET}$  signal is asserted. On power-up,  $\overline{RESET}$  is kept active (LOW) for approximately 350ms after the power supply voltage has reached the selected tolerance. This allows the power supply and microprocessor to stabilize before  $\overline{RESET}$  is released.

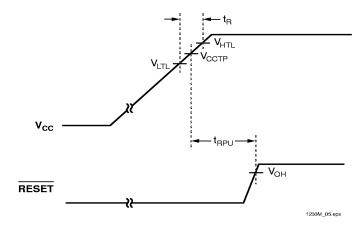


Figure 1. Timing Diagram: Power-Up

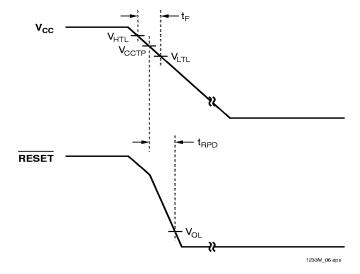
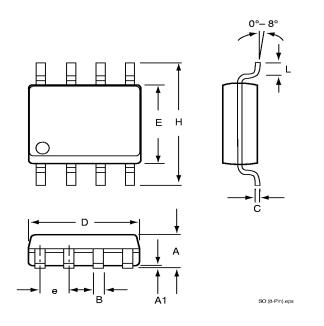


Figure 2. Timing Diagram: Power-Down

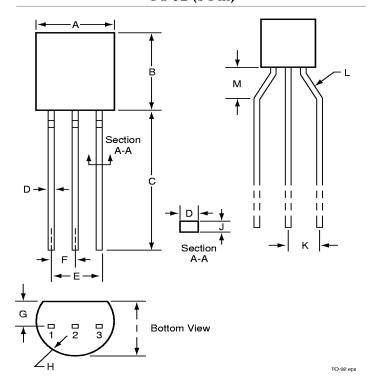


# **Absolute Maximum Ratings**

#### Plastic SO-8 (8-Pin)



TO-92 (3-Pin)



	Inche	s	Millimeters				
	Min	Max	Min	Max			
Plastic SO-8 (8-Pin)							
Α	0.053	0.069	1.35	1.75			
A1	0.004	0.010	0.10	0.25			
В	0.013	0.020	0.33	0.51			
С	0.007	0.010	0.19	0.25			
е	0.0	50	1.27				
Е	0.150	0.157	3.80	4.00			
Н	0.228	0.244	5.80	6.20			
L	0.016	0.050	0.40	1.27			
۵	0.189	0.197	4.80	2.00			
TO-92* (3-Pin)							
Α	0.175	0.195	4.45	4.95			
В	0.170	0.192	4.32	4.96			
С	0.500	0.610	12.70	15.49			
D	0.016	0.022	0.406	0.559			
Е	0.095	0.105	2.41	2.67			
F	0.045	0.60	1.14	1.52			
G	0.45	0.060	1.14	1.52			
Н	0.085	0.095	2.16	2.41			
ı	0.130	0.155	3.30	3.94			
J	0.014	0.020	0.35	0.51			
K	0.093	0.115	2.36	2.92			
L	45°	60°	45°	60°			
М	0.118	Typical	3.0	00			

<sup>\*</sup> Formed leads are standard. Add /S to Part Number for straight leads.