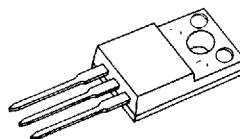


IRFS740/741/742/743**N-CHANNEL
POWER MOSFETS****FEATURES**

- Lower $R_{DS(on)}$
- Improved inductive ruggedness
- Fast switching times
- Rugged polysilicon gate cell structure
- Lower input capacitance
- Extended safe operating area
- Improved high temperature reliability

TO-220F



IRFS740/741/742/743

PRODUCT SUMMARY

Part Number	V_{DS}	$R_{DS(on)}$	I_D
IRFS740	400V	0.55Ω	5.5A
IRFS741	350V	0.55Ω	5.5A
IRFS742	400V	0.80Ω	4.5A
IRFS743	350V	0.80Ω	4.5A

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ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	IRFS740	IRFS741	IRFS742	IRFS743	Unit
Drain-Source Voltage (1)	V_{DSS}	400	350	400	350	Vdc
Drain-Gate Voltage ($R_{GS}=1.0M\Omega$)(1)	V_{DGR}	400	350	400	350	Vdc
Gate-Source Voltage	V_{GS}	± 20				Vdc
Continuous Drain Current $T_C=25^\circ C$	I_D	5.5	5.5	4.5	4.5	Adc
Continuous Drain Current $T_C=100^\circ C$	I_D	3	3	2.5	2.5	Adc
Drain Current—Pulsed (3)	I_{DM}	40	40	33	33	Adc
Gate Current—Pulsed	I_{GM}	± 1.5				Adc
Single Pulsed Avalanche Energy (4)	E_{AS}	157				mJ
Avalanche Current	I_{AS}	5.5				A
Total Power Dissipation at $T_C=25^\circ C$ Derate above $25^\circ C$	P_D	40 0.32				Watts W/ $^\circ C$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-55 to 150				$^\circ C$
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T_L	300				$^\circ C$

Notes: (1) $T_J=25^\circ C$ to $150^\circ C$ (2) Pulse test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

(3) Repetitive rating: Pulse with limited by max. junction temperature

(4) $L=9.1mH$, $V_{dd}=50V$, $R_G=25\Omega$, Starting $T_J=25^\circ C$

IRFS740/741/742/743**N-CHANNEL
POWER MOSFETS****ELECTRICAL CHARACTERISTICS** ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
BV_{DSS}	Drain-Source Breakdown Voltage IRFS740/742 IRFS741/743	400 350	— —	— —	V	$V_{GS}=0V$, $I_D=250\mu A$
$V_{GS(th)}$	Gate Threshold Voltage	2.0	—	4.0	V	$V_{DS}=V_{GS}$, $I_D=250\mu A$
I_{GSS}	Gate-Source Leakage Forward	—	—	100	nA	$V_{GS}=20V$
I_{GSS}	Gate-Source Leakage Reverse	—	—	-100	nA	$V_{GS}=-20V$
I_{DSS}	Zero Gate Voltage Drain Current	— —	— —	250 1000	μA	$V_{DS}=\text{Max. Rating}$, $V_{GS}=0V$ $V_{DS}=0.8\text{Max. Rating}$, $T_C=125^\circ\text{C}$
$I_{D(on)}$	On-State Drain-Source Current (2) IRFS740/741 IRFS742/743	10 8.3	— —	— —	A	$V_{DS}\geq 8.8V$, $V_{GS}=10V$
$R_{DS(on)}$	Static Drain-Source On-State Resistance (2) IRFS740/741 IRFS742/743	— —	— —	0.55 0.80	Ω	$V_{GS}=10V$, $I_D=5.2A$
g_{fs}	Forward Transconductance (2)	5.8	87	—	μ	$V_{DS}\geq 50V$, $I_D=5.2A$
C_{iss}	Input Capacitance	—	1500	—	pF	$V_{GS}=0V$
C_{oss}	Output Capacitance	—	178	—	pF	$V_{DS}=25V$
C_{rss}	Reverse Transfer Capacitance	—	75	—	pF	$f=1.0\text{MHz}$
$t_{d(on)}$	Turn-On Delay Time	—	14	21	ns	$V_{DD}=0.5 BV_{DSS}$, $I_D=10A$, $Z_O=9.1\Omega$ (MOSFET switching times are essentially independent of operating temperature)
t_r	Rise Time	—	27	41	ns	
$t_{d(off)}$	Turn-Off Delay Time	—	50	75	ns	
t_f	Fall Time	—	24	36	ns	
Q_g	Total Gate Charge (Gate-Source Pulse Gate-Drain)	—	42	63	nC	$V_{GS}=10V$, $I_D=10A$, $V_{DS}=0.8\text{Max. Rating}$ (Gate charge is essentially independent of operating temperature.)
Q_{gs}	Gate-Source Charge	—	6.0	9.0	nC	
Q_{gd}	Gate-Drain ("Miller") Charge	—	21	32	nC	

THERMAL RESISTANCE

R_{thJC}	Junction-to-Case	Max	3.12	K/W	Mounting surface flat Free Air Operation
R_{thCS}	Case-to-Sink	Typ	0.5	K/W	
R_{thJA}	Junction-to-Ambient	Max	80	K/W	

Notes: (1) $T_J=25^\circ\text{C}$ to 150°C

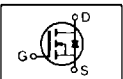
(2) Pulse test: Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

(3) Repetitive rating: Pulse width limited by max. junction temperature

IRFS740/741/742/743

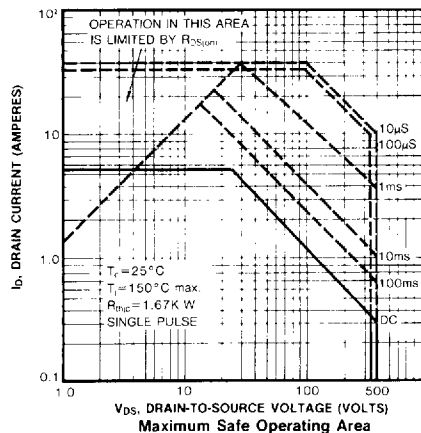
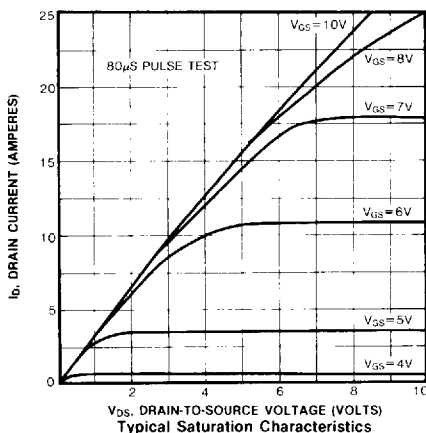
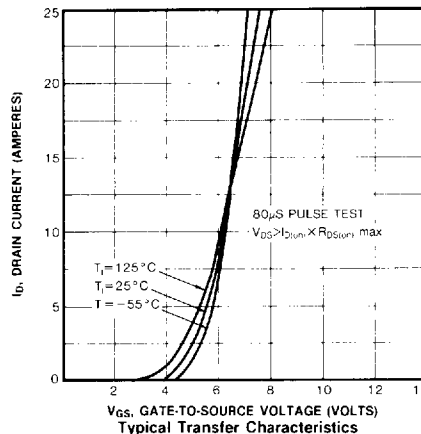
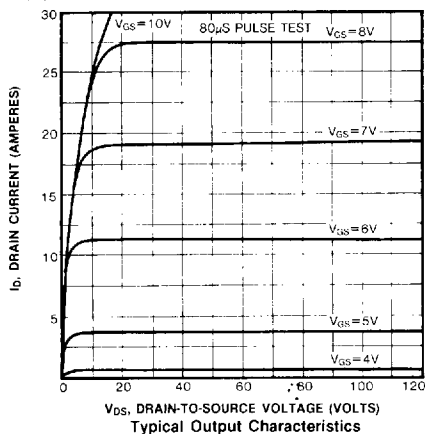
N-CHANNEL
POWER MOSFETS

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

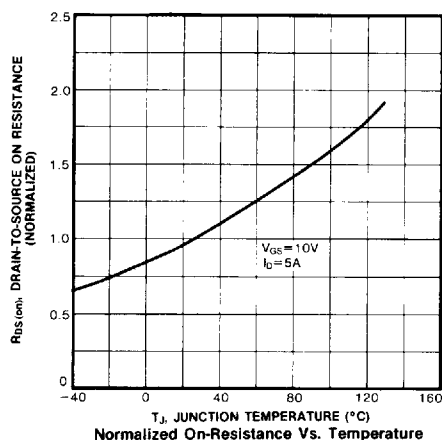
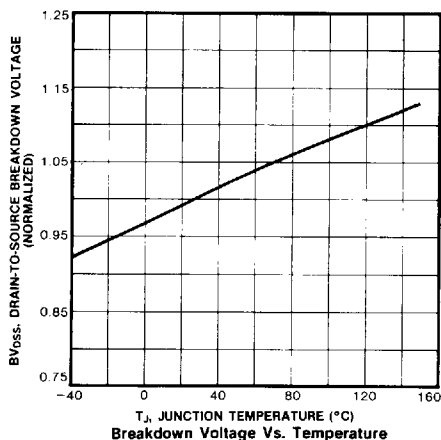
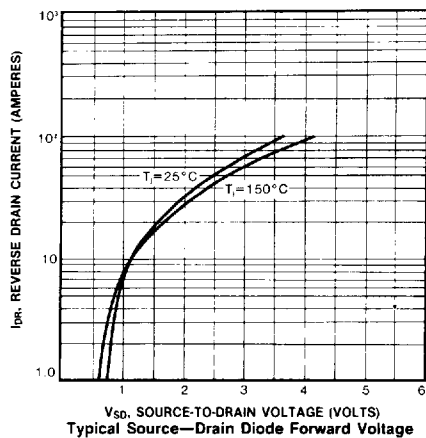
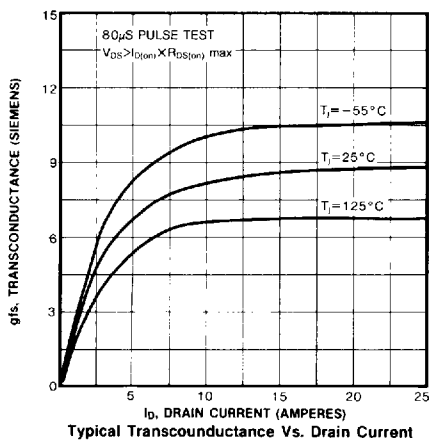
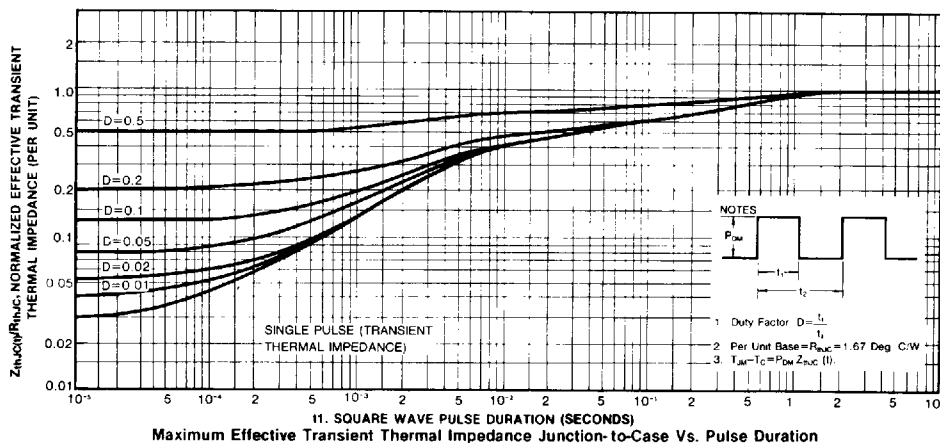
Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode) IRFS740/741 IRFS742/743	—	—	10 8	A	Modified MOSFET integral reverse P-N junction rectifier 
I_{SM}	Pulse Source Current (3) IRFS740/741 IRFS742/743	—	—	40 32	A	
V_{SD}	Diode Forward Voltage (2) IRFS740/741 IRFS742/743	—	—	2.0 1.9	V	$T_C = 25^\circ\text{C}$, $I_S = 10\text{A}$, $V_{GS} = 0\text{V}$ $T_C = 25^\circ\text{C}$, $I_S = 8\text{A}$, $V_{GS} = 0\text{V}$
t_{rr}	Reverse Recovery Time	—	370	—	ns	$T_J = 25^\circ\text{C}$, $I_F = 10\text{A}$, $dI_F/dt = 100\text{A}/\mu\text{s}$

Notes: (1) $T_J = 25^\circ\text{C}$ to 150°C (2) Pulse test: Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

(3) Repetitive rating: Pulse with limited by max. junction temperature



IRFS740/741/742/743

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