The documentation and process conversion measures necessary to comply with this revision shall be completed by 05 November 1999.

INCH-POUND

MIL-PRF-19500/370D 05 August 1999 SUPERSEDING MIL-S-19500/370C 25 September 1994

PERFORMANCE SPECIFICATION SHEET

SEMICONDUCTOR DEVICE, TRANSISTOR, NPN, SILICON, HIGH-POWER TYPE 2N3442, JAN, JANTX AND JANTXV

This specification is approved for use by all Departments and Agencies of the Department of Defense.

1. SCOPE

- 1.1 <u>Scope</u>. This specification covers the performance requirements for NPN silicon, high-power transistor. Three levels of product assurance are provided for each device type as specified in MIL-PRF-19500.
 - 1.2 Physical dimensions. See figure 1 (similar to TO-3), (see 3.3).
 - 1.3 Maximum ratings.

P _T <u>1</u> /	P _T <u>2</u> /	V _{СВО}	VCEO	V _{EBO}	VCER	ΙΒ	IC	TOP and TSTG
T _C = +25°C	$T_{C} = +25^{\circ}C$							
<u>W</u>	<u>W</u>	V dc	V dc	V dc	V dc	A dc	A dc	<u>°C</u>
6.0	117	160	140	7	150	7	10	-65 to +200

- $\underline{1}$ / Derate linearly 34.2 mW/°C above T_A = +25°C.
- $\underline{2}$ / Derate linearly 668 mW/°C above T_C = +25°C.
- 1.4 Primary electrical characteristics.

	h _{FE1} 1/	VCE(sat) 1/	h _{fe}	R _{ÐJC}
	$V_{CE} = 4 \text{ V dc}$ $I_{C} = 3 \text{ A dc}$	$I_{B} = 300 \text{ mA dc}$	$V_{CE} = 4 \text{ V dc}$ $I_{C} = 3 \text{ A dc}$ $f = 100 \text{ kHz}$	
Min Max	20 70	<u>V dc</u> 1.0	1.0	<u>°C/W</u> 1.5

1/ Pulsed (see 4.5.1).

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Commander, Defense Supply Center Columbus, ATTN: DSCC-VAC, 3990 East Broad St., Columbus, OH 43216-5000, by using the addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

AMSC N/A FSC 5961

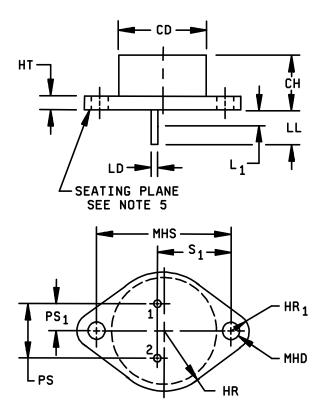


FIGURE 1. Physical dimensions (similar to TO-3).

Ltr		Dimer	nsions		Notes	
	Incl	hes	Millim	Millimeters		
	Min	Max	Min	Max		
CD		.875		22.22		
CH	.270	.350	6.86	8.89		
HR	.495	.525	12.57	13.34		
HR ₁	.131	.188	3.33	4.78		
HT	.060	.135	1.52	3.43		
LD	.038	.043	0.97	1.09		
LL	.312	.500	7.92	12.70		
L ₁		.050		1.27		
MHD	.151	.165	3.84	4.19		
MHS	1.177	1.197	29.90	30.40		
PS	.420	.440	10.67	11.18	4,5	
PS ₁	.205	.225	5.21	5.72	4,5	
S 1	.655	.675	16.64	17.15		

NOTES:

- 1. Dimensions are in inches.
- 2. Metric equivalents are given for general information only.
- 3. Lead 1 is base; lead 2 is emitter, and case is collector.
- 4. Measured at points .050 inch (1.27 mm) .055 inch (1.4 mm) below the seating plane. When gauge is not used, measurement will be made at the seating plane.
- 5. The seating plane of the header shall be flat within .001 inch (0.03 mm) concave to .004 inch (0.10 mm) convex inside a .930 inch (23.62 mm) diameter circle on the center of the header and flat within .001 inch (0.03 mm) concave to .006 inch (0.15 mm) convex overall.
- 6. In accordance with ANSI Y14.5M, diameters are equivalent to ϕx symbology.

FIGURE 1. Physical dimensions (similar to TO-3) - Continued.

2. APPLICABLE DOCUMENTS

2.1 <u>General</u>. The documents listed in this section are specified in sections 3 and 4 of this specification. This section does not include documents cited in other sections of this specification or recommended for additional information or as examples. While every effort has been made to ensure the completeness of this list, document users are cautioned that they must meet all specified requirements documents cited in sections 3 and 4 of this specification, whether or not they are listed.

2.2 Government documents.

2.2.1 <u>Specifications, standards, and handbooks</u>. The following specifications, standards, and handbooks form a part of this document to the extent specified herein. Unless otherwise specified, the issues of these documents are those listed in the issue of the Department of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation (see 6.2).

SPECIFICATION

DEPARTMENT OF DEFENSE

MIL-PRF-19500 - Semiconductor Devices, General Specification for.

STANDARD

DEPARTMENT OF DEFENSE

MIL-STD-750 - Test Methods for Semiconductor Devices.

(Unless otherwise indicated, copies of the above specifications, standards, and handbooks are available from the Defense Automated Printing Service, 700 Robbins Avenue, Building 4D (DPM – DODSSP), Philadelphia, PA 19111-5094.)

2.3 <u>Order of precedence</u>. In the event of a conflict between the text of this document and the references cited herein (except for related associated specifications or specification sheets), the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

- 3.1 Associated specification. The individual item requirements shall be in accordance with MIL-PRF-19500 and as specified herein.
- 3.2 <u>Abbreviations, symbols, and definitions</u>. Abbreviations, symbols, and definitions used herein shall be as specified in MIL-PRF-19500.
- 3.3 <u>Interface requirements and physical dimensions</u>. The Interface requirements and physical dimensions shall be as specified in MIL-PRF-19500 and figure 1.
- 3.3.1 <u>Lead finish</u>. Lead finish shall be solderable in accordance with MIL-PRF-19500, MIL-STD-750, and herein. Where a choice of lead finish is desired, it shall be specified in the acquisition document (see 6.2).
 - 3.4 Marking. Marking shall be in accordance with MIL-PRF-19500.
- 3.5 <u>Electrical performance characteristics</u>. Unless otherwise specified herein, the electrical performance characteristics are as specified in 1.3, 1.4, and table I.

- 3.6 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table I herein.
- 3.7 Qualification. Devices furnished under this specification shall be products that are authorized by the qualifying activity for listing on the applicable qualified manufacturers list before contract award (see 4.2 and 6.4).
 - 4. VERIFICATION
 - 4.1 Classification of Inspections. The inspection requirements specified herein are classified as follows:
 - a. Qualification inspection (see 4.2).
 - b. Screening (see 4.3)
 - c. Conformance inspection (see 4.4).
 - 4.2 Qualification inspection. Qualification inspection shall be in accordance with MIL-PRF-19500.
- 4.3 <u>Screening (JANTX and JANTXV levels)</u>. Screening shall be in accordance with table IV of MIL-PRF-19500, and as specified herein. The following measurements shall be made in accordance with table I herein. Devices that exceed the limits of table I herein shall not be acceptable.

Screen (see table IV of MIL-PRF-19500)	Measurement JANTX, JANTXV levels only
11	hFE1, ICEX
12	See 4.3.1
13	Subgroup 2 of table I herein. $\Delta I_{CEX} = 100$ percent or 500 μA dc whichever is greater; $\Delta h_{FE1} = 25$ percent of initial value.

4.3.1 Power burn-in conditions. Power burn-in conditions are as follows:

$$T_{J} = +187.5$$
°C ± 12.5 °C; $V_{CB} = 24$ V dc; $T_{A} \le +35$ °C.

- 4.4 Conformance inspection. Conformance inspection shall be in accordance with MIL-PRF-19500.
- 4.4.1 <u>Group A inspection</u>. Group A inspection shall be conducted in accordance with MIL-PRF-19500, and table I herein. Electrical measurements (end-points) shall be in accordance with the applicable inspections of table I, group A, subgroup 2 herein.

- 4.4.2 <u>Group B inspection</u>. Group B inspection shall be conducted in accordance with the conditions specified for subgroup testing in table VIb (JAN, JANTX, and JANTXV) of MIL-PRF-19500 and paragraph 4.4.2.1 herein. Electrical measurements (end-points) shall be in accordance with the applicable inspections of table I, group A, subgroup 2 herein. Delta measurements shall be in accordance with table II herein.
 - 4.4.2.1 Group B inspection, table VIb (JAN, JANTX and JANTXV) of MIL-PRF-19500.

Subgroup	<u>Method</u>	Conditions
В3	1037	For solder die attach: 2,000 cycles; $V_{CB} \ge 10 \text{ V dc}, T_A \le 35^{\circ}C.$
В3	1026	For eutectic die attach: VCB ≥ 10 V dc, TA $\leq 35^{\circ}C,$ adjust PT to achieve TJ = 175°C minimum.
B5	3131	See 4.5.2.

4.4.3 <u>Group C inspection</u>. Group C inspection shall be conducted in accordance with the conditions specified for subgroup testing in table VII of MIL-PRF-19500, and as follows. Electrical measurements (end-points) shall be in accordance with table I, group A, subgroup 2 herein. Delta measurements shall be in accordance with table II herein.

4.4.3.1. Group C inspection, table VII of MIL-PRF-19500.

Subgroup	Method	Conditions
C2	2036	Test condition A, weight = 10 pounds ± 10 oz., $t = 15$ s.
C6	1037	For solder die attach: 6,000 cycles; VCB \geq 10 V dc, TA \leq 35°C.
C6	1026	For eutectic die attach: $V_{CB} \ge 10 \text{ V}$ dc, $T_A \le 35^{\circ}\text{C}$, adjust P_T to achieve $T_{LI} = 175^{\circ}\text{C}$ minimum.

- 4.5 Method of inspection. Methods of inspection shall be as specified in the appropriate tables and as follows.
- 4.5.1 Pulse measurements. Conditions for pulse measurement shall be as specified in section 4 of MIL-STD-750.
- 4.5.2 <u>Thermal resistance</u>. Thermal resistance measurements shall be conducted in accordance with test method 3131 of MIL-STD-750. The following details shall apply.
 - a. Collector current magnitude during power applications shall be 2 A dc.
 - b. Collector to emitter voltage magnitude shall be 20 V dc.
 - c. Reference temperature measuring point shall be the case.
 - Reference point temperature shall be +25°C ≤ T_R ≤ +75°C and recorded before the test is started.

TABLE I. Group A inspection. 1/

Inspection		MIL-STD-750	Symbol	Lin	nits	Unit
	Method	Conditions		Min	Max	
Subgroup 1						
Visual and mechanical examination	2071					
Subgroup 2						
Collector to emitter breakdown voltage	3011	Bias condition D; IC = 3 A dc , pulsed (see 4.5.1),	V(BR)CEO	140		V dc
Collector to emitter breakdown voltage	3011	Bias condition B; IC = 1.5 A dc, RBE = 100 Ω , see figure 2	V(BR)CER	150		V dc
Collector to emitter breakdown voltage	3011	Bias condition A; IC = 1.5 A dc, VEB = 1.5 V dc, see figure 2	V(BR)CEX	160		V dc
Collector to emitter cutoff current	3041	Bias condition A; VCE = 140 V dc, VEB = 1.5 V dc	ICEX		1.0	mA dc
Emitter to base cutoff current	3061	Bias condition D;VEB = 7 V dc	IEBO		1.0	mA dc
Base emitter voltage (nonsaturated)	3066	Test condition B; pulsed (see 4.5.1) IC = 3 A dc, VCE = 4.0 V dc	VBE		1.7	V dc
Saturation voltage and resistance	3071	Pulsed (see 4.5.1) IC = 3 A dc, IB = 300 mA dc	VCE(sat)		1.0	V dc
Forward-current transfer ratio	3076	VCE = 4 V dc, IC = 3 A dc, pulsed (see 4.5.1)	hFE1	20	70	mA

See footnote at end of table.

TABLE I. Group A inspection – Continued. $\underline{1}/$

	MIL-STD-750	Symbol	Lir	mits	Unit
Method	Conditions		Min	Max	
	T _A = +150°C				
3036	Bias condition D; VCB = 140 V dc	ICBO		10	mA dc
	T _A = -55°C				
3076	VCE = 4 V dc, IC = 3 A dc, pulsed (see 4.5.1)	hFE2	15		
3306	VCE = 4 V dc, IC = 3 A dc, f = 100 kHz	h _{fe}	1.0		
3051	T _C = +25°C; t = 1 s				
	V _{CE} = 11.7 V dc, I _C = 10 A dc				
	VCE = 78 V dc, IC = 1.5 A dc				
	VCE = 140 V dc, IC = 0.5 A dc, see figure 3				
	See table I, subgroup 2 herein				
	3036 3076 3306	Method Conditions TA = +150°C 3036 Bias condition D; VCB = 140 V dc TA = -55°C 3076 VCE = 4 V dc, IC = 3 A dc, pulsed (see 4.5.1) 3306 VCE = 4 V dc, IC = 3 A dc, f = 100 kHz 3051 TC = +25°C; t = 1 s VCE = 11.7 V dc, IC = 10 A dc VCE = 78 V dc, IC = 1.5 A dc VCE = 140 V dc, IC = 1.5 A dc, see figure 3	Method Conditions TA = +150°C ICBO 3036 Bias condition D; VCB = 140 V dc TA = -55°C TA = -55°C 3076 VCE = 4 V dc, IC = 3 A dc, pulsed (see 4.5.1) Additional control contr	Method Conditions Min TA = +150°C ICBO 3036 Bias condition D; VCB = 140 V dc ICBO TA = -55°C hFE2 15 3076 VCE = 4 V dc, IC = 3 A dc, pulsed (see 4.5.1) hFE2 15 3306 VCE = 4 V dc, IC = 3 A dc, f = 100 kHz hfe 1.0 3051 TC = +25°C; t = 1 s VCE = 11.7 V dc, IC = 10 A dc VCE = 78 V dc, IC = 1.5 A dc VCE = 140 V dc, IC = 1.5 A dc VCE = 140 V dc, IC = 0.5 A dc, see figure 3	Method Conditions Min Max TA = +150°C 3036 Bias condition D; VCB = 140 V dc ICBO 10 TA = -55°C hFE2 15 3076 VCE = 4 V dc, IC = 3 A dc, pulsed (see 4.5.1) hFE2 15 3306 VCE = 4 V dc, IC = 3 A dc, f = 100 kHz hfe 1.0 3051 TC = +25°C; t = 1 s VCE = 11.7 V dc, IC = 10 A dc VCE = 78 V dc, IC = 1.5 A dc VCE = 140 V dc, IC = 0.5 A dc, see figure 3 VCE = 140 V dc, IC = 0.5 A dc, see figure 3 VCE = 140 V dc, IC = 0.5 A dc, see figure 3

^{1/} For sampling plan, see MIL-PRF-19500.

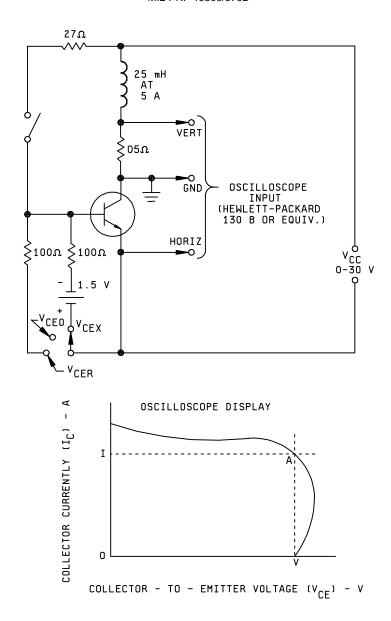
TABLE II. Groups A, B, and C delta measurements. 1/2/

Step	Inspection	MIL-STD-750		Symbol	Lim	its	Unit
		Method	Conditions		Min	Max	
1.	Collector to emitter cutoff current	3041	Bias condition A; VCE = 140 V dc	ΔICEX <u>3</u> /	100 percent or 500 μA c		ŭ
			V _{EB} = 1.5 V dc		greater.		

- 1/ The delta measurements for table VIb (JAN, JANTX and JANTXV) of MIL-PRF-19500 are as follows:
 - a. Subgroup 3, see table II herein, step 1.
 - b. Subgroup 6, see table II herein, step 1.
- 2/ The delta measurements for table VII of MIL-PRF-19500 are as follows:
 - a. Subgroup 6, see table II herein, step 1.
- 3/ Devices which exceed the group A limits for this test shall not be accepted.

5. PACKAGING

5.1 <u>Packaging</u>. For acquisition purposes, the packaging requirements shall be as specified in the contract or order (see 6.2). When actual packaging of material is to be performed by DoD personnel, these personnel need to contact the responsible packaging activity to ascertain requisite packaging requirements. Packaging requirements are maintained by the Inventory Control Points' packaging activity within the Military Department or Defense Agency, or within the Military Departments' System Command. Packaging data retrieval is available from the managing Military Department's or Defense Agency's automated packaging files, CD-ROM products, or by contacting the responsible packaging activity.



NOTE: The voltages VCEO, VCER, or VCEX are acceptable when the trace falls to the right and above point "A".

FIGURE 2. Test circuit for $V_{\mbox{CEO}}, V_{\mbox{CER}},$ and $V_{\mbox{CEX}}.$

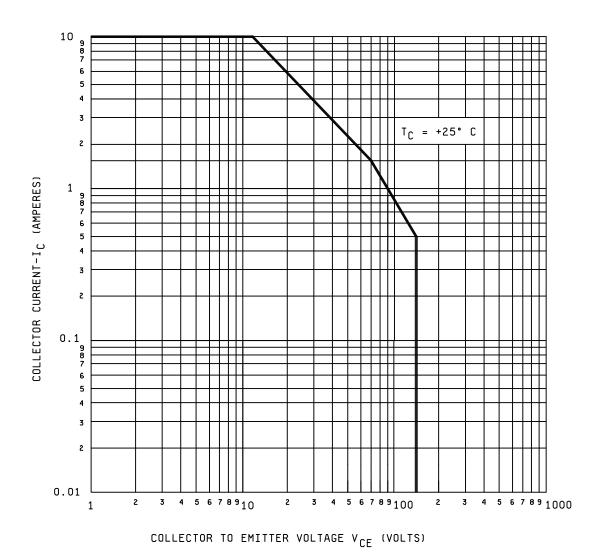


FIGURE 3. Maximum safe operating graph (dc).

6. NOTES

(This section contains information of a general or explanatory nature that may be helpful, but is not mandatory.)

- 6.1 Notes. The notes specified in MIL-PRF-19500 are applicable to this specification.
- 6.2 Acquisition requirements. Acquisition documents should specify the following:
 - a. Issue of DODISS to be cited in the solicitation and, if required, the specific issue of individual documents referenced (see 2.2.1).
 - b. Lead finish (see 3.3.1).
 - c. Type designation and product assurance level.
 - d. Packaging requirements (see 5.1).
- 6.3 <u>Changes from previous issue</u>. Marginal notations are not used in this revision to identify changes with respect to the previous issue due to the extent of the changes.
- 6.4 Qualification. With respect to products requiring qualification, awards will be made only for products which are, at the time of award of contract, qualified for inclusion in Qualified Manufacturers List QML-19500 whether or not such products have actually been so listed by that date. The attention of the contractors is called to these requirements, and manufacturers are urged to arrange to have the products that they propose to offer to the Federal Government tested for qualification in order that they may be eligible to be awarded contracts or purchase orders for the products covered by this specification. Information pertaining to qualification of products may be obtained from Defense Supply Center Columbus, DSCC-VQE, Columbus, OH 43216.

Custodians:

Army - CR

Navy - EC

Air Force - 11

DLA - CC

Preparing activity:

DLA - CC

(Project 5961-2164)

Review activities:

Army - AR, MI

Navy - AS, CG, MC

Air Force - 19, 99

	IPROVEMENT PROPOSAI	

INSTRUCTIONS

- 1. The preparing activity must complete blocks 1, 2, 3, and 8. In block 1, both the document number and revision letter should be given.
- 2. The submitter of this form must complete blocks 4, 5, 6, and 7, and send to preparing activity.
- 3. The preparing activity must provide a reply within 30 days from receipt of the form.

NOTE: This form may not be used to request copies of documents, nor to request waivers, or clarification of requirements on current contracts. Comments submitted on this form do not constitute or imply authorization to waive any portion of the referenced document(s) or to amend contractual requirements.

requirements.				
I RECOMMEND A CHANGE:	DOCUMENT NUMBER	2. DOCUMENT DATE (YYYYMMDD)		
	MIL-PRF-19500/370D			
3. DOCUMENT TITLE				
SEMICONDUCTOR DEVICE, TRANSISTOR, N	IPN, SILICON, HIGH POWER, TYPE 2N344	2, JAN, JANTX AND JANTXV		
4. NATURE OF CHANGE (Identify paragraph r				
5. REASON FOR RECOMMENDATION				
6. SUBMITTER				
a. NAME (Last, First Middle Initial)	b. ORGANIZATION			
c. ADDRESS (Include Zip Code)	d. TELEPHONE (Inc. (1) Commercial (2) DSN (If applicable)	lude Area Code) 7. DATE SUBMITTED (YYYYMMDD)		
8. PREPARING ACTIVITY				
a. NAME Alan Barone	b. TELEPHONE (Inc. (1) Commercial 614-692-0510	lude Area Code (2) DSN 850-0510		
c. ADDRESS (Include Zip Code) DSCC-VAC 3990 east Broad Street Columbus, Ohio 43216-5000	Defense Standardiz 8725 John J. Kingm Fort Belvoir, Virginia	IF YOU DO NOT RECEIVE A REPLY WITHIN 45 DAYS, CONTACT: Defense Standardization Program Office (DLSC-LM) 8725 John J. Kingman Road, Suite 2533 Fort Belvoir, Virginia 22060-6221 Telephone (703)767-6888 DSN 427-6888		